

OS Awareness Manual

RTX-ARM

MANUAL

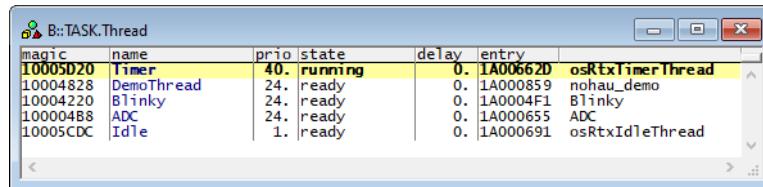
TRACE32 Online Help

TRACE32 Directory

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Overview



magic	name	prio	state	delay	entry	thread
10005D20	Timer	40	running	0.	1A00662D	osRtxTimerThread
10004828	DemoThread	24	ready	0.	1A000859	nohau_demo
10004220	Blinky	24	ready	0.	1A0004F1	Blinky
100004B8	ADC	24	ready	0.	1A000655	ADC
10005CDC	Idle	1	ready	0.	1A000691	osRtxIdleThread

The OS Awareness for RTX-ARM contains special extensions to the TRACE32 Debugger. This manual describes the additional features, such as additional commands and statistic evaluations.

Brief Overview of Documents for New Users

Architecture-independent information:

- [“Training Basic Debugging”](#) (training_debugger.pdf): Get familiar with the basic features of a TRACE32 debugger.
- [“T32Start”](#) (app_t32start.pdf): T32Start assists you in starting TRACE32 PowerView instances for different configurations of the debugger. T32Start is only available for Windows.
- [“General Commands”](#) (general_ref_<x>.pdf): Alphabetic list of debug commands.

Architecture-specific information:

- [“Processor Architecture Manuals”](#): These manuals describe commands that are specific for the processor architecture supported by your Debug Cable. To access the manual for your processor architecture, proceed as follows:
 - Choose **Help** menu > **Processor Architecture Manual**.
- [“OS Awareness Manuals”](#) (rtos_<os>.pdf): TRACE32 PowerView can be extended for operating system-aware debugging. The appropriate OS Awareness manual informs you how to enable the OS-aware debugging.

Supported Versions

Currently RTX-ARM is supported for the following versions:

- RTX-ARM V3.x, V4.x and V5.x on all ARM derivatives
- mbed OS 5.x with RTX V4
- mbed OS 5.x with RTX V5

The **TASK.CONFIG** command loads an extension definition file called “rtx.t32” (directory “`~/demo/<processor>/kernel/rtxarm/<version(v4/v5)>`”). It contains all necessary extensions.

Automatic configuration tries to locate the RTX-ARM internals automatically. For this purpose all symbol tables must be loaded and accessible at any time the OS Awareness is used.

If you want to display the OS objects “On The Fly” while the target is running, you need to have access to memory while the target is running. In case of ICD, you have to enable **SYStem.MemAccess** or **SYStem.CpuAccess** (CPU dependent).

For system resource display and trace functionality, you can do an automatic configuration of the OS Awareness. For this purpose it is necessary that all system internal symbols are loaded and accessible at any time, the OS Awareness is used. Each of the **TASK.CONFIG** arguments can be substituted by '0', which means that this argument will be searched and configured automatically. For a fully automatic configuration omit all arguments:

TASK.CONFIG rtx

See also “[Hooks & Internals](#)” for details on the used symbols.

To get a quick access to the features of the OS Awareness for RTX-ARM with your application, follow this roadmap:

1. Copy the files “rtx.t32” and “rtx.men” to your project directory from TRACE32 directory:
For RTX-ARM V3.x and V4.x: “`~/demo/<processor>/kernel/rtxarm/v4`”.
For RTX-ARM V5.x: “`~/demo/<processor>/kernel/rtxarm/v5`”.
2. Start the TRACE32 Debugger.
3. Load your application as normal.
4. Execute the command:

```
TASK.CONFIG rtx
```

See “[Configuration](#)”.

5. Execute the command:

```
MENU.ReProgram rtx
```

See “[RTX-ARM Specific Menu](#)”.

6. Start your application.

Now you can access the RTX-ARM extensions through the menu.

In case of any problems, please carefully read the previous Configuration chapter.

Hooks & Internals in RTX-ARM

No hooks are used in the kernel.

For retrieving the kernel data and structures, the OS Awareness uses the global kernel symbols and structure definitions. Ensure that access to those structures is possible every time when features of the OS Awareness are used.

Be sure that your application is compiled and linked with debugging symbols switched on.

RTX ARM V3.x and V4.x: For detecting the current running task, the kernel symbol “`os_runtask`” or “`os_tsk`” is used.

For stack coverage, set the RTX configuration value `OS_STKINIT` (available since RTX v4.78). This initializes the stack with a predefined pattern to detect used stack space.

RTX ARM V5.x: For detecting the current running task, the kernel symbol “`osRtxInfo.thread.run.curr`” is used.

The OS Awareness for RTX-ARM supports the following features.

Display of Kernel Resources

The extension defines new commands to display various kernel resources. Information on the following RTX-ARM components can be displayed:

TASK.Task

Tasks

For a description of the commands, refer to chapter “[RTX-ARM Commands](#)”.

If your hardware allows memory access while the target is running, these resources can be displayed “On The Fly”, i.e. while the application is running, without any intrusion to the application.

Without this capability, the information will only be displayed if the target application is stopped.

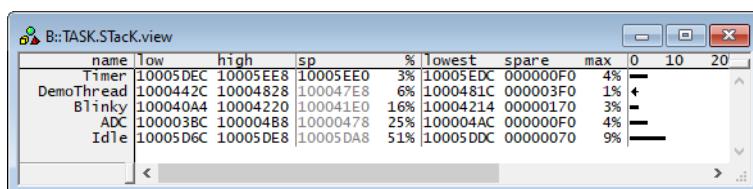
Task Stack Coverage

For stack usage coverage of tasks, you can use the **TASK.STack** command. Without any parameter, this command will open a window displaying with all active tasks. If you specify only a task magic number as parameter, the stack area of this task will be automatically calculated.

To use the calculation of the maximum stack usage, a stack pattern must be defined with the command **TASK.STack.Pattern** (default value is zero).

To add/remove one task to/from the task stack coverage, you can either call the **TASK.STack.Add** or **TASK.STack.Remove** commands with the task magic number as the parameter, or omit the parameter and select the task from the **TASK.STack.*** window.

It is recommended to display only the tasks you are interested in because the evaluation of the used stack space is very time consuming and slows down the debugger display.



The screenshot shows a Windows-style application window titled "B::TASK.STack.view". The window contains a table with the following data:

	name	low	high	sp	%	lowest	spare	max	0	10	20
	Timer	10005DEC	10005EE8	10005EE0	3%	10005EDC	000000F0	4%	—	—	—
	DemoThread	1000442C	10004828	100047E8	6%	1000481C	000003F0	1%	+	—	—
	Blinky	100040A4	10004220	100041E0	16%	10004214	00000170	3%	—	—	—
	ADC	100003BC	100004B8	10000478	25%	100004AC	000000F0	4%	—	—	—
	Idle	10005D6C	10005DE8	10005DA8	51%	10005DDC	00000070	9%	—	—	—

Since RTX v4.78, if the configuration value OS_STKINIT is set, RTX initializes the stack with a predefined stack pattern. The default pattern is 0xCC. Declare this pattern to the debugger with:

```
TASK.STACK.PATTERN 0xCC
```

RTX ARM V5.x: RTX initializes the stack with a predefined stack pattern. The default pattern is 0x00.

Task-Related Breakpoints

Any breakpoint set in the debugger can be restricted to fire only if a specific task hits that breakpoint. This is especially useful when debugging code which is shared between several tasks. To set a task-related breakpoint, use the command:

Break.Set <address>|<range> [/<option>] /TASK <task> Set task-related breakpoint.

- Use a magic number, task ID, or task name for <task>. For information about the parameters, see [“What to know about the Task Parameters”](#) (general_ref_t.pdf).
- For a general description of the **Break.Set** command, please see its documentation.

By default, the task-related breakpoint will be implemented by a conditional breakpoint inside the debugger. This means that the target will *always* halt at that breakpoint, but the debugger immediately resumes execution if the current running task is not equal to the specified task.

NOTE: Task-related breakpoints impact the real-time behavior of the application.

On some architectures, however, it is possible to set a task-related breakpoint with *on-chip* debug logic that is less intrusive. To do this, include the option **/Onchip** in the **Break.Set** command. The debugger then uses the on-chip resources to reduce the number of breaks to the minimum by pre-filtering the tasks.

For example, on ARM architectures: *If* the RTOS serves the Context ID register at task switches, and *if* the debug logic provides the Context ID comparison, you may use Context ID register for less intrusive task-related breakpoints:

Break.CONFIG.UseContextID ON	Enables the comparison to the whole Context ID register.
Break.CONFIG.MatchASID ON	Enables the comparison to the ASID part only.
TASK.List.tasks	If TASK.List.tasks provides a trace ID (traceid column), the debugger will use this ID for comparison. Without the trace ID, it uses the magic number (magic column) for comparison.

When single stepping, the debugger halts at the next instruction, regardless of which task hits this breakpoint. When debugging shared code, stepping over an OS function may cause a task switch and coming back to the same place - but with a different task. If you want to restrict debugging to the current task,

you can set up the debugger with **SETUP.StepWithinTask ON** to use task-related breakpoints for single stepping. In this case, single stepping will always stay within the current task. Other tasks using the same code will not be halted on these breakpoints.

If you want to halt program execution as soon as a specific task is scheduled to run by the OS, you can use the **Break.SetTask** command.

Dynamic Task Performance Measurement

The debugger can execute a dynamic performance measurement by evaluating the current running task in changing time intervals. Start the measurement with the commands **PERF.Mode TASK** and **PERF.Arm**, and view the contents with **PERF.ListTASK**. The evaluation is done by reading the 'magic' location (= current running task) in memory. This memory read may be non-intrusive or intrusive, depending on the **PERF.METHOD** used.

If **PERF** collects the PC for function profiling of processes in MMU-based operating systems (**SYStem.Option.MMUSPACES ON**), then you need to set **PERF.MMUSPACES**, too.

For a general description of the **PERF** command group, refer to "**General Commands Reference Guide P**" (general_ref_p.pdf).

Task Runtime Statistics

NOTE:	This feature is <i>only</i> available, if your debug environment is able to trace task switches (program flow trace is not sufficient). It requires either an on-chip trace logic that is able to generate task information (eg. data trace), or a software instrumentation feeding one of TRACE32 software based traces (e.g. FDX or Logger). For details, refer to " OS-aware Tracing " (glossary.pdf).
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Based on the recordings made by the **Trace** (if available), the debugger is able to evaluate the time spent in a task and display it statistically and graphically.

To evaluate the contents of the trace buffer, use these commands:

Trace.List List.TASK DEFault

Display trace buffer and task switches

Trace.STATistic.TASK

Display task runtime statistic evaluation

Trace.Chart.TASK	Display task runtime timechart
Trace.PROfileSTATistic.TASK	Display task runtime within fixed time intervals statistically
Trace.PROfileChart.TASK	Display task runtime within fixed time intervals as colored graph
Trace.FindAll Address TASK.CONFIG(magic)	Display all data access records to the “magic” location
Trace.FindAll CYcle owner OR CYcle context	Display all context ID records

The start of the recording time, when the calculation doesn't know which task is running, is calculated as “(unknown)”.

Task State Analysis

NOTE: This feature is *only* available, if your debug environment is able to trace task switches and data accesses (program flow trace is not sufficient). It requires either an on-chip trace logic that is able to generate a data trace, or a software instrumentation feeding one of TRACE32 software based traces (e.g. [FDX](#) or [Logger](#)). For details, refer to “[OS-aware Tracing](#)” (glossary.pdf).

The time different tasks are in a certain state (running, ready, suspended or waiting) can be evaluated statistically or displayed graphically.

This feature requires that the following data accesses are recorded:

- All accesses to the status words of all tasks
- Accesses to the current task variable (= magic address)

Adjust your trace logic to record all data write accesses, or limit the recorded data to the area where all TCBs are located (plus the current task pointer).

Example: This script assumes that the TCBs are located in an array named TCB_array and consequently limits the tracing to data write accesses on the TCBs and the task switch.

```
Break.Set Var.RANGE(TCB_array) /Write /TraceData
Break.Set TASK.CONFIG(magic) /Write /TraceData
```

To evaluate the contents of the trace buffer, use these commands:

Trace.STATistic.TASKState	Display task state statistic
Trace.Chart.TASKState	Display task state timechart

The start of the recording time, when the calculation doesn't know which task is running, is calculated as "(unknown)".

All kernel activities up to the task switch are added to the calling task.

Function Runtime Statistics

NOTE:

This feature is *only* available, if your debug environment is able to trace task switches (program flow trace is not sufficient). It requires either an on-chip trace logic that is able to generate task information (e.g. data trace), or a software instrumentation feeding one of TRACE32 software based traces (e.g. [FDX](#) or [Logger](#)). For details, refer to "[OS-aware Tracing](#)" (glossary.pdf).

All function-related statistic and time chart evaluations can be used with task-specific information. The function timings will be calculated dependent on the task that called this function. To do this, in addition to the function entries and exits, the task switches must be recorded.

To do a selective recording on task-related function runtimes based on the data accesses, use the following command:

```
; Enable flow trace and accesses to the magic location  
Break.Set TASK.CONFIG(magic) /TraceData
```

To do a selective recording on task-related function runtimes, based on the Arm Context ID, use the following command:

```
; Enable flow trace with Arm Context ID (e.g. 32bit)  
ETM.ContextID 32
```

To evaluate the contents of the trace buffer, use these commands:

Trace.ListNesting	Display function nesting
Trace.STATistic.Func	Display function runtime statistic
Trace.STATistic.TREE	Display functions as call tree
Trace.STATistic.sYmbol /SplitTASK	Display flat runtime analysis
Trace.Chart.Func	Display function timechart
Trace.Chart.sYmbol /SplitTASK	Display flat runtime timechart

The start of the recording time, when the calculation doesn't know which task is running, is calculated as "(unknown)".

The menu file “rtx.men” contains a menu with RTX-ARM specific menu items. Load this menu with the **MENU.ReProgram** command.

You will find a new menu called **RTX-ARM**.

- The **Display** menu items launch the kernel resource display windows.
- The **Stack Coverage** submenu starts and resets the RTX-ARM specific stack coverage and provides an easy way to add or remove tasks from the stack coverage window.

In addition, the menu file (*.men) modifies these menus on the TRACE32 [main menu bar](#):

- The **Trace** menu is extended. In the **List** submenu, you can choose if you want a trace list window to show only task switches (if any) or task switches together with default display.
- The **Perf** menu contains additional submenus for task runtime statistics and statistics on task states.

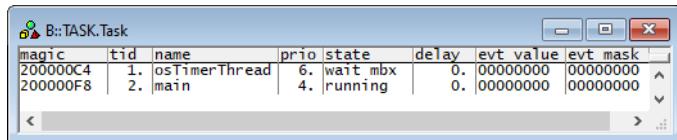
TASK.Task / TASK.Thread

Display tasks or threads

Format: **TASK.Task** ; V4
TASK.Thread ; V5

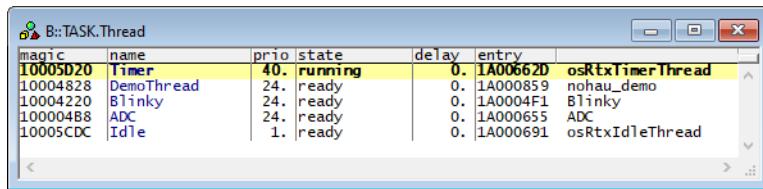
Displays the task table of RTX-ARM.

RTX ARM V3.x and V4.x



magic	tid	name	prio	state	delay	evt value	evt mask
200000C4	1.	osTimerThread	6.	wait_mbx	0.	00000000	00000000
200000F8	2.	main	4.	running	0.	00000000	00000000

RTX ARM V5.x



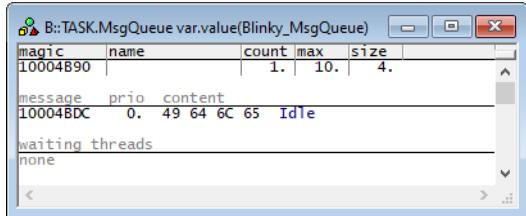
magic	name	prio	state	delay	entry	
10005D20	Timer	40.	running	0.	1A00662D osRtxTimerThread	
10004828	DemoThread	24.	ready	0.	1A000859 nohau_demo	
10004220	Blinky	24.	ready	0.	1A0004F1 Blinky	
100004B8	ADC	24.	ready	0.	1A000655 ADC	
10005CDC	Idle	1.	ready	0.	1A000691 osRtxIdleThread	

“magic” is a unique ID, used by the OS Awareness to identify a specific task (address of the TCB).

The fields “magic” and “name” are mouse sensitive, double clicking on them opens appropriate windows. Right clicking on them will show a local menu.

Format: **TASK.MsgQueue <queueid>** ; V5

Displays a message queue of RTX-ARM. This command is only available on RTX-ARM version 5.



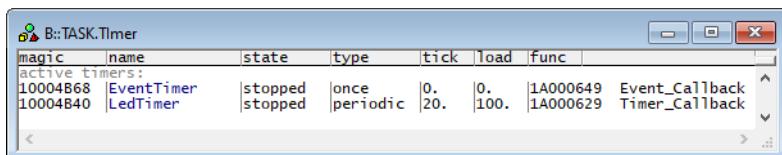
“magic” is a unique ID, used by the OS Awareness to identify a specific message queue (message queue ID).

The fields “magic” and “name” are mouse sensitive, double clicking on them opens appropriate windows. Right clicking on them will show a local menu.

Format: **TASK.Timer [<queueid>]** ; V5

Displays a timer of RTX-ARM. This command is only available on RTX-ARM version 5.

If no argument is given, the currently active timers are displayed.



“magic” is a unique ID, used by the OS Awareness to identify a specific timer (timer id).

The fields “magic” and “name” are mouse sensitive, double clicking on them opens appropriate windows. Right clicking on them will show a local menu.

There are special definitions for RTX-ARM specific PRACTICE functions.

TASK.CONFIG()

OS Awareness configuration information

Syntax: **TASK.CONFIG(magic | magicsize)**

Parameter and Description:

magic	Parameter Type: String (<i>without</i> quotation marks). Returns the magic address, which is the location that contains the currently running task (i.e. its task magic number).
magicsize	Parameter Type: String (<i>without</i> quotation marks). Returns the size of the task magic number (1, 2 or 4).

Return Value Type: Hex value.