

Training AURIX Tracing

MANUAL

Training AURIX Tracing

TRACE32 Online Help

TRACE32 Directory

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History

31-Aug-22 The MCDS command group and the [MCDS.state](#) window have been fully redesigned. Parts of this training are therefore outdated. This applies to the TriCore AURIX TC2x, TC3x and TC4x.

15-Feb-13 Initial version.

Protocol Description

The MCDS (Multi Core Debug Solution) included in the ED device allows to generate trace information.

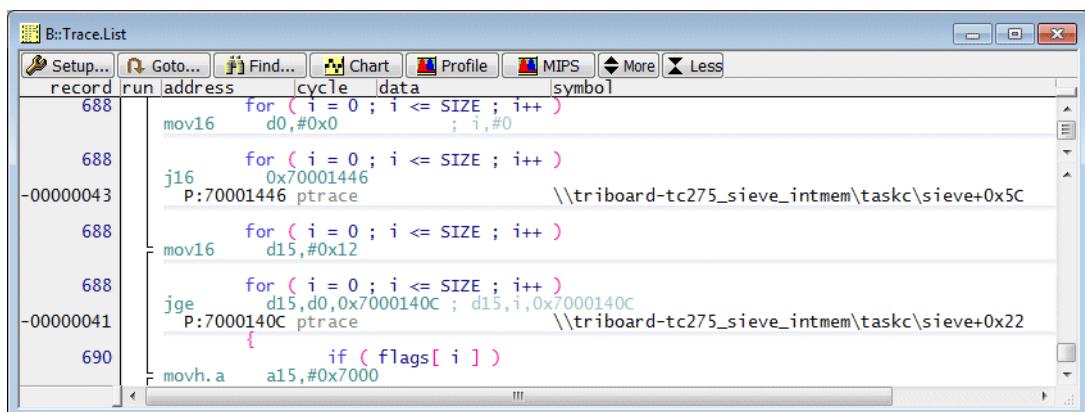
The following message types are generated:

- **Instruction Pointer Call Messages (ptrace)**

Instruction Pointer Call Messages are generated for all taken direct and indirect branches, interrupts and traps. They include:

- take-off address (branch source address)
- landing address (branch destination address)
- number of bytes executed since the last Instruction Pointer Call Message
- source-ID information

(the AURIX can generate Instruction Pointer Call Messages for up to 2 cores)



record	run	address	cycle	data	symbol
688		for (i = 0 ; i <= SIZE ; i++)			
		mov16 d0,#0x0			; i,#0
688					
-00000043		for (i = 0 ; i <= SIZE ; i++)			
		j16 0x70001446			P:70001446 ptrace \\triboard-tc275_sieve_intmem\taskc\sieve+0x5C
688					
-00000041		mov16 d15,#0x12			
688					
-00000041		for (i = 0 ; i <= SIZE ; i++)			
		jge d15,d0,0x7000140C ; d15,i,0x7000140C			P:7000140C ptrace \\triboard-tc275_sieve_intmem\taskc\sieve+0x22
690					
		{			
		if (flags[i])			
		movh.a a15,#0x7000			

Support for the **Continuous Compact Function Trace (CFT)** is currently under construction.

For some trace analysis features (e.g. nesting function run-time analysis) it might be beneficial to differentiate between taken branches and interrupts/traps.

TRACE32 reads the contents of the BIV (Interrupt Vector Table Pointer) register and regards the following address space as interrupt vector table:

- BIV[0]==0: <contents_of_biv>++(255. * 32. byte)
- BIV[0]==1: <contents_of_biv>++(255. * 16. byte)

If the landing address (branch destination address) of an Instruction Pointer Call Messages is the address of an interrupt vector, TRACE32 indicates an interrupt in the trace recording.

record	run	address	cycle	data	symbol	busmaster	ti.back
+00023580	0	jne d2,#0x0,0xA0006D36			P:A0006D36 ptrace	0.000us	
+00023583	0	= call 0xA0006A24			P:A0006A24 ptrace	0.030us	
+00023587	0	= movh.a a15,#0x9000			P:A0006A24 ptrace	0.070us	
+00023590	0	= P:A00F0040 ptrace			P:A00F0040 ptrace	0.230us	
		interrupt			\\APP\\Os_Vector__text_inttab0_intvec_002		

Example

C	-	D0	0	A0	0	SP	00000000
V	-	D1	1F	A1	0	+04	00000000
SV	-	D2	1	A2	0	+08	00000000
AV	-	D3	D000FF00	A3	F0000478	+0C	00000000
SAV	-	D4	1	A4	800000F4	+10	00000000
PRS	0	D5	0	A5	9000006C	+14	00000000
TO	2	D6	5	A6	F0062800	+18	00000000
IS	I	D7	1F	A7	AFFFCE3A	+1C	00000000
GW	G	D8	800080D1	A8	0	+20	00000000
CDE	C	D9	0	A9	0	+24	00000000
CDC	2	D10	77	A10	70014000	+28	00000000
		D11	800080D1	A11	A0006D3A	+2C	00000000
IE	I	D12	28EA	A12	F8001000	+30	00000000
CCPN	0	D13	4	A13	0	+34	00000000
PIPn		D14	7000	A14	F0060000	+38	00000000
		D15	0006276B	A15	90000000	+3C	00000000
		PSW	0B82	PC	A0006A2C	+40	00000000
		PCXI	003D03FE	ISP	0100	+44	00000000
		FCX	000D03FD	ICR	00028000	+48	00000000
		LCX	000D0000	BIV	A00F0000	+4C	00000000
				BTV	A00F6000	+50	00000000
						+54	00000000

BIV == 0xa00f0000 results in an Interrupt Vector Table within the following address range:
0xa00f0000++0x1fd

Further options for the exception decoding are described in [“Exception Decoding”](#) (mcds_user.pdf).

TRACE32 reads the contents of the BTV (Trap Vector Table Pointer) register and regards the following address space as trap vector table:

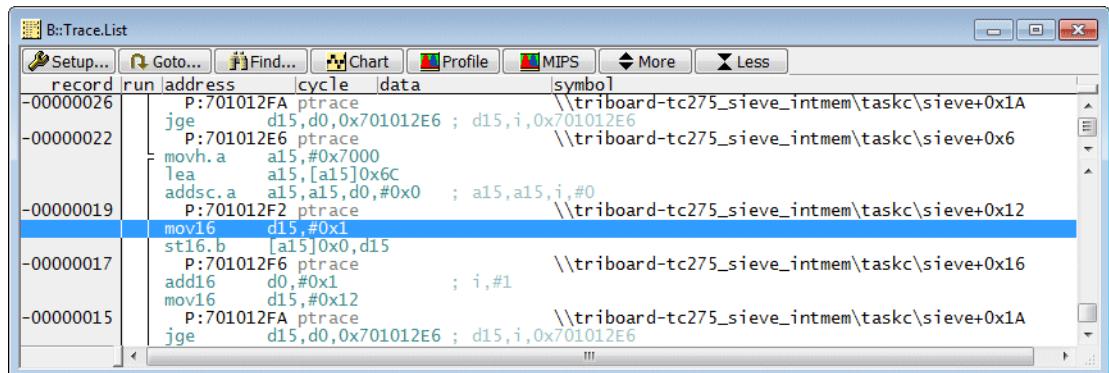
- $\langle contents_of_btv \rangle \text{++} (8. * 32. \text{Byte})$

If the landing address (branch destination address) of an Instruction Pointer Call Messages is a trap vector TRACE32 indicates a trap in the trace recording.

Further options for the trap decoding are described in “[Exception Decoding](#)” (mcds_user.pdf).

- **Instruction Pointer Messages (ptrace)**

Instruction Pointer Messages can be generated between the branches if required.



The screenshot shows the B::Trace.List window with the following assembly code:

record	run	address	cycle	data	symbol
-00000026		P:701012FA	ptrace		\\triboard-tc275_sieve_intmem\taskc\sieve+0x1A
		jge	d15,d0,0x701012E6	; d15,i,0x701012E6	
-00000022		P:701012E6	ptrace		\\triboard-tc275_sieve_intmem\taskc\sieve+0x6
		movh.a	a15,#0x7000		
		lea	a15,[a15]0x6C		
-00000019		addsc.a	a15,a15,d0,#0x0	; a15,a15,i,#0	
		P:701012F2	ptrace		\\triboard-tc275_sieve_intmem\taskc\sieve+0x12
		mov16	d15,#0x1		
-00000017		st16.b	[a15]0x0,d15		
		P:701012F6	ptrace		\\triboard-tc275_sieve_intmem\taskc\sieve+0x16
		add16	d0,#0x1	; i,#1	
-00000015		mov16	d15,#0x12		
		P:701012FA	ptrace		\\triboard-tc275_sieve_intmem\taskc\sieve+0x1A
		jge	d15,d0,0x701012E6	; d15,i,0x701012E6	

- **Write Data Trace Messages**

Write Data Trace Messages can be generated for

- core write accesses
- transactions on the bus system

They include:

- data write address
- data write value
- source-ID information

Source-ID information: Write Data Trace Messages can be generated by up to 2 cores, by the SPB (System Peripheral Bus) and by the SRI (Shared Resource Interconnect)

record	run	address	cycle	data	symbol	busmaster	ti.back
-00005528	196		jlt	d15,#0x5,0x70000D52; regvar,#5,0x70000D52	\\\triboard-tc275_sieve_intmem\taskc\func2+0x22		0.100us
	196		ld16.w	d0,[a10]0x0	mstatic1 += regvar*autovar;		
	196		ld.w	d1,0x7000030	; d1,mstatic1		
	195		mstatic1 += regvar*autovar;				
	195		madd	d0,d1,d15,d0	; d0,d1,regvar,d0		
	196		st.w	0x7000030,d0	; mstatic1,d0		
	196		for (regvar = 0; regvar < 5 ; regvar++)				
	195		add16	d15,#0x1	; regvar,#1		
	195		for (regvar = 0; regvar < 5 ; regvar++)				
-00005524			jlt	d15,#0x5,0x70000D52; regvar,#5,0x70000D52	\\\triboard-tc275_sieve_intmem\taskc\mstatic1		0.120us
-00005516	196		D:70000030	wr-data 06A6FCBA	\\\triboard-tc275_sieve_intmem\taskc\func2+0x22		0.080us
	196		P:70000052	ptrace	\\\triboard-tc275_sieve_intmem\taskc\func2+0x22		
	196		ld16.w	d0,[a10]0x0	mstatic1 += regvar*autovar;		
	196				mstatic1 += regvar*autovar;		

Write Data Trace Message generated by a core write access

Currently write cycles can not be assigned to its executing instruction. This is why **wr-data** is printed in red.

```
; BusMaster indicates the source of the bus transaction
Trace.List DEFAULT BusMaster
```

record	run	address	cycle	data	symbol	busmaster	ti.back
-*****							
T00000000		D:F883FE08	wr-sri	5000003A		CPU0 DMI	
+00000013		D:F883FD00	rd-sri	00000005		CPU0 DMI	0.420us

Write Data Trace Message generated by SRI transfer

- **Read Data Trace Messages**

Read Data Trace Messages can be generated for

- core read accesses
- transactions on the bus system

They include:

- data read address
- data read value (transactions on the bus system only)
- source-ID information

Source-ID information: Read Data Trace Messages can be generated by up to 2 cores, by the SPB (System Peripheral Bus) and by the SRI (Shared Resource Interconnect)

record	run	address	cycle	data	symbol	busmaster	ti.back
		ld.w	d15,0x70000030		; d15,mstatic1		
		extr	d15,d15,0x0,#0x8				
		{					
		auto char autovar; /* char stack variable */					
		register char regvar; /* char register variable */					
210		autovar = regvar = mstatic1;					
211		st16.b	[a10]0x0,d15		; [a10]0,regvar		
		autovar++;					
		add16	d15,#0x1		; regvar,#1		
		st16.b	[a10]0x0,d15				
213		func1c(&autovar); /* to force autovar as stack-scope */					
		lea	a4,[a10]0x0				
		call	0x70000D26		; func1c		
		D:70000030	rd-data		\\triboard-tc275_sieve_intmem\taskc\mstatic1		0.120us
+000000196							0.220us
+000000204							
		}					

Read Data Trace Message generated by a core read access (data read address only)

Currently read cycles can not be assigned to its executing instruction. This is why **rd-data** is printed in red.

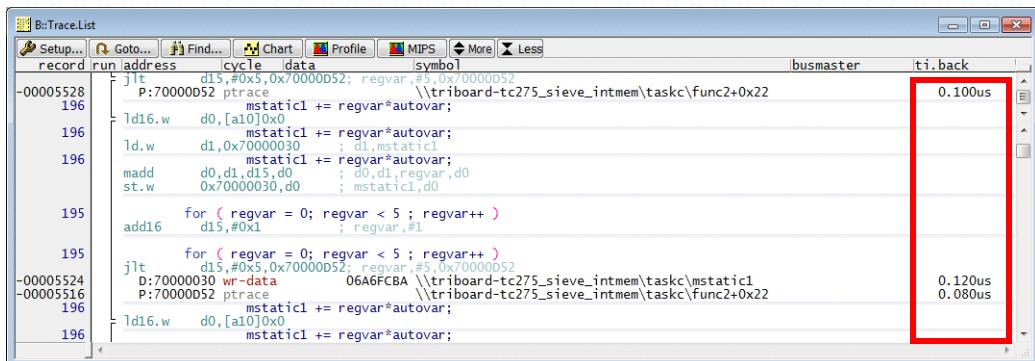
```
; BusMaster indicates the source of the bus transaction
Trace.List DEFAULT BusMaster
```

record	run	address	cycle	data	symbol	busmaster	ti.back
-*****-							
T00000000		D:F883FE08	wr-sri	5000003A		CPU0 DMI	
+00000013		D:F883FD00	rd-sri	00000005		CPU0 DMI	0.420us

Read Data Trace Message generated by SRI transfer (data read address and data read value)

- **Timestamp Messages**

The MCDS trace infrastructure generates trace messages that provide the number of MCDS clocks needed by a set of instructions. If the MCDS clock is known, TRACE32 calculates time out of this information.



Timestamp calculated out of the ticks (number of MCDS clocks) needed by a set of instructions

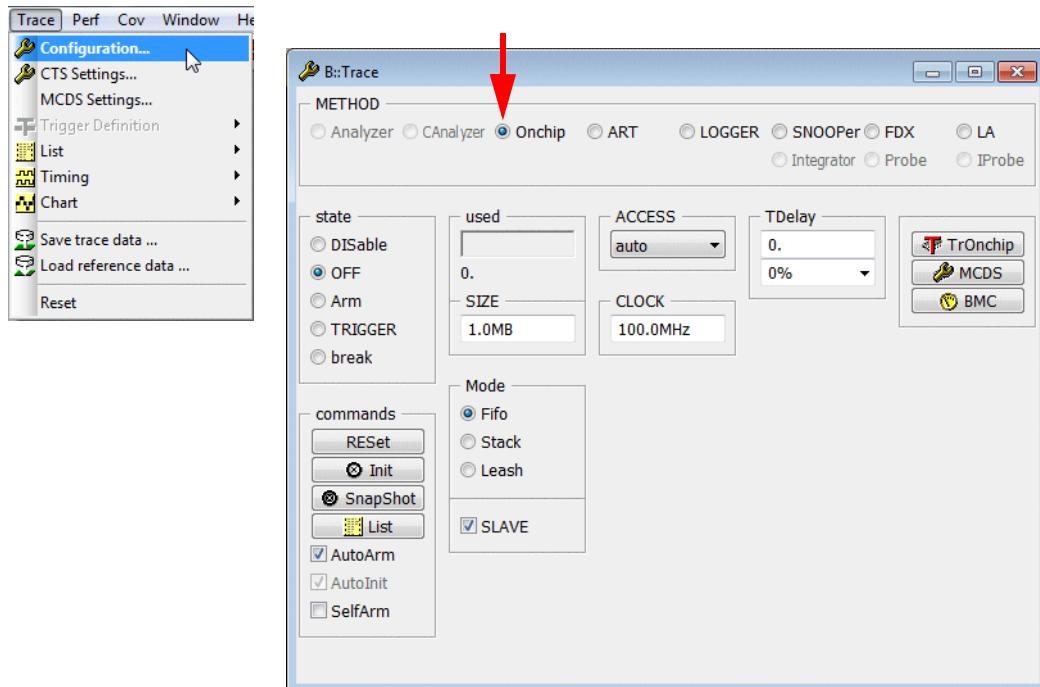
Source for the Recorded Trace Information

Onchip Trace Buffer (EMEM)

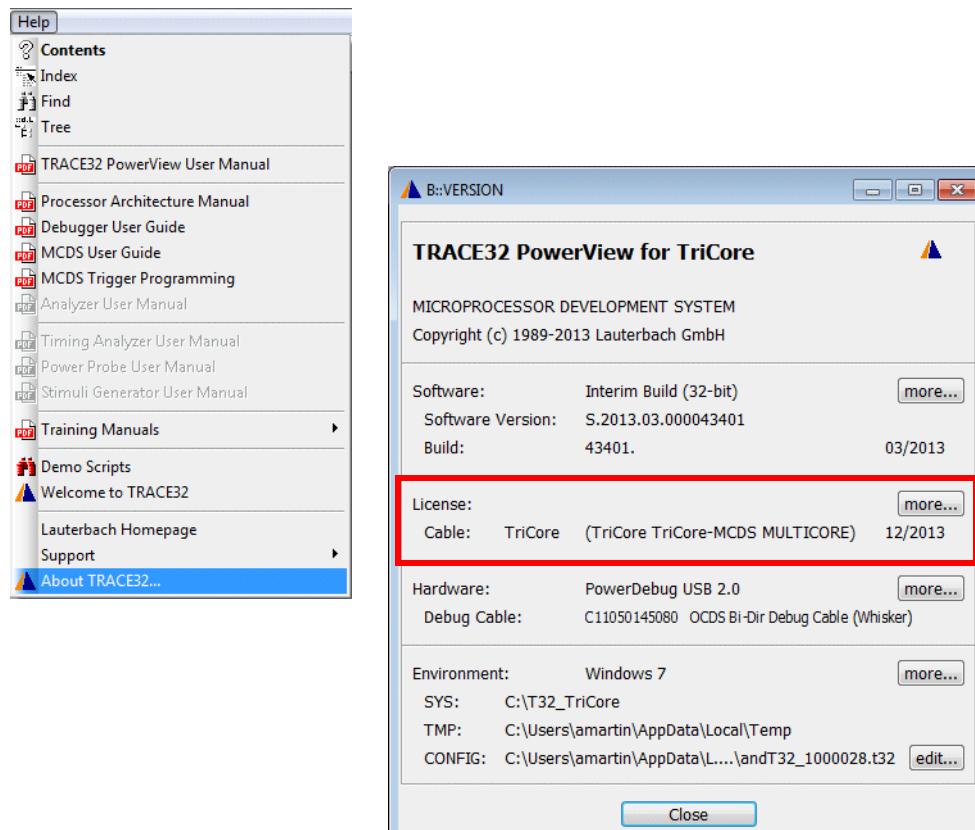
If TRACE32 is started

- when a TriCore debugger is connected
- and if the debug communication to an AURIX ED device is established

the source for the trace information is the so-called **Onchip** trace (**Trace.METHOD Onchip**).



Please be aware that tracing with the **Onchip** trace requires an **TriCore-MCDS** license in the debug cable. This can be checked as follows:

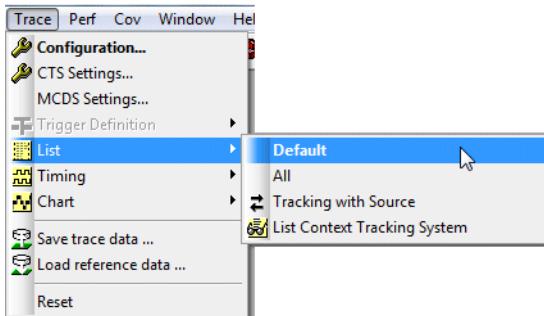


The setting **Trace.METHOD Onchip** has the following impacts:

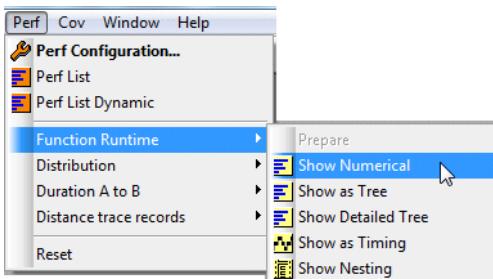
1. **Trace** is an alias for **Onchip**.

Trace.List	; Trace.List means ; Onchip.List
Trace.Mode Fifo	; Trace.Mode Fifo means ; Onchip.Mode Fifo

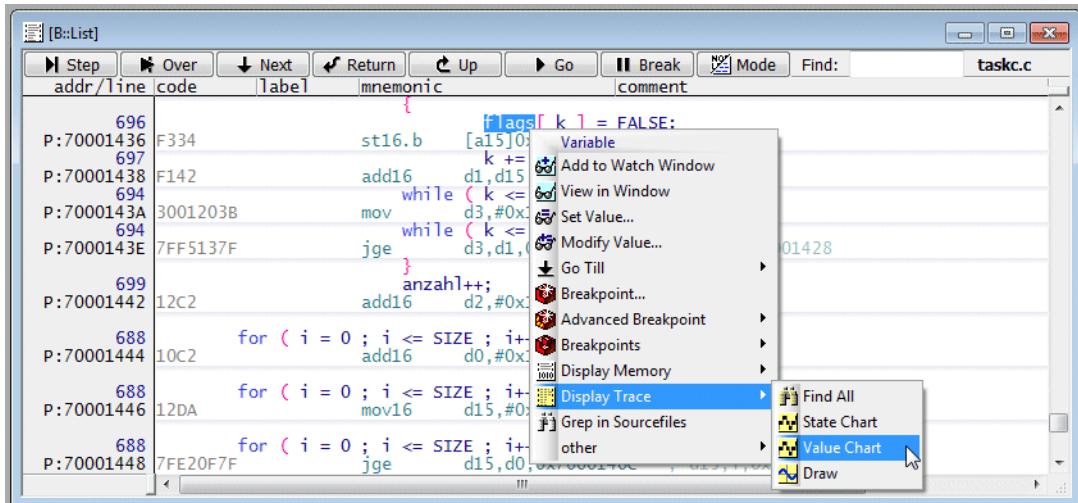
2. All commands from the **Trace** menu apply to the trace information stored in the Onchip trace buffer.



3. All Trace commands from the **Perf** menu apply to the trace information stored in the Onchip trace buffer.



4. **Display Trace** in the **Variable** pull-down applies to the trace information stored in the Onchip trace buffer.



5. TRACE32 is advised to use the trace information stored to the Onchip trace buffer as source for the trace evaluations for the following command groups:

COverage.<sub_cmd>

Trace-based code coverage

ISTAT.<sub_cmd>

Detailed instruction analysis

MIPS.<sub_cmd>

MIPS analysis

BMC.<sub_cmd>

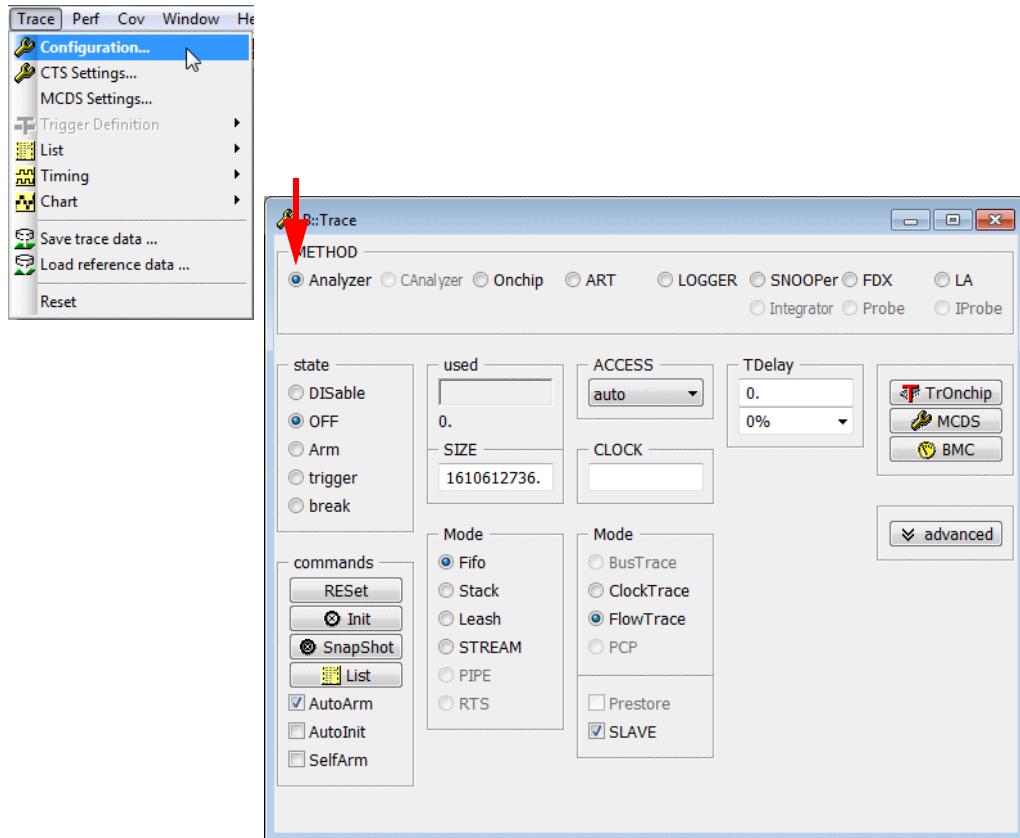
Synthesize instruction flow with recorded benchmark counter information

Trace Buffer in TRACE32 PowerTrace

If TRACE32 is started

- when a PowerTrace hardware and a PREPROCESSOR SERIAL is connected

the source for the trace information is the so-called **Analyzer** (Trace.METHOD Analyzer).

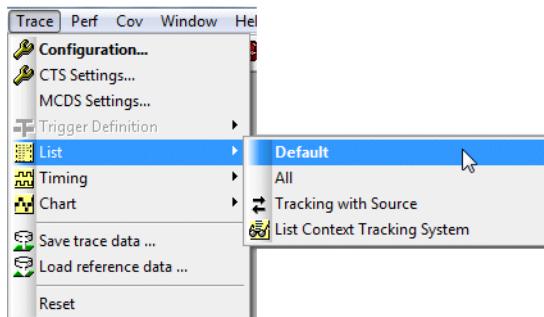


The setting **Trace.METHOD Analyzer** has the following impacts:

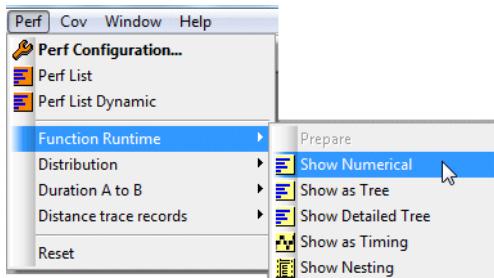
1. **Trace** is an alias for **Analyzer**.

Trace.List	;	Trace.List means	
		;	Analyzer.List
Trace.Mode Fifo	;	Trace.Mode Fifo means	
		;	Analyzer.Mode Fifo

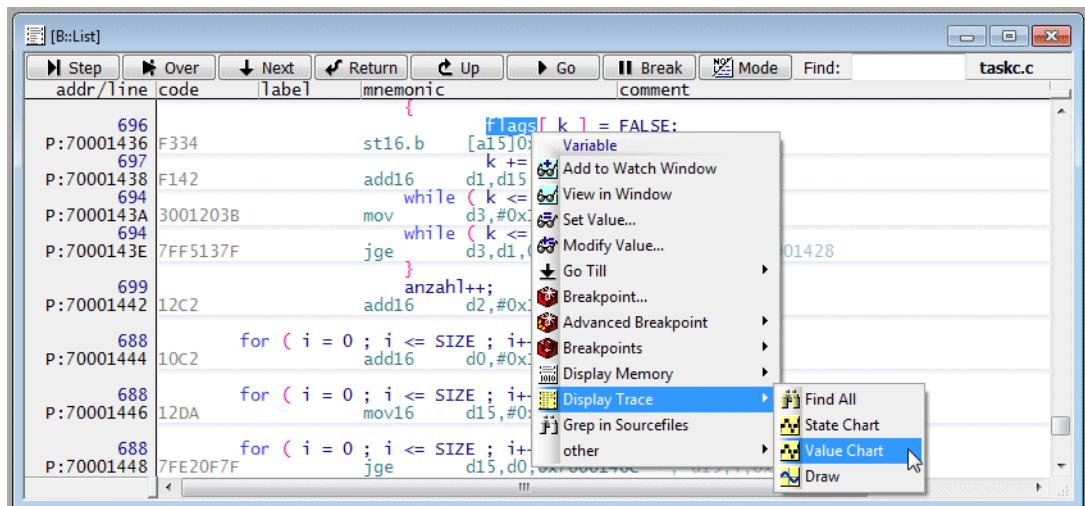
2. All commands from the **Trace** menu apply to the trace information stored in the trace memory of the PowerTrace hardware (Analyzer).



3. All **Trace** commands from the **Perf** menu apply to the trace information stored in the trace memory of the PowerTrace hardware (Analyzer).



4. **Display Trace** in the **Variable** pull-down applies to the trace information stored in the trace memory of the PowerTrace hardware (Analyzer).



5. TRACE32 is advised to use the trace information recorded to the trace memory of the PowerTrace hardware (Analyzer) as source for the trace evaluations for the following command groups:

COVerage.<sub_cmd>	Trace-based code coverage
ISTAT.<sub_cmd>	Detailed instruction analysis
MIPS.<sub_cmd>	MIPS analysis
BMC.<sub_cmd>	Synthesize instruction flow with recorded benchmark counter information

Onchip Trace Configuration

Steps

Using the onchip trace might require the following setup:

1. Specify the size of the onchip trace buffer.
2. Enable Timestamp Messages (chip timestamp).

1. Specify the size of the onchip trace buffer

TRACE32 will use the complete available emulation memory (EMEM) provided by the ED device as onchip trace buffer if not specified otherwise.

If you want to use part of the emulation memory for other purposes e.g. calibration this has to be configured before the communication between the debugger and the core(s) is established by **SYStem.Up**.

The following commands are available to specify the size of the onchip trace buffer.

MCDS.TraceBuffer SIZE <size>

Specify the trace buffer <size>.

MCDS.TraceBuffer UpperGAP <size>

Specify <size> of upper gap in EMEM that can not be used as onchip trace buffer.

MCDS.TraceBuffer LowerGAP <size>

Specify <size> of lower gap in EMEM that can not be used as onchip trace buffer.

If a third-party tool or the application has already allocated its part of the EMEM, the following command can detect which part of the EMEM can be used as trace buffer. Please be aware that the result of **MCDS.TraceBuffer DETECT** always requires a sanity check.

MCDS.TraceBuffer DETECT

Auto-detect EMEM configuration

The following 5 use cases will show you how the size of the onchip trace buffer is configured.

Use case 1: First calibration memory then trace buffer

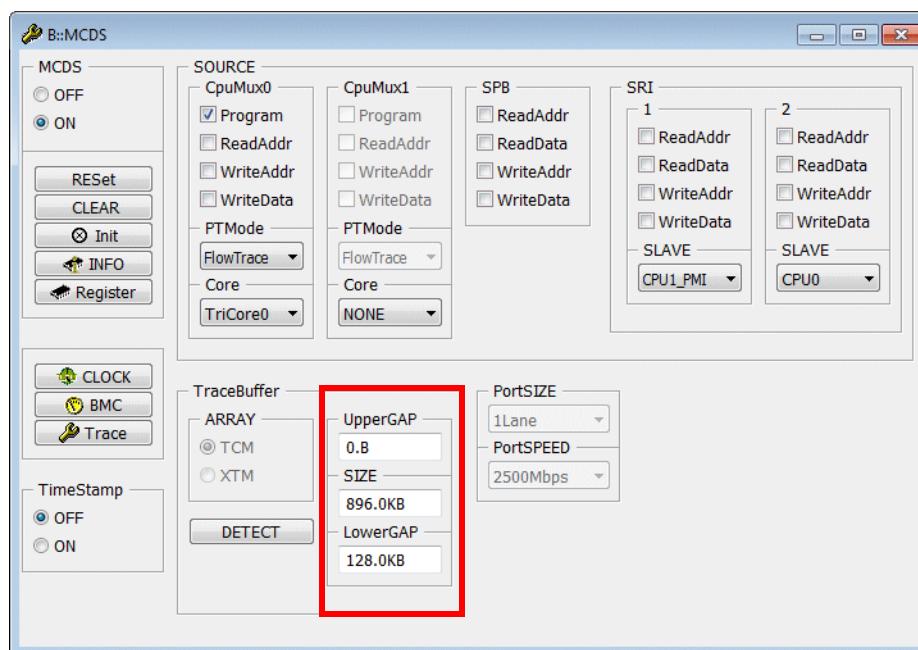
E.g. tile 0 and 1 are used by the calibration tool, the rest of the EMEM can be used as onchip trace buffer.



1024 KByte EMEM
64 KByte per tile

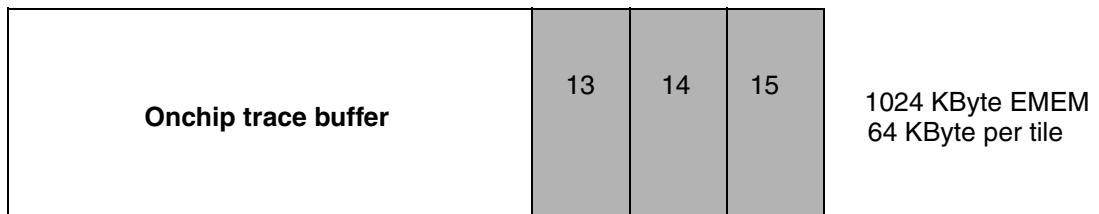
```
MCDS.TraceBuffer SIZE 896.KB ; size of trace buffer
; 1024 KByte - (2 * 64 KByte)

; the lower gap is automatically
; calculated by TRACE32
```



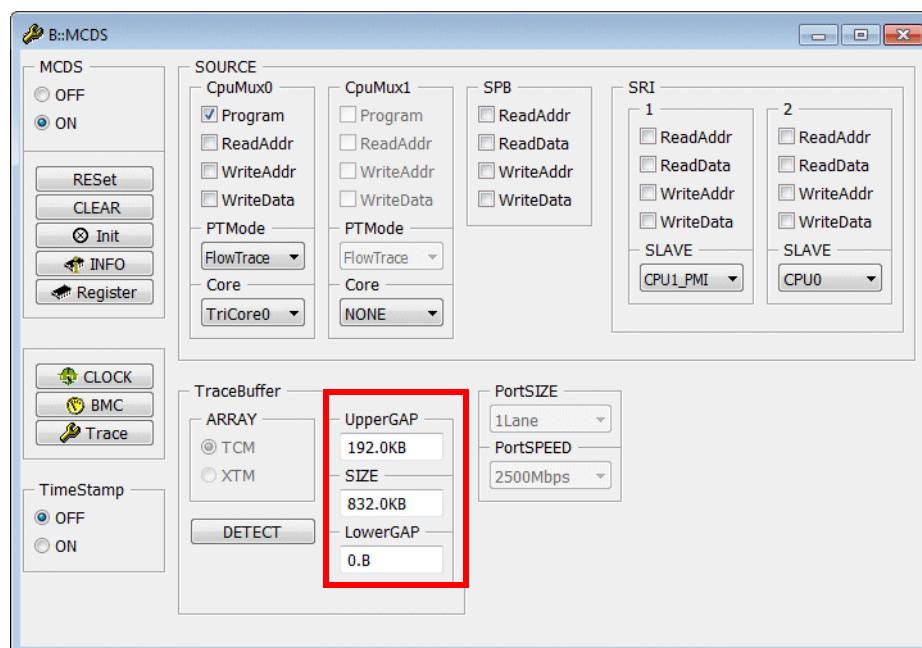
Use case 2: First trace buffer then calibration memory

E.g. tile 13 to 15 are used by the calibration tool, the rest of the EMEM can be used as onchip trace buffer.



```
MCDS.TraceBuffer UpperGAP 192.KB ; size of upper gap
; 3 * 64 KByte

; the size of the onchip trace
; buffer is automatically
; calculated by TRACE32
```



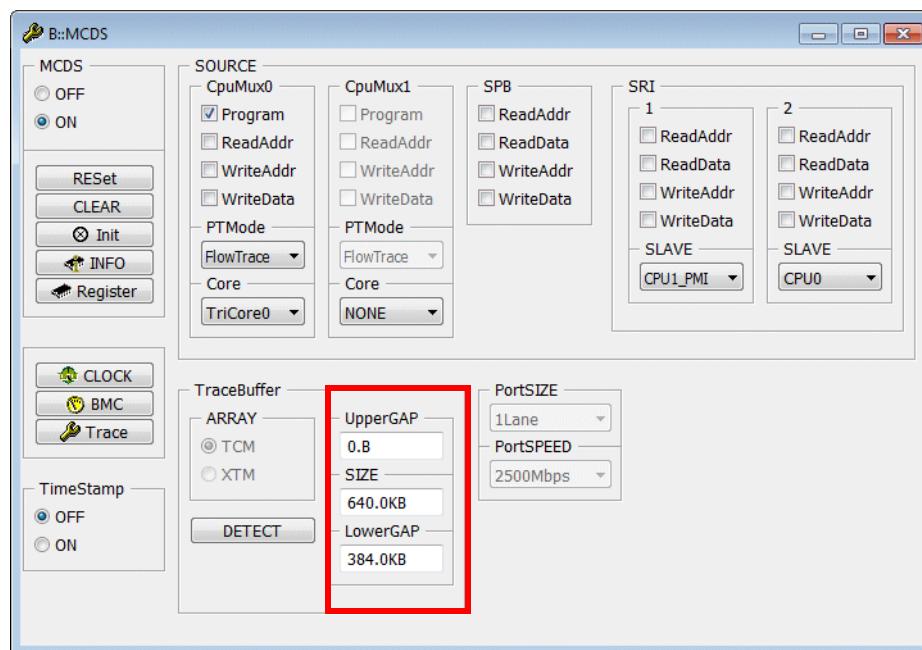
Use case 3: Calibration memory in the middle of EMEM, trace buffer after the calibration memory

E.g. tile 4 and 5 are used by the calibration tool, the rest of the EMEM can be used as onchip trace buffer. Since the trace buffer has to be mapped continuously tile 0-3 can not be used.



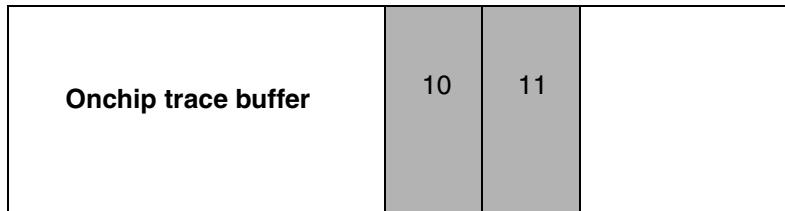
```
MCDS.TraceBuffer SIZE 640.KB ; size of trace buffer
; 1024 KByte - (6 * 64 KByte)

; the lower gap is automatically
; calculated by TRACE32
```



Use case 4: Calibration memory in the middle of EMEM, trace buffer before the calibration memory

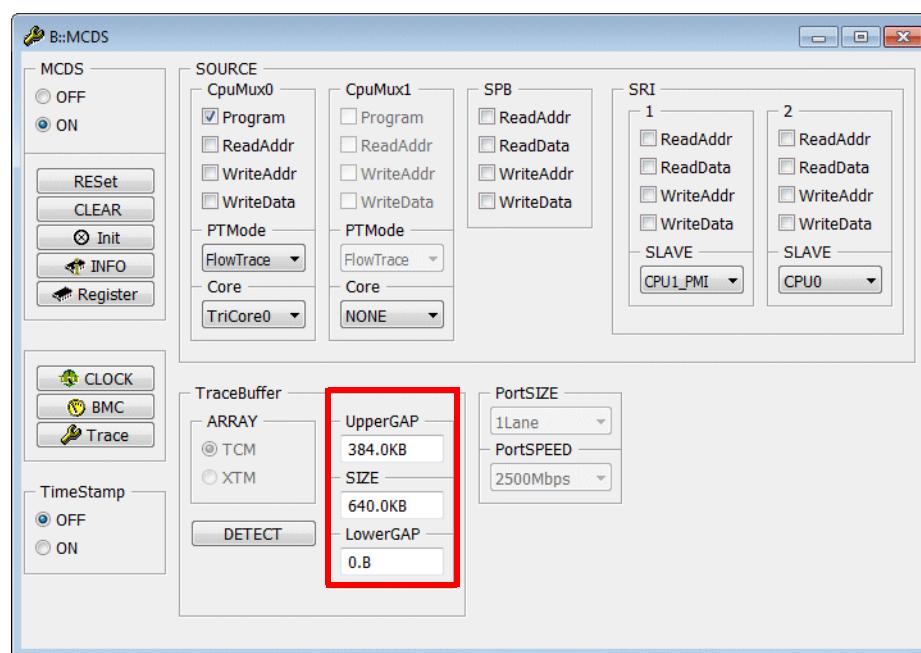
E.g. tile 10 and 11 are used by the calibration tool, the rest of the EMEM can be used as onchip trace buffer. Since the trace buffer has to be mapped continuously tile 12 to 15 can not be used.



1024 KByte EMEM
64 KByte per tile

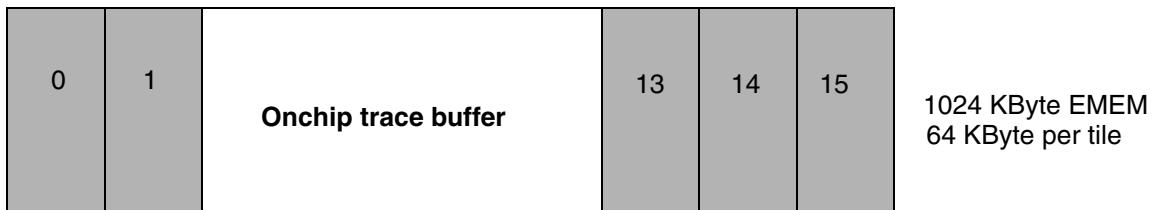
```
MCDS.TraceBuffer UpperGAP 384.KB ; size of upper gap
; 6 * 64 KByte

; the size of the onchip trace
; buffer is automatically
; calculated by TRACE32
```



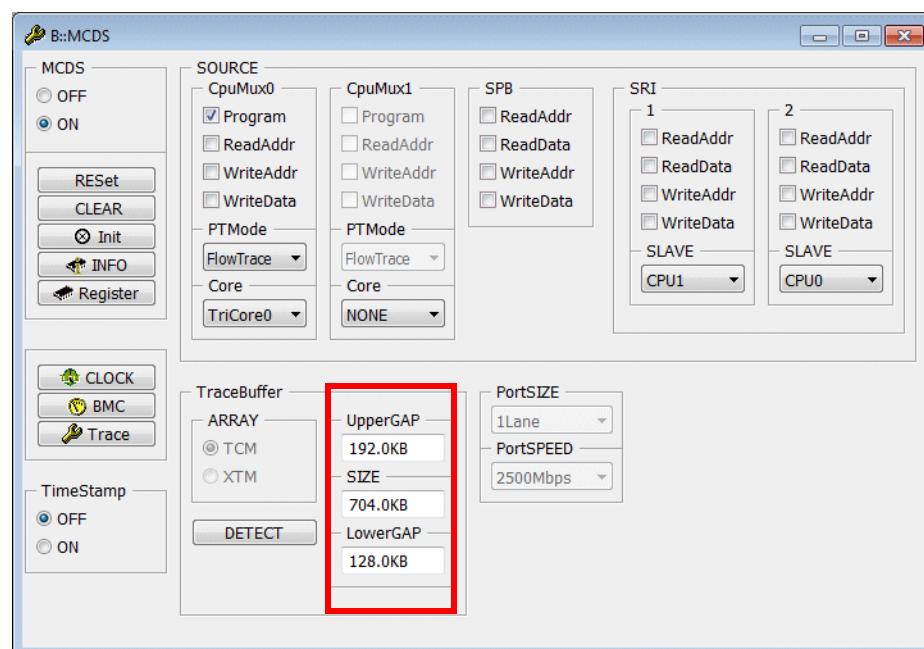
Use case 5: First calibration memory then trace buffer then application memory

E.g. tile 0 and 1 are used by the calibration tool, tile 13 to 15 are used by the application. The rest of the EMEM can be used as onchip trace buffer.



```
MCDS.TraceBuffer SIZE 704.KB ; size of trace buffer
; 1024 KByte - (5 * 64 KByte)

MCDS.TraceBuffer LowerGAP 128.KB ; size of the lower gap
; 2 * 64 KByte
```



2. Enable MCDS timestamp messages

The Timestamp Messages are disabled by default.

Enabling the time information requires the following steps:

2.1. Inform TRACE32 about the system clocks (mainly oscillator clock frequency) and enable all derived calculations. One of the derived calculations is **f(mcds)**. Since Timestamp Messages provide the number of MCDS clocks needed by a set of executed instructions $1/f(\text{mcds})$ can be used to calculate time information.

2.2. Enable Timestamp Messages.

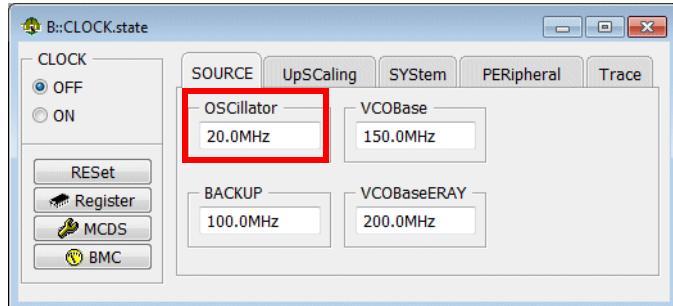
NOTE: Please be aware that there are constraints for the CPU clock, the Backbone Bus clock and the MCDS clock. For details refer to your AURIX manual.

A more complex configuration is required if you use a free-running clock.

2.1. Inform TRACE32 about the system clocks (mainly oscillator clock frequency) and enable all derived calculations.

CLOCK.state

Display the system clocks configuration/calculation window.

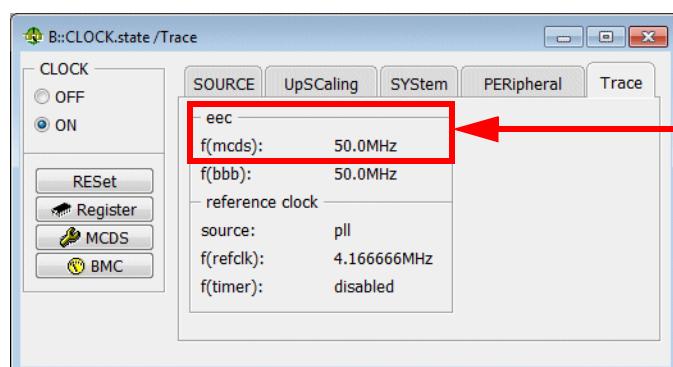


CLOCK.OSCillator <frequency>

Specify your board oscillator frequency, if it differs from the default setting.

CLOCK.ON

Declare system clocks as valid.



f(mclds) is needed to calculate time information

NOTE:

The system clock and its derived calculations are valid for all cores in a chip. The TRACE32 Resource Management ensures that the settings are consistent between the different TRACE32 instances of an AMP system (joint settings).

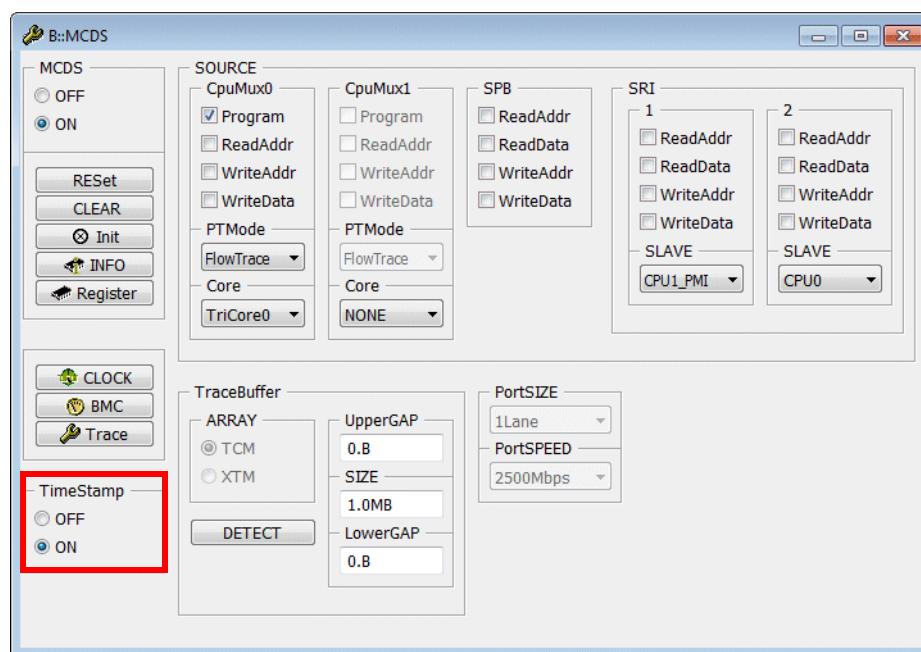
Please be aware that calculating the time out of the MCDS clock fails, if the pll change during the trace recording.

Please be aware that $f(\text{mcds})$ and $f(\text{cpu})$ can be set via TRACE32 commands, e.g. for the post mortem trace analysis (PowerTrace only).

MCDS.CLOCK.Frequency.McdsClock $<frequency>$	Specify $f(\text{mcds})$.
Trace.CLOCK $<freq>$	Specify $f(\text{cpu})$.
Trace.CLOCK $<freq0> <freq1> \dots$ (SMP tracing only)	Specify $f(\text{cpu0})$, $f(\text{cpu1})$, $f(\text{cpu2})$.

2.1. Enable Timestamp Messages.

MCDS.TimeStamp ON	Ticks (number of MCDS clock cycles) are enabled.
--------------------------	--



Examples

Example 1

```
; use complete emulation memory
; (EMEM) provided by ED device as
; onchip trace buffer

SYStem.Up

;...

CLOCK.ON ; declare settings of system clocks
; as valid
; your board oscillator frequency
; is 20.MHz

; this setting provides f(mcdfs) to
; TRACE32

MCDS.TimeStamp ON ; enable Timestamp Messages
; (ticks)
```

Example 2

```
; use complete emulation memory
; (EMEM) provided by ED device as
; onchip trace buffer

SYStem.Up

;...

CLOCK.state ; display system clocks
; configuration and calculation
; window

CLOCK.OSCillator 30.MHz ; your board oscillator frequency
; is 30.MHz

CLOCK.ON ; declare settings of system clocks
; as valid

; this setting provides f(mcdfs) to
; TRACE32

MCDS.TimeStamp ON ; enable Timestamp Messages
; (ticks)
```

Example 3

```
MCDS.TraceBuffer SIZE 640.KB          ; size of trace buffer
; 1024 KByte - (6 * 64 KByte)

; ...

SYStem.Up

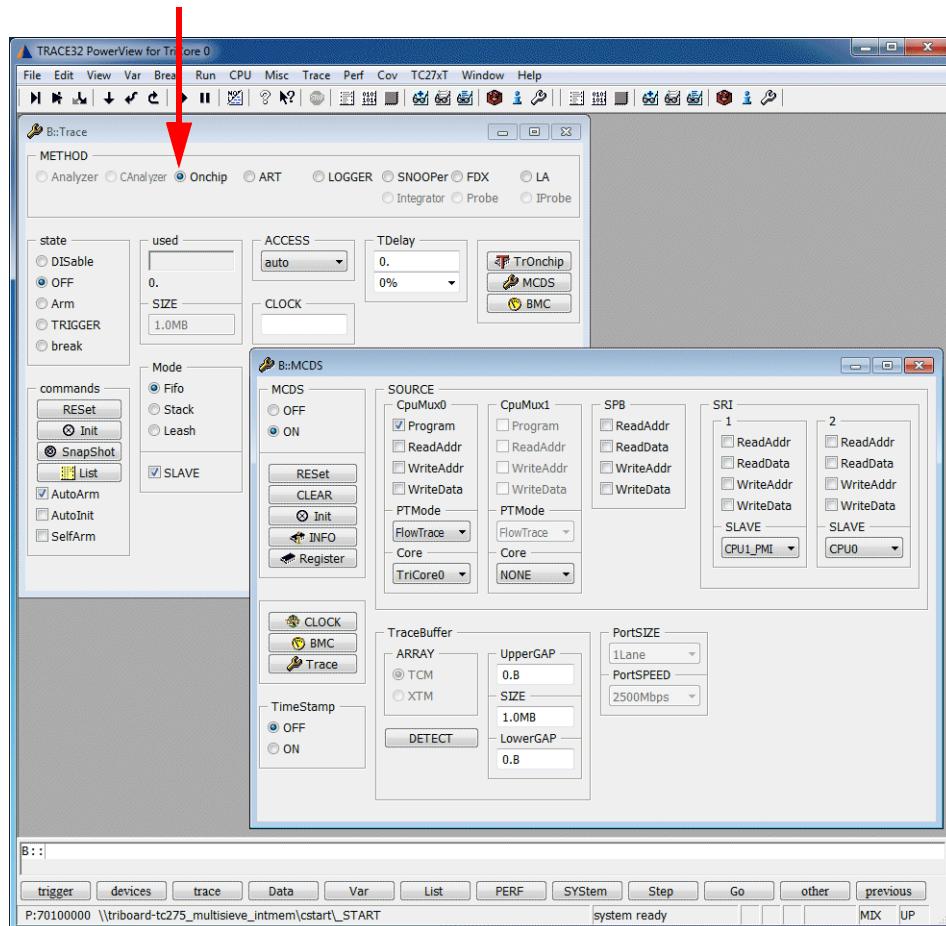
; ...

CLOCK.ON          ; declare settings of system clocks
; as valid
; your board oscillator frequency
; is 20.MHz

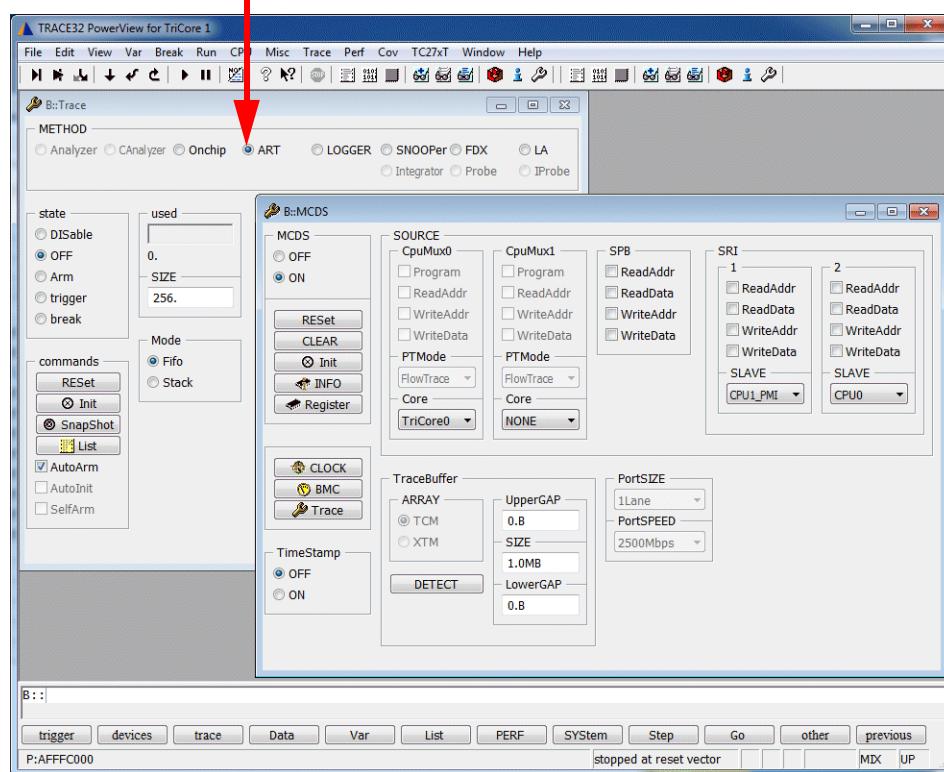
; this setting provides f(mcda) to
; TRACE32

MCDS.TimeStamp ON ; enable Timestamp Messages
; (ticks)
```

Please be aware that **Trace.METHOD Onchip** is only set for the first TRACE32 instance.



For all other TRACE32 instances **Trace METHOD ART** (Advanced Register Trace) is selected.



It is recommended to set **Trace.METHOD Onchip** for all other TRACE32 instances:

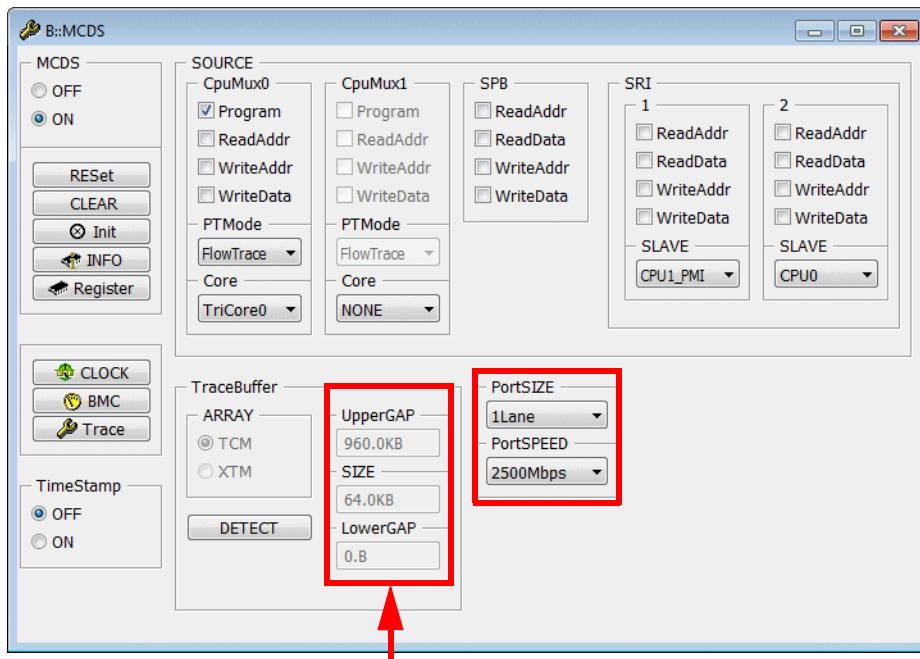
Trace.METHOD Onchip

; select Trace.METHOD Onchip

Off-chip Trace Configuration

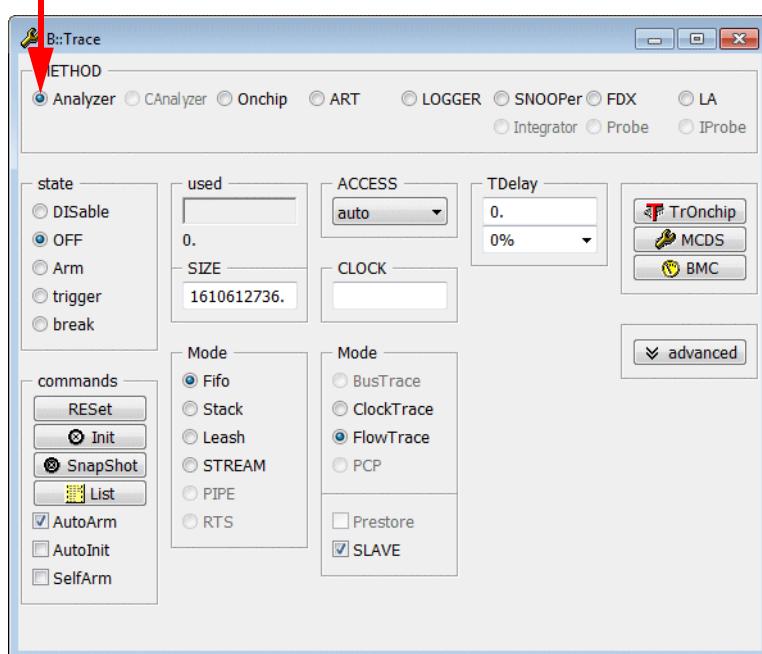
Auto-Configuration

When the communication between the debugger and the core(s) is established by **SYStem.Up** channel training is performed for the Aurora GigaBit Trace.



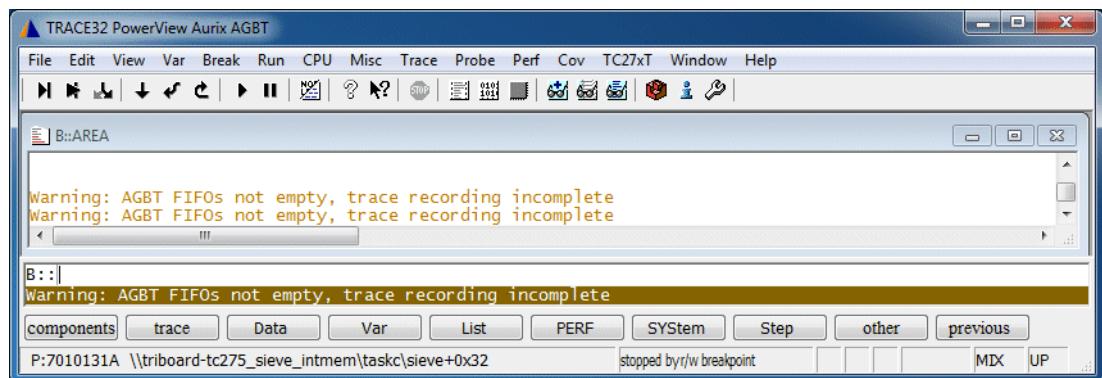
Tile 0 of the EMEM is used as internal buffer (FIFO) for the Aurora GigaBit Trace

Analyzer (off-chip tracing) is automatically selected by TRACE32



Restrictions

When the program execution is stopped, it may happen that the trace information is not completely flushed. TRACE32 can detect that, but it can not trigger flushing. This is a known AGBT bug.

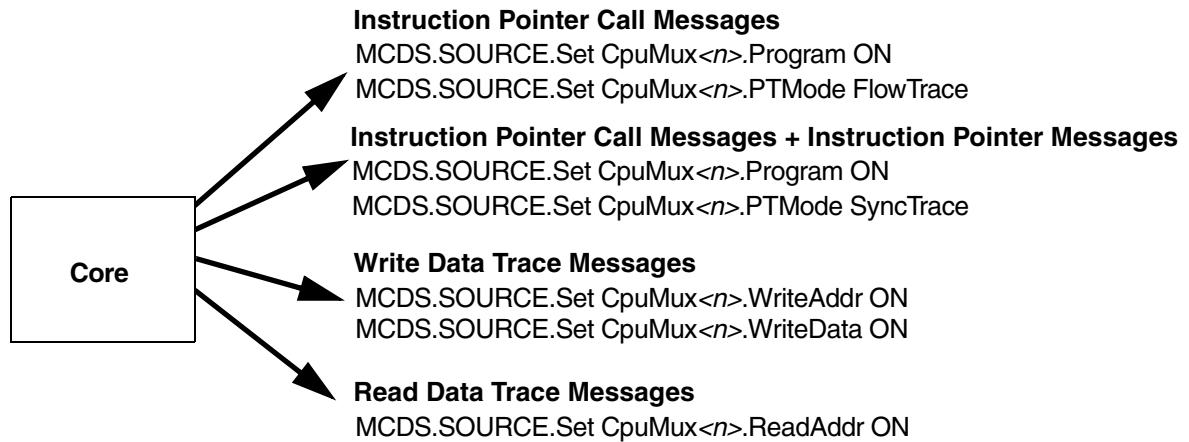


Trace information on the last executed instructions might be lost.

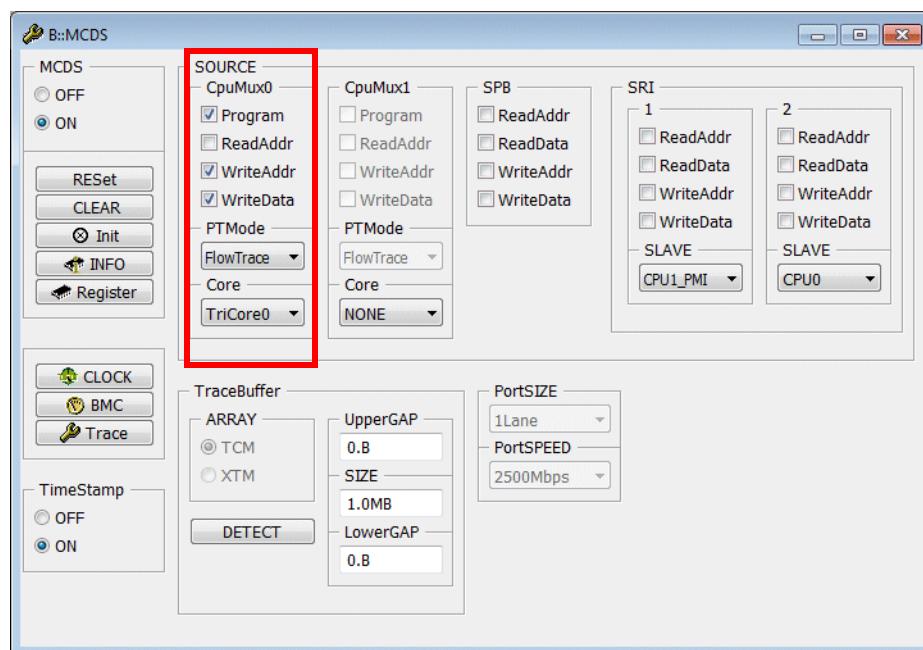
Trace Sources and Their Messages

Cores as Trace Source

A core can generate the following trace messages



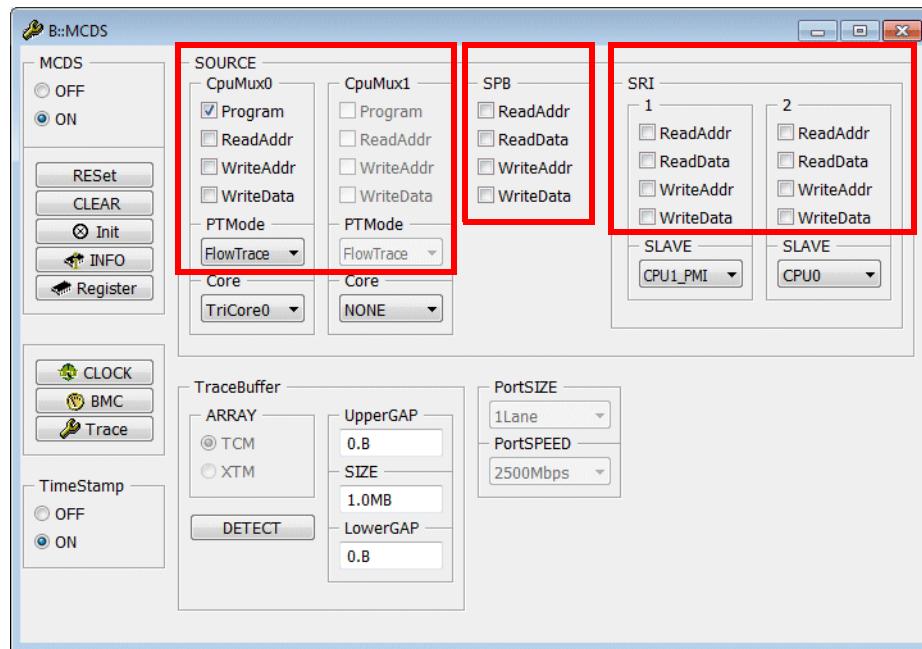
```
MCDS.state ; display MCDS configuration  
; window
```



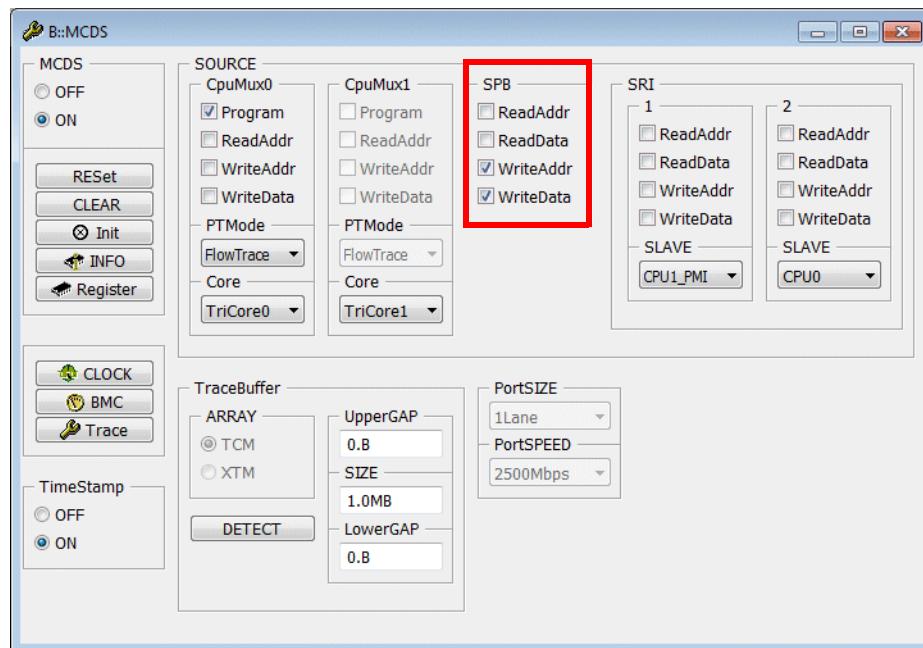
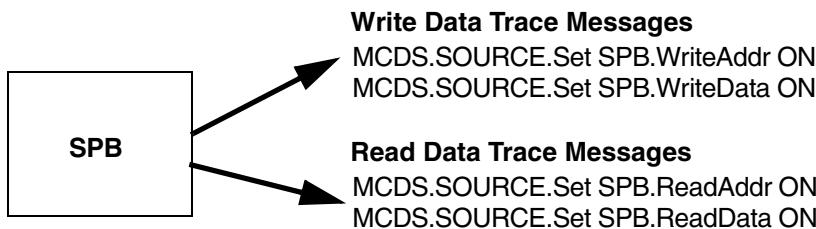
MCDS.SOURCE.DEFault

; reset the message configuration
; to its default setting

The command **MCDS.SOURCE.DEFault** applies to the settings marked in the picture below:



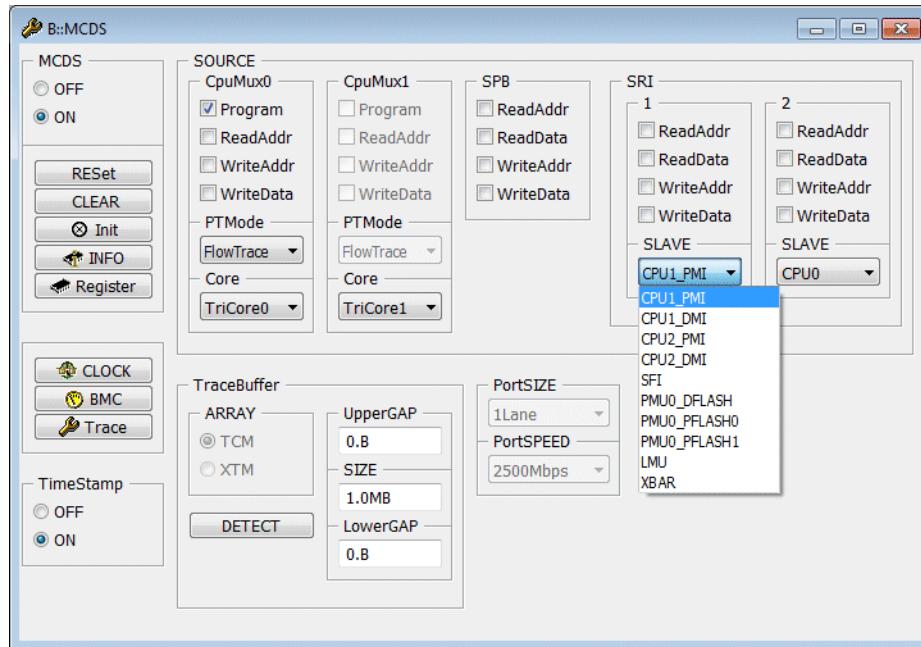
The following trace messages are generated for the System Peripheral Bus



Shared Resource Interconnect as Trace Source

Due to bandwidth issues it is not possible to generate trace messages for all transfers on the SRI.

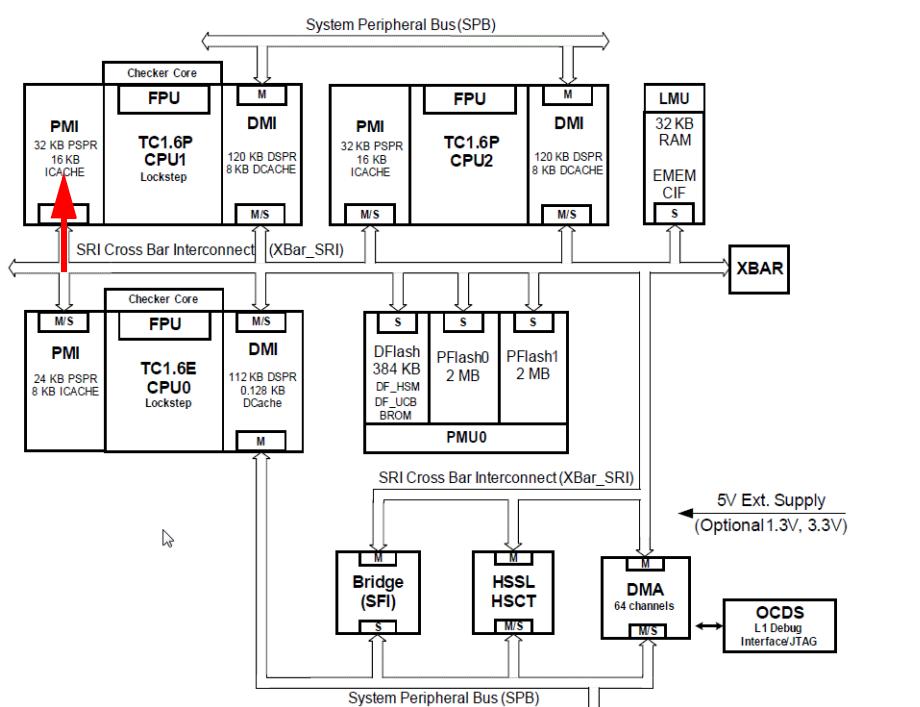
Trace messages can be generated for transfers to two selected slaves.



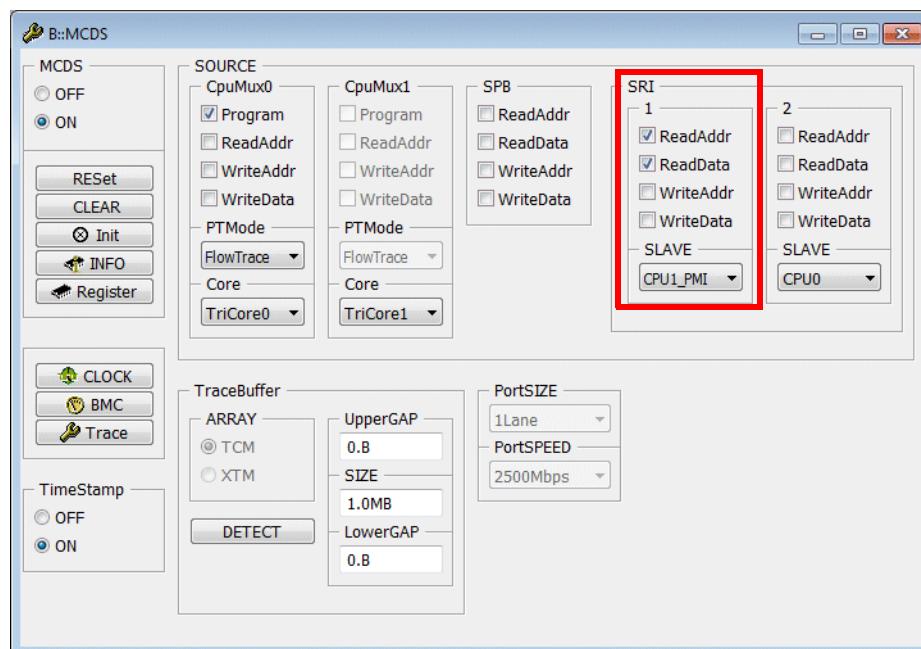
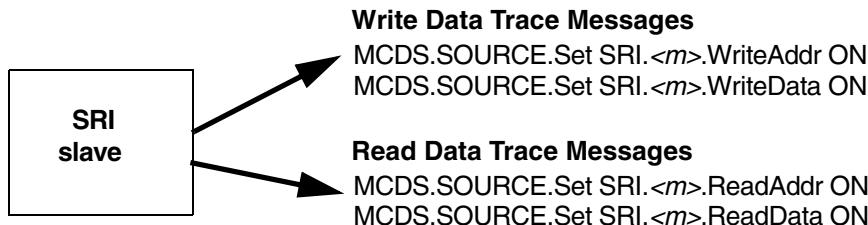
```

; Select PMI of CPU1 as first SRI slave
; This selection enables MCDS to generate trace messages for all
; transfers to the PMI of core 1
MCDS.SOURCE.Set SRI.1.SLAVE CPU1_PMI

```



For each SRI slave the following messages can be generated:



Message Display in TRACE32

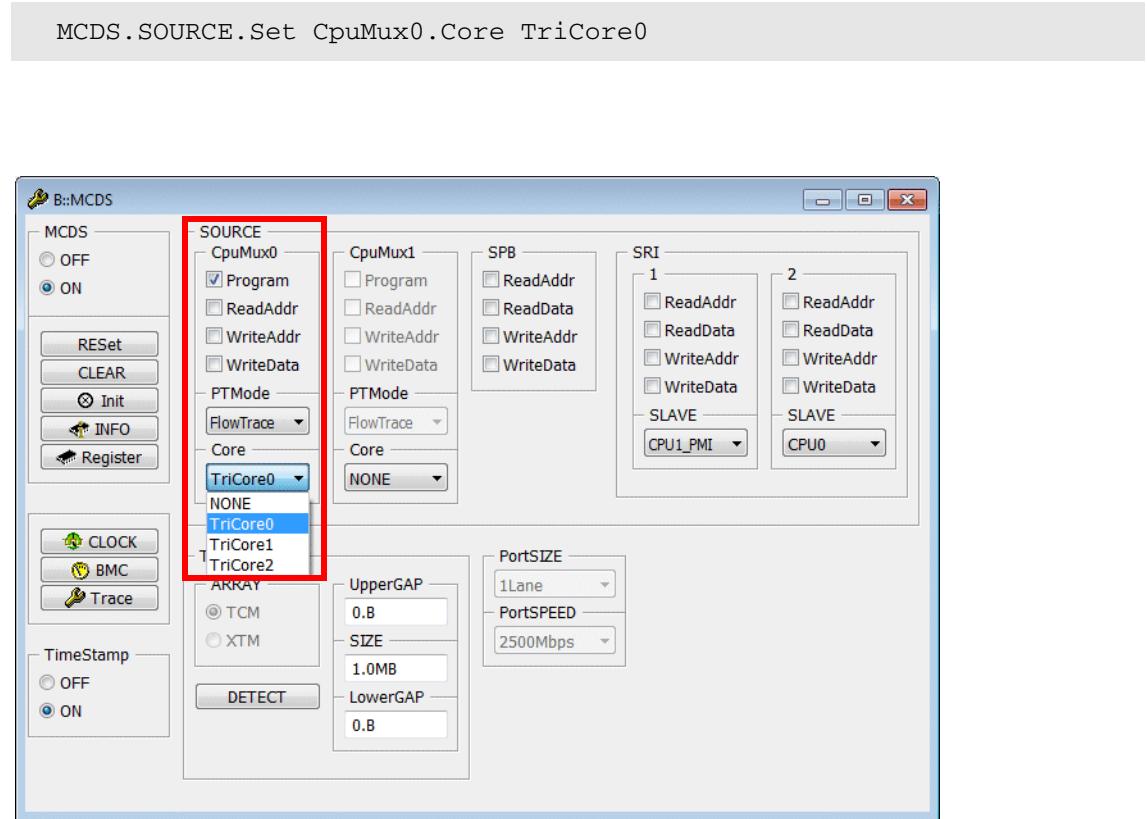
In general, the following applies:

- A TRACE32 instance displays trace information for all the cores it controls.
- Trace information generated by the SPB/SRI is always displayed in all TRACE32 instances that are started to debug the TriCore cores of the AURIX chip.

Tracing of a Single Core

A TRACE32 instance controls one TriCore core (TC 1.6.1 CPU0 here).

- Please make sure that you have selected the core that is controlled by the TRACE32 instance in the MCDS window. For example:



All trace messages enabled for the selected core and SPB/SRI are displayed in the TRACE32 Trace Listing.

The image shows the TRACE32 Trace Listing window. The assembly code in the listing is as follows:

```
add16    dd,#0x1          ; i,#
686      for ( i = 0 ; i <= SIZE ; flags[ i++ ] = TRUE ) ;
       mov16    d15,#0x12
686      for ( i = 0 ; i <= SIZE ; flags[ i++ ] = TRUE ) ;
       jge     d15,0x700013F0      ; i,#
-01043650  D:700013F0 rd-sri  10300009700091 \\\triboard-tc275_sieve_intmem\taskc\sieve+0x6
-01043645  D:700013E0 rd-sri  FE3C0004006003C \\\triboard-tc275_sieve_intmem\taskc\main+0x1C8
-01043631  D:700013F8 rd-sri  FF34010A600F001 \\\triboard-tc275_sieve_intmem\taskc\sieve+0x0E
-01043637  D:70001400 rd-sri  043C006202829000 \\\triboard-tc275_sieve_intmem\taskc\sieve+0x2
-01043601  D:70001400 rd-sri  7FF60FF12DA10C2 \\\triboard-tc275_sieve_intmem\taskc\Global\flags
-01043587  D:70000700 rd-data  01 \\\triboard-tc275_sieve_intmem\taskc\sieve+0x16
-01043580  D:70001400 rd-sri  F70000911E3C0082 \\\triboard-tc275_sieve_intmem\taskc\sieve+0x1E
-01043566  D:70001410 rd-sri  F600F0011030FFD9 \\\triboard-tc275_sieve_intmem\taskc\sieve+0x26
-01043551  D:70001418 rd-sri  001A146E0800FF09 \\\triboard-tc275_sieve_intmem\taskc\sieve+0x2E
-01043536  P:700013F0 ptrace
686      for ( i = 0 ; i <= SIZE ; flags[ i++ ] = TRUE ) ;
```

The trace listing shows multiple entries for the same assembly code, indicating multiple executions or parallel tasks. The columns include record, run, address, cycle, data, symbol, busmaster, and ti.back.

Three TRACE32 instances are started, each TRACE32 instance controls one TC 1.6.1 core.

Since MCDS can generate trace information only for 2 cores, you have to configure the trace multiplexers.

```
; configuration command for trace multiplexer CpuMux0
MCDS.SOURCE.Set CpuMux0.Core TriCore0 | TriCore1 | TriCore2

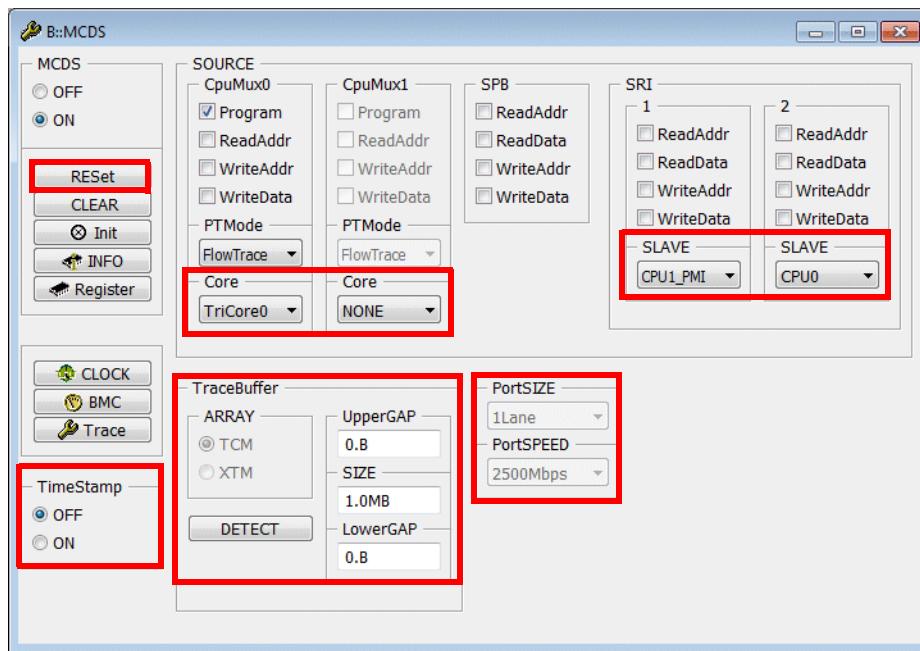
; configuration command for trace multiplexer CpuMux1
MCDS.SOURCE.Set CpuMux1.Core TriCore1 | TriCore2 | OTGM
```

Each TRACE32 instance has its own MCDS window.

Since more than one TRACE32 instance can configure MCDS (single source) the following rules apply:

1. Joint settings

The TRACE32 Resource Management maintains consistency between the TRACE32 instances.



MCDS reset

MCDS.RESet

On-chip/off-chip trace configuration

```
MCDS.TraceBuffer.SIZE <size>
MCDS.TraceBuffer.LowerGAP <size>
MCDS.TraceBuffer.UpperGAP <size>
MCDS.PortSIZE <lanes>
MCDS.PortSPEED <speed>
```

Timestamp configuration

MCDS.TimeStamp OFF | ON

Configuration of the trace multiplexers

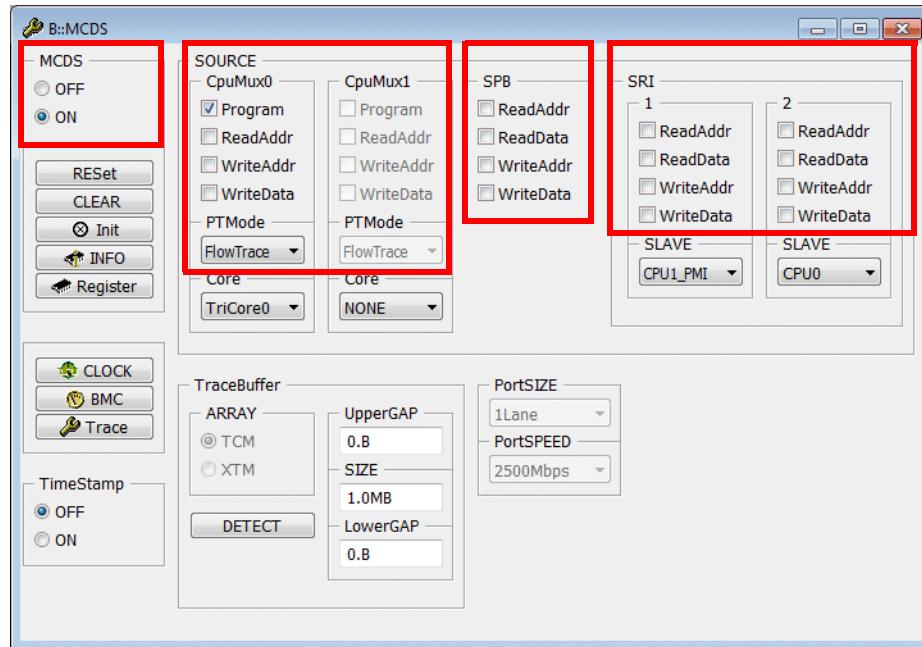
```
MCDS.SOURCE.Set CpuMux0.Core TriCore0 | TriCore1 | TriCore2
```

```
MCDS.SOURCE.Set CpuMux1.Core TriCore1 | TriCore2 | OTGM
```

```
MCDS.SOURCE.Set SRI.1|2.SLAVE <slave>
```

2. Exclusive settings

These settings can be done by each TRACE32 instance individually.



Connect/disconnect core from MCDS

MCDS.ON

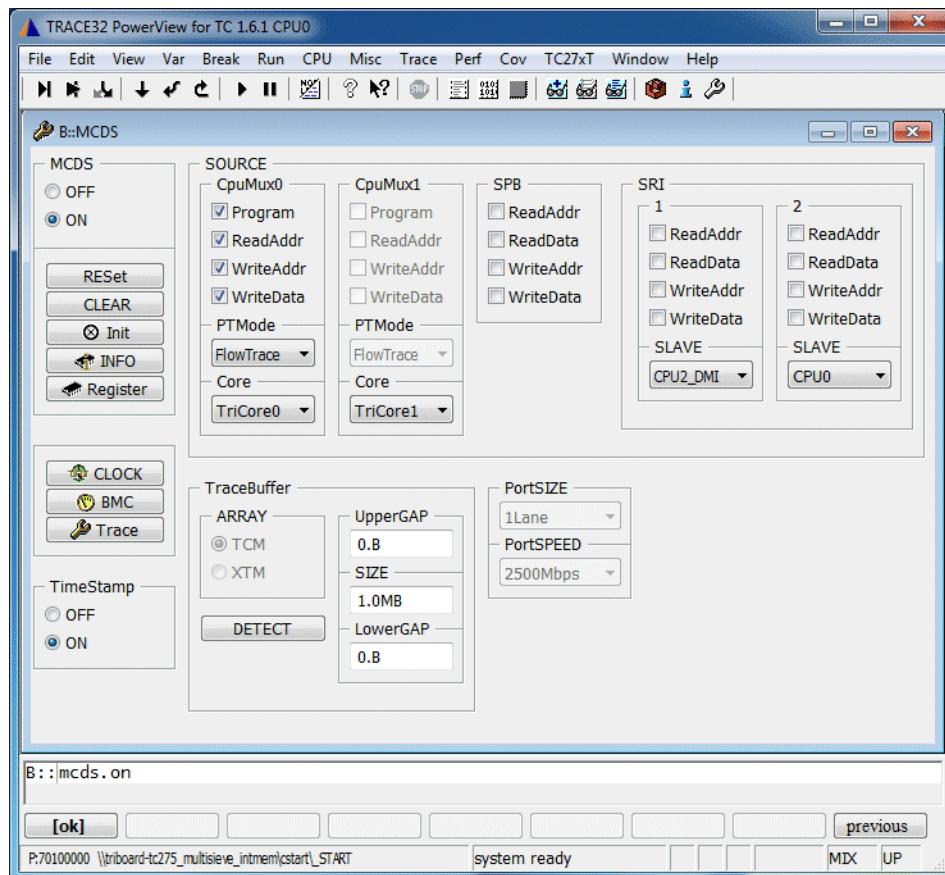
MCDS.OFF

Enabling/disabling of trace messages

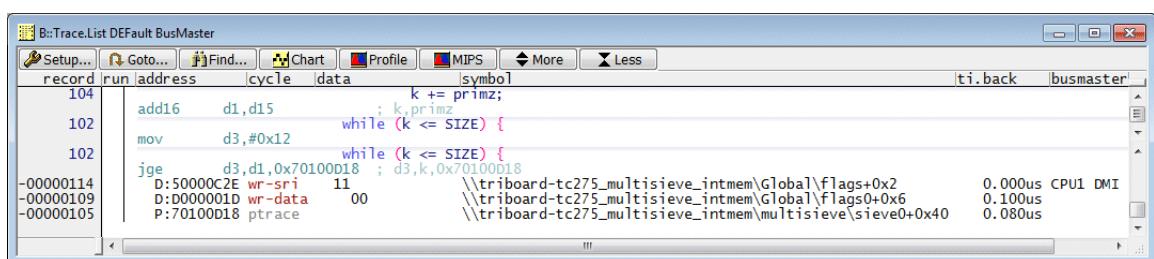
```
MCDS.SOURCE.Set CpuMux0.<source> ON | OFF
MCDS.SOURCE.Set CpuMux1.<source> ON | OFF
MCDS.SOURCE.Set SPB.<source> ON | OFF
MCDS.SOURCE.Set SRI 1.<source> ON | OFF
MCDS.SOURCE.Set SRI 2.<source> ON | OFF
```

Example

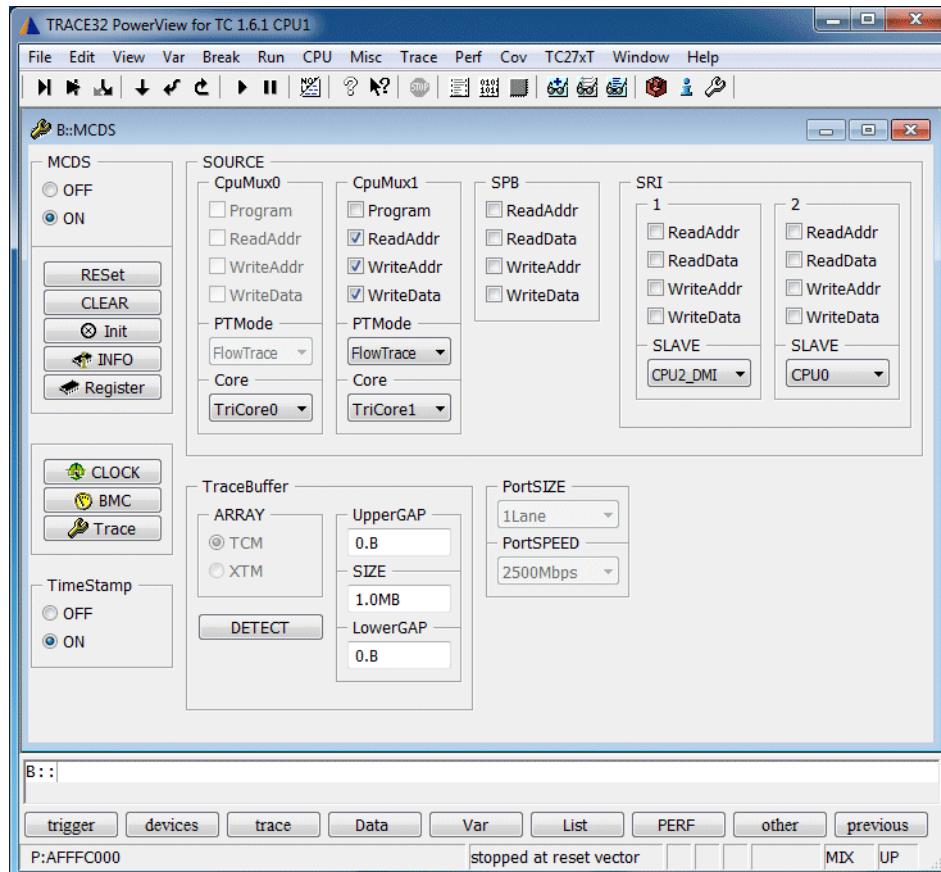
MCDS settings for TC 1.6.1 CPU0



All trace messages enabled for TriCore0 and all SPB/SRI trace messages enabled in any TRACE32 instance are displayed in the TRACE32 Trace Listing.



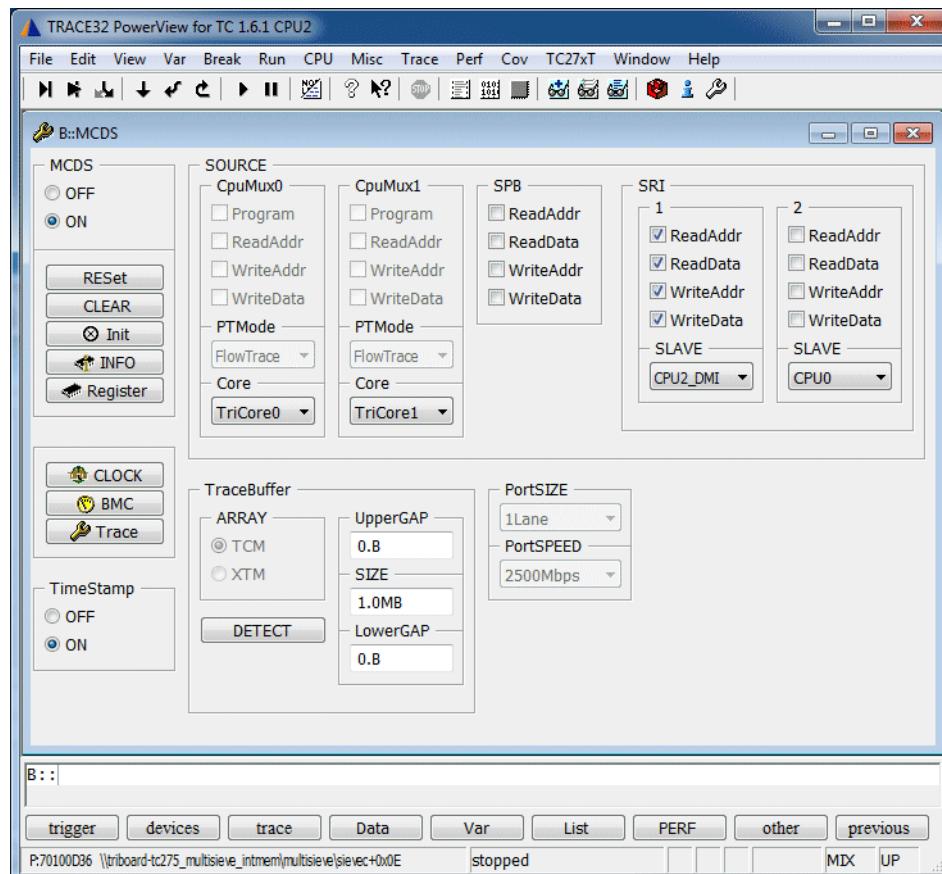
MCDS setting for TC 1.6.1 CPU1



All trace messages enabled for TriCore1 and all SPB/SRI trace messages enabled in any TRACE32 instance are displayed in the TRACE32 Trace Listing.

record	run	address	cycle	data	symbol	ti.back	busmaster
-00000235		D:D0004FB8	wr-data	0000000000000000	\\triboard-tc275_multisieve_intmem\Global\\lc_ub_csa_tc1+0xFB8	0.020us	
-00000226		D:D0004F88	wr-data	0000000000000000	\\triboard-tc275_multisieve_intmem\Global\\lc_ub_csa_tc1+0xF88	0.000us	
-00000214		D:D0004FA8	wr-data	0000000000000000	\\triboard-tc275_multisieve_intmem\Global\\lc_ub_csa_tc1+0xFA8	0.020us	
-00000175		D:50000C2C	wr-data	11	\\triboard-tc275_multisieve_intmem\Global\Flags	0.720us	
-00000166		D:50000C2C	wr-sri	11	\\triboard-tc275_multisieve_intmem\Global\Flags	0.060us	CPU1 DMI
-00000144		D:50000C2D	wr-data	11	\\triboard-tc275_multisieve_intmem\Global\Flags+0x1	0.300us	
-00000140		D:50000C2D	wr-sri	11	\\triboard-tc275_multisieve_intmem\Global\Flags+0x1	0.060us	CPU1 DMI
-00000120		D:50000C2E	wr-data	11	\\triboard-tc275_multisieve_intmem\Global\Flags+0x2	0.300us	
-00000114		D:50000C2E	wr-sri	11	\\triboard-tc275_multisieve_intmem\Global\Flags+0x2	0.060us	CPU1 DMI

MCDS setting for TC 1.6.1 CPU2



All SPB/SRI trace messages enabled in any TRACE32 instance are displayed in the TRACE32 Trace Listing.

B::Trace.List Default BusMaster										
record	run	address	cycle	data	symbol	ti.back	busmaster
-000001237		D:50000C3A	rd-sri	10	\\triboard-tc275_multisieve_intmem\Global\fFlags+0x0E	0.380us	CPU0 DMI			
-000001187		D:50000C3B	rd-sri	00	\\triboard-tc275_multisieve_intmem\Global\fFlags+0x0F	0.440us	CPU0 DMI			
-000001162		D:50000C3C	rd-sri	00	\\triboard-tc275_multisieve_intmem\Global\fFlags+0x10	0.260us	CPU0 DMI			
-000001140		D:50000C3D	rd-sri	10	\\triboard-tc275_multisieve_intmem\Global\fFlags+0x11	0.200us	CPU0 DMI			
-000001106		D:50000C3E	rd-sri	00	\\triboard-tc275_multisieve_intmem\Global\fFlags+0x12	0.400us	CPU0 DMI			
-00000166		D:50000C2C	wr-sri	11	\\triboard-tc275_multisieve_intmem\Global\fFlags+0x13	13.840us	CPU1 DMI			
-00000140		D:50000C2D	wr-sri	11	\\triboard-tc275_multisieve_intmem\Global\fFlags+0x1	0.360us	CPU1 DMI			
-00000114		D:50000C2E	wr-sri	11	\\triboard-tc275_multisieve_intmem\Global\fFlags+0x2	0.360us	CPU1 DMI			

Tracing of SMP Systems

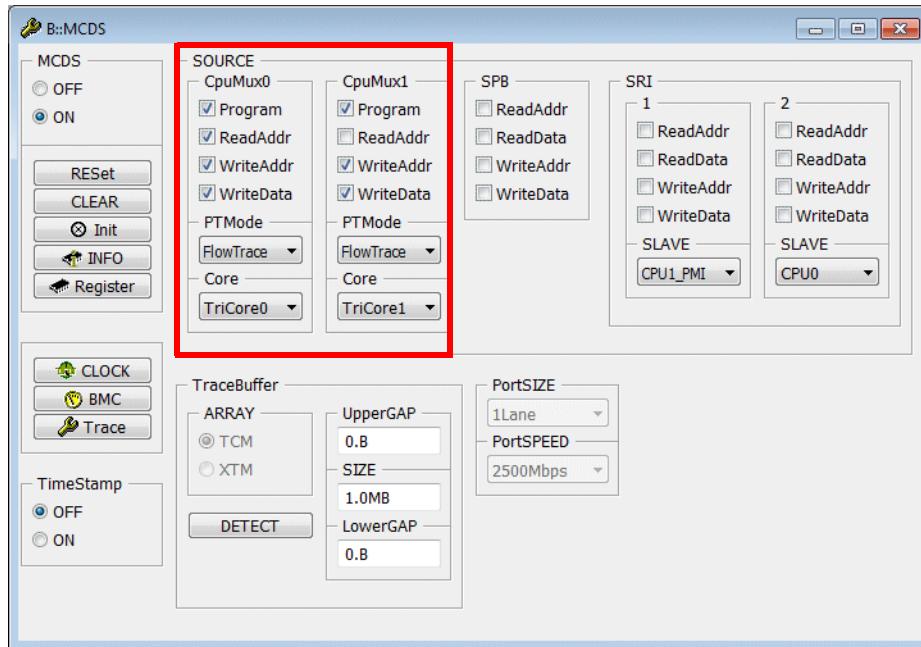
A TRACE32 instance controls all TriCore cores.

Since MCDS can generate trace information only for 2 cores, you have to configure the trace multiplexers.

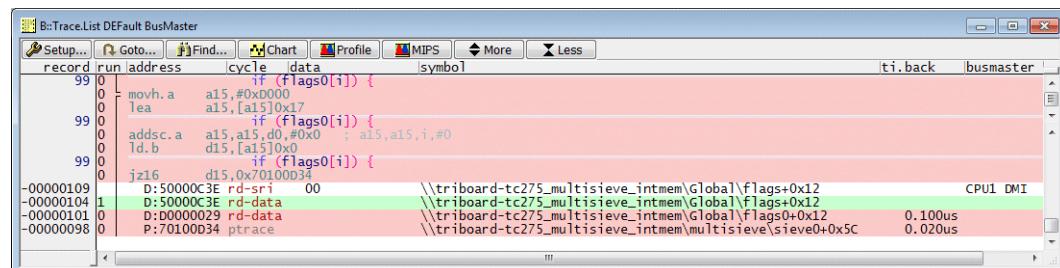
```
; configuration command for trace multiplexer CpuMux0
MCDS.SOURCE.Set CpuMux0.Core TriCore0 | TriCore1 | TriCore2

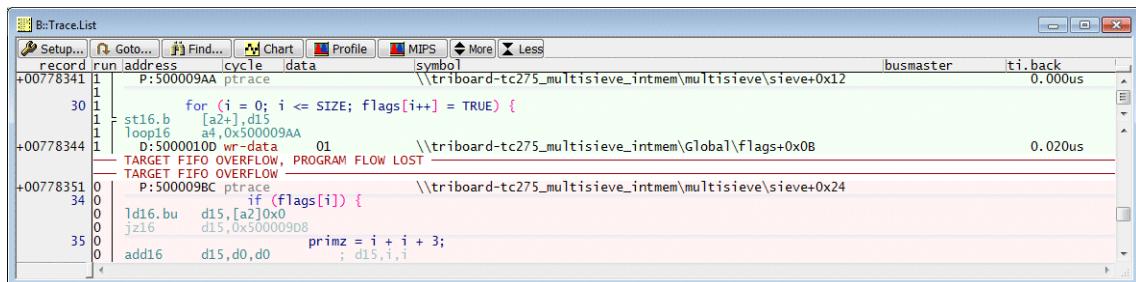
; configuration command for trace multiplexer CpuMux1
MCDS.SOURCE.Set CpuMux1.Core TriCore1 | TriCore2 | OTGM
```

One MCDS window is provided to control the message generation for all cores.



All trace messages enabled for the selected cores and SPB/SRI trace messages are displayed in the TRACE32 Trace Listing.





TARGET FIFO OVERFLOW, PROGRAM FLOW LOST occurs, when more trace information is generated than can be transferred into the EMEM.

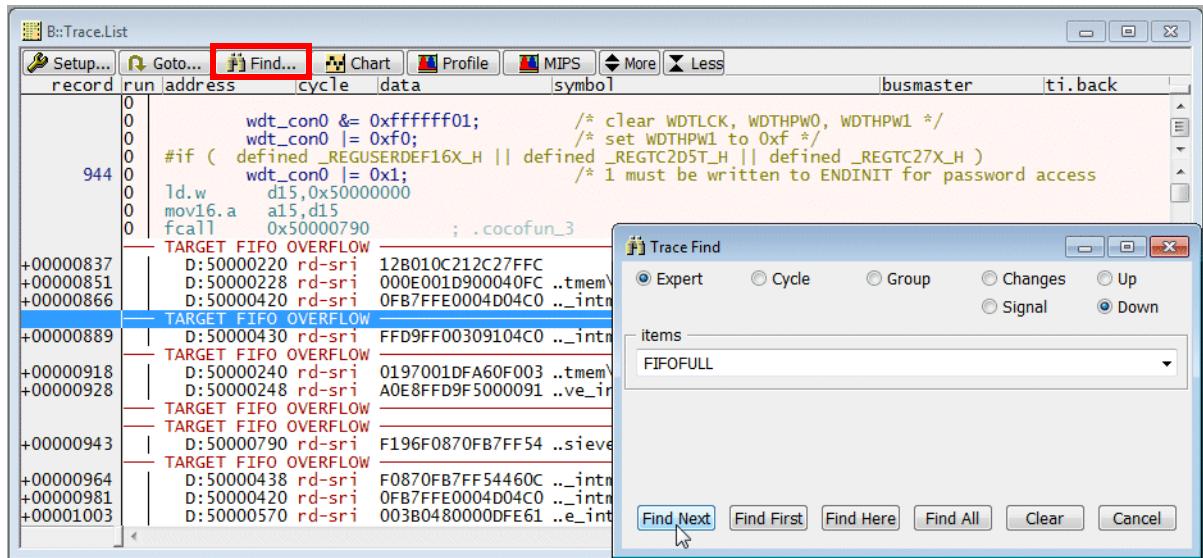
In order to get an immediate display of the trace contents TRACE32 uploads only the currently displayed section from the trace buffer to the host.

To check the number of FIFOFULLs it is recommended to upload the complete trace contents to the host by the command: **Trace.FLOWPROCESS**.

The complete number of FIFOFULL can be displayed by:

```
PRINT %Decimal Trace.FLOW.FIFOFULL()
```

The single FIFOFULLs can be found in the trace:

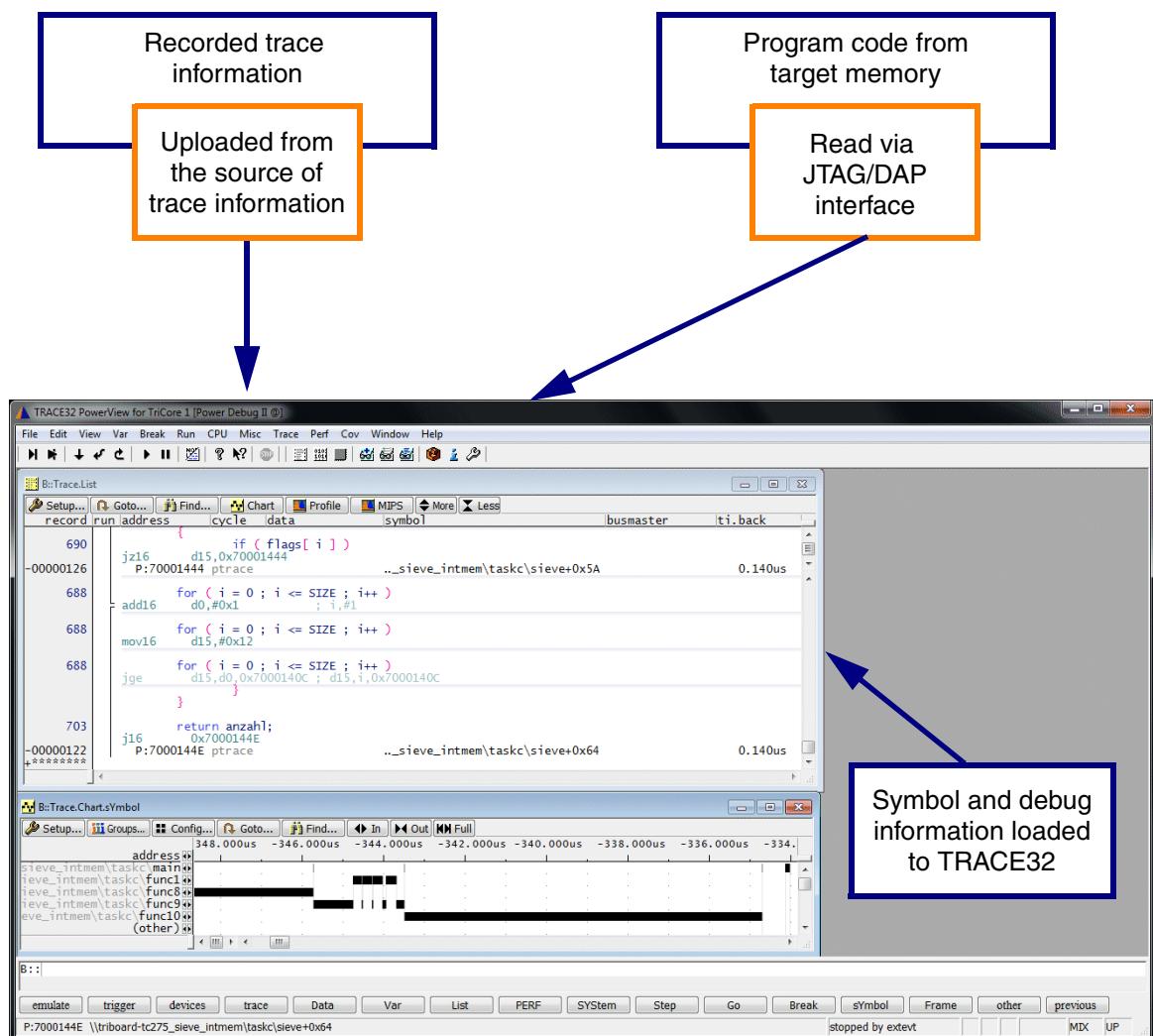


Displaying the Trace Contents

Sources of Information for the Trace Display

In order to provide an intuitive trace display the following sources of information are merged:

- The trace information recorded.
- The program code from the target memory read via the JTAG/DAP interface.
- The symbol and debug information already loaded to TRACE32.



Influencing Factors on the Trace Information

The main influencing factor on the trace information is the MCDS. It specifies what type of trace messages is generated for the user.

Basic settings for trace messages were introduced in “[Trace Sources and Their Messages](#)”, page 37.

More advanced settings are described later in “[Trace Control by Filter and Trigger - Overview](#)”, page 93.

Another important influencing factor are the settings in the **TRACE32 Trace Configuration** window. It specifies how much trace information can be recorded and when the trace recording is stopped.

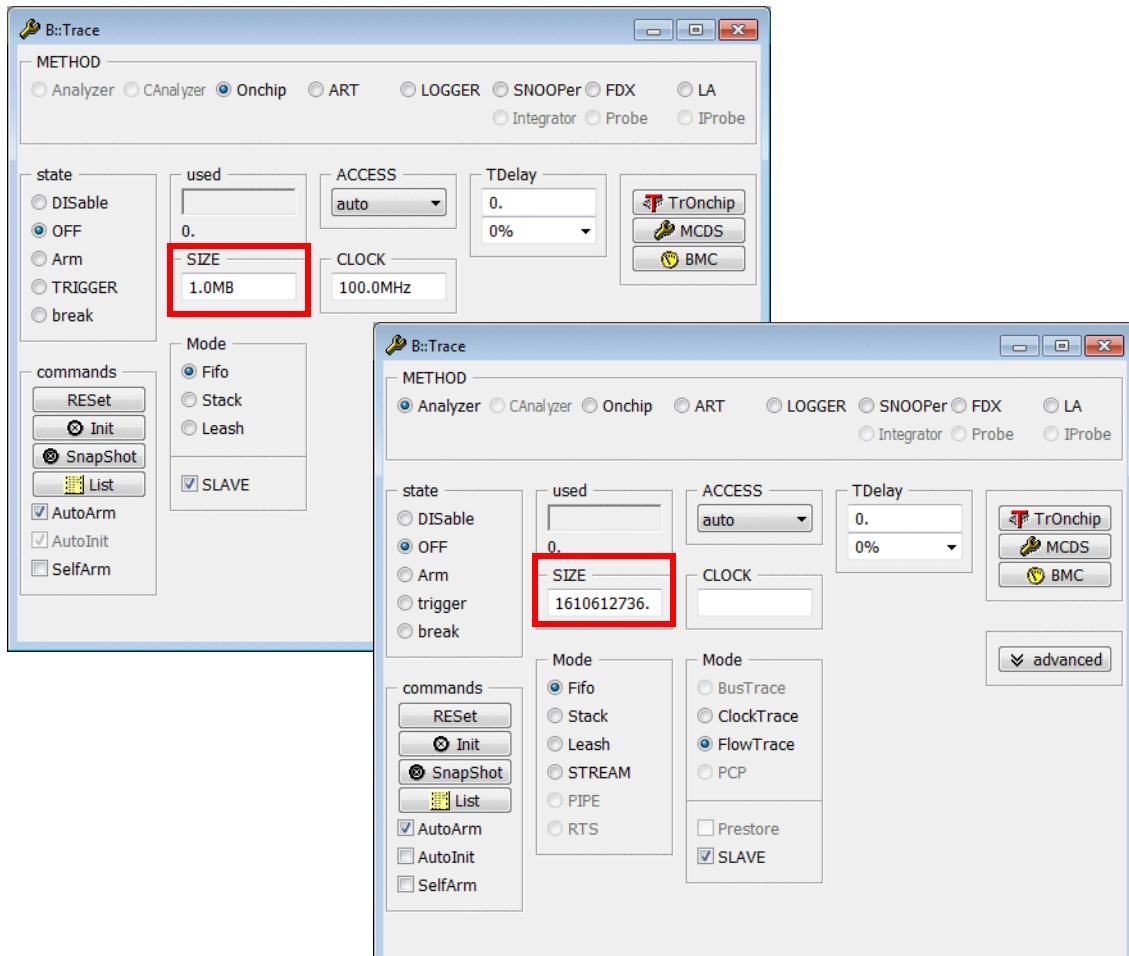
Mode Setting

The **Mode** settings in the Trace Configuration window specify how much trace information can be recorded and when the trace recording is stopped.

The following modes are provided:

- **Fifo, Stack, Leash Mode:** allow to record as much trace records as indicated in the **SIZE** field of the Trace Configuration window.

Onchip trace buffer (EMEM)



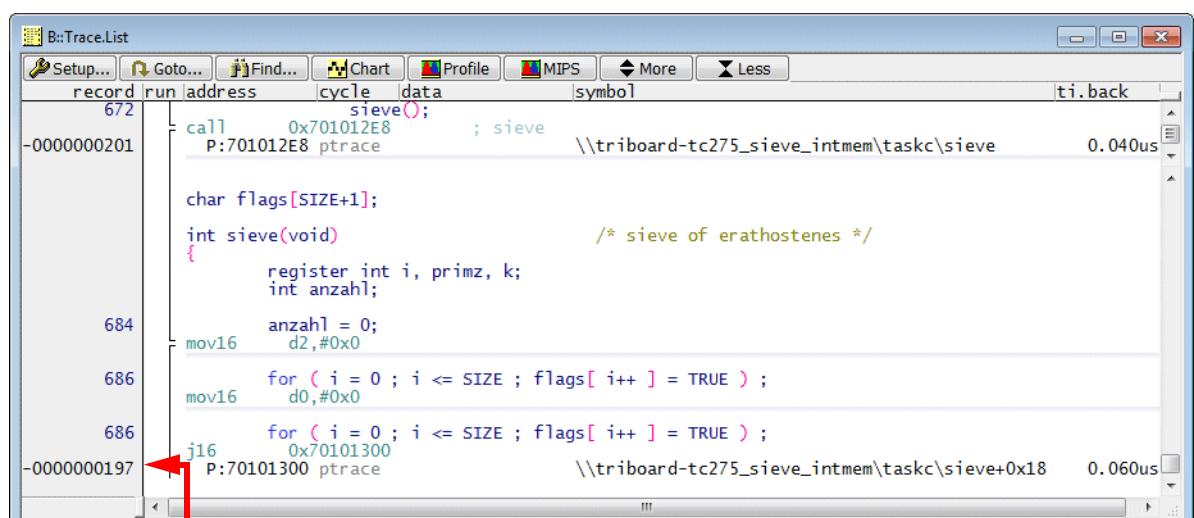
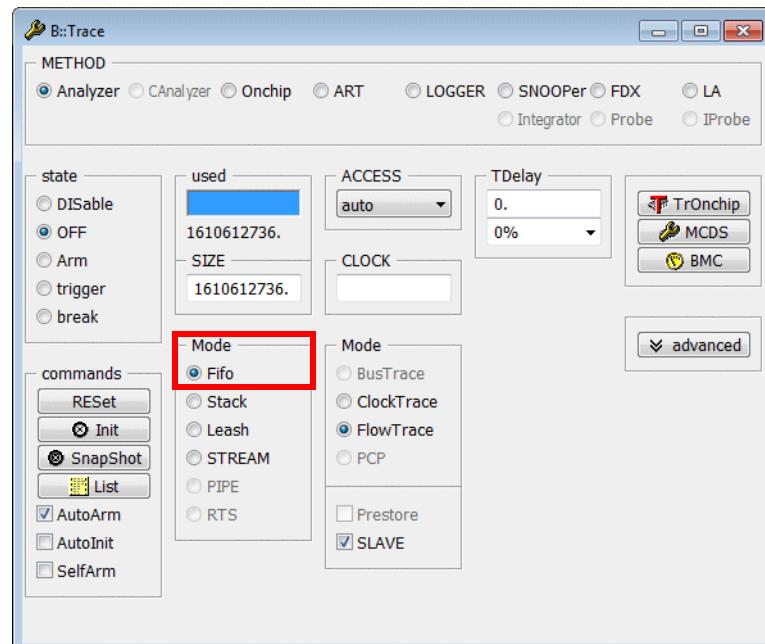
Trace memory of PowerTrace hardware

- **STREAM Mode (PowerTrace hardware only):** STREAM mode specifies that the trace information is immediately streamed to a file on the host computer. Peak loads at the trace port are intercepted by the trace memory of the PowerTrace, which can be considered as a large FIFO.

STREAM mode allows a trace memory of several Tera Frames.

STREAM mode required a 64-bit host computer and a 64-bit TRACE32 executable to handle the large trace record numbers.

```
Trace.Mode Fifo ; default mode
; when the trace memory is full
; the newest trace information will
; overwrite the oldest one
; the trace memory contains all
; information generated until the
; program execution stopped
```

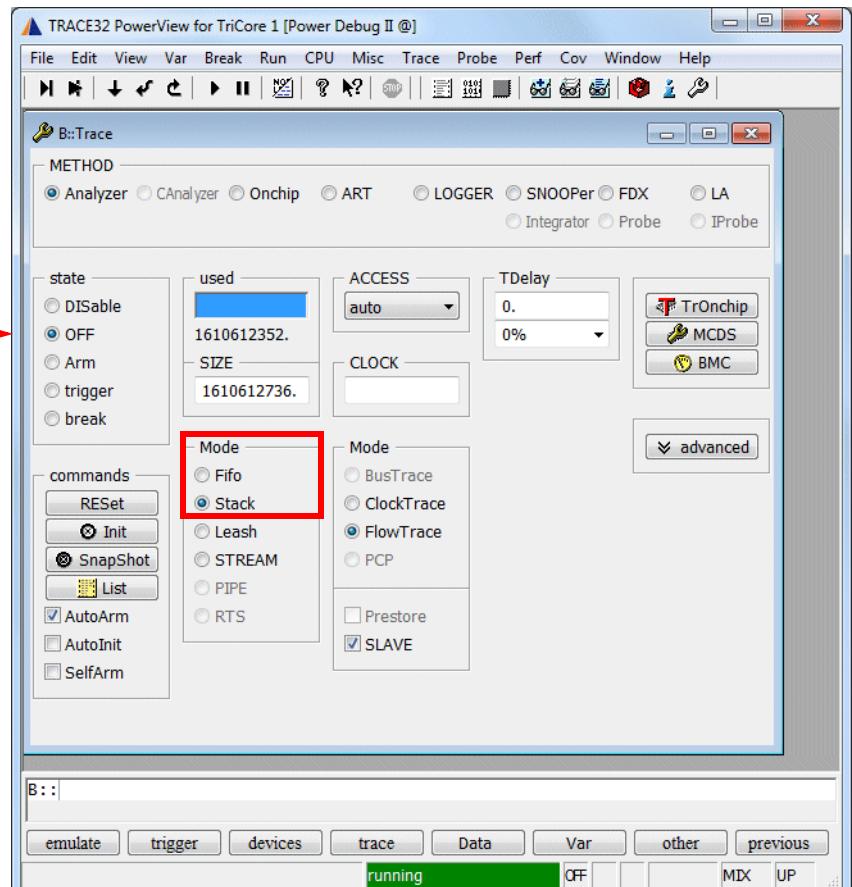


In **Fifo** mode negative record numbers are used. The last record gets the smallest negative number.

Trace.Mode Stack

; when the trace memory is full
; the trace recording is stopped

; the trace memory contains all
; information generated directly
; after the start of the program
; execution

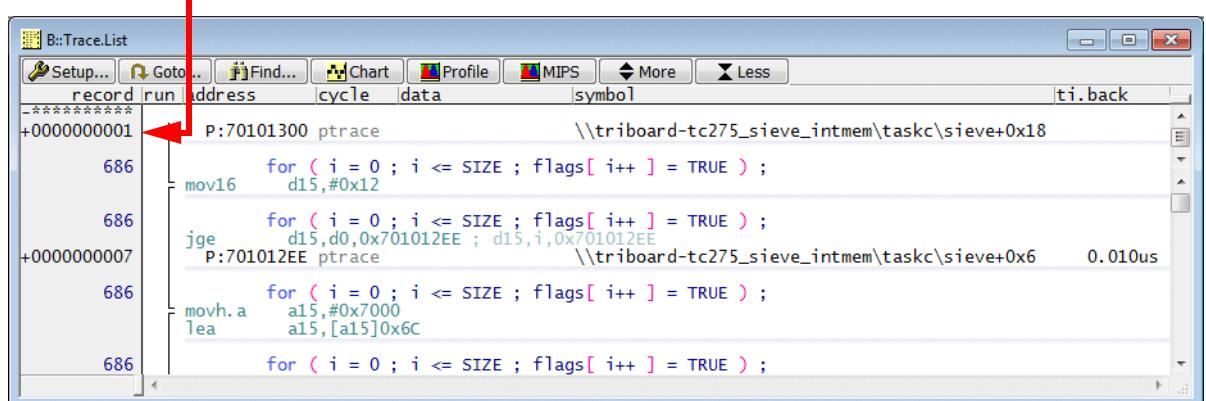


The trace recording is
stopped as soon as
the trace memory is
full (OFF state)

Green **running** in the Debug field
indicates that program execution is running

OFF in the Trace field
indicates that the trace
recording is switched off

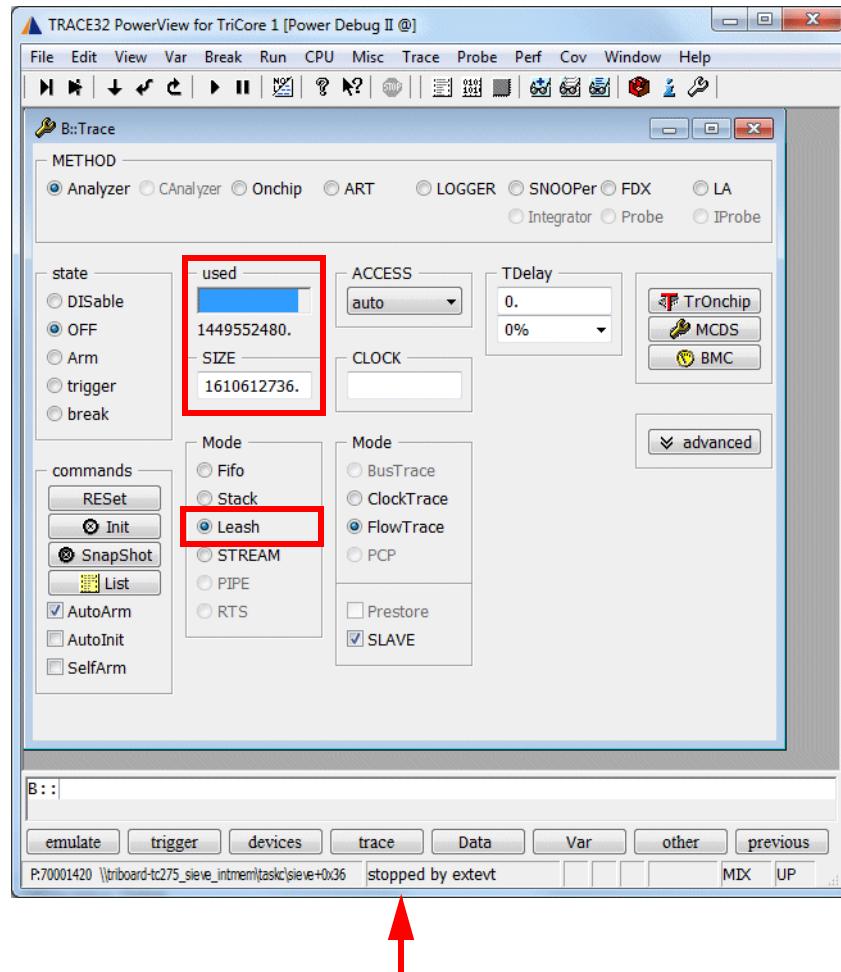
Since the trace recording starts with the program execution and stops when the trace memory is full, positive record numbers are used in **Stack** mode. The first record in the trace gets the smallest positive number.



The screenshot shows the B::Trace.List window with the following content:

record	run	address	cycle	data	symbol	ti.back
+0000000001		P:70101300	ptrace		\\triboard-tc275_sieve_intmem\taskc\sieve+0x18	
686			for (i = 0 ; i <= SIZE ; flags[i++] = TRUE) ;			
		mov16	d15,#0x12			
686			for (i = 0 ; i <= SIZE ; flags[i++] = TRUE) ;			
+0000000007		jge	d15,d0,0x701012EE ; d15,i,0x701012EE		P:701012EE	ptrace \\triboard-tc275_sieve_intmem\taskc\sieve+0x6 0.010us
686			for (i = 0 ; i <= SIZE ; flags[i++] = TRUE) ;			
		movh.a	a15,#0x7000			
		lea	a15,[a15]0x6C			
686			for (i = 0 ; i <= SIZE ; flags[i++] = TRUE) ;			

```
Trace.Mode Leash ; when the trace memory is nearly
                   ; full the program execution is
                   ; stopped
                   ; Leash mode uses the same record
                   ; numbering scheme as Stack mode
```



The program execution is **stopped** as soon as the trace buffer is nearly full.

Since stopping the program execution when the trace buffer is nearly full requires some logic/time, **used** is smaller than the maximum **SIZE**.

The trace information is immediately streamed to a file on the host computer after it was placed into the trace memory. This procedure extends the size of the trace memory up to several T Frames.

STREAM mode required a 64-bit host computer and a 64-bit TRACE32 executable to handle the large trace record numbers.

By default the streaming file is placed into the TRACE32 temp. directory ([OS.PresentTemporaryDirectory\(\)](#)).

The command **Trace.STREAMFILE** <file> allows to specific a different name and location for the streaming file.

```
Trace.STREAMFILE d:\temp\mystream.t32 ; specify the location for  
; your streaming file
```

TRACE32 stops the streaming when less then the 1 GByte free memory is left on the drive by default.

The command **Trace.STREAMFileLimit** <+/- limit in bytes> allows a user-defined free memory limitation.

```
Trace.STREAMFileLimit 5000000000. ; streaming file is limited to  
; 5 GByte  
  
Trace.STREAMFileLimit -5000000000. ; streaming is stopped when less  
; the 5 GByte free memory is left  
; on the drive
```

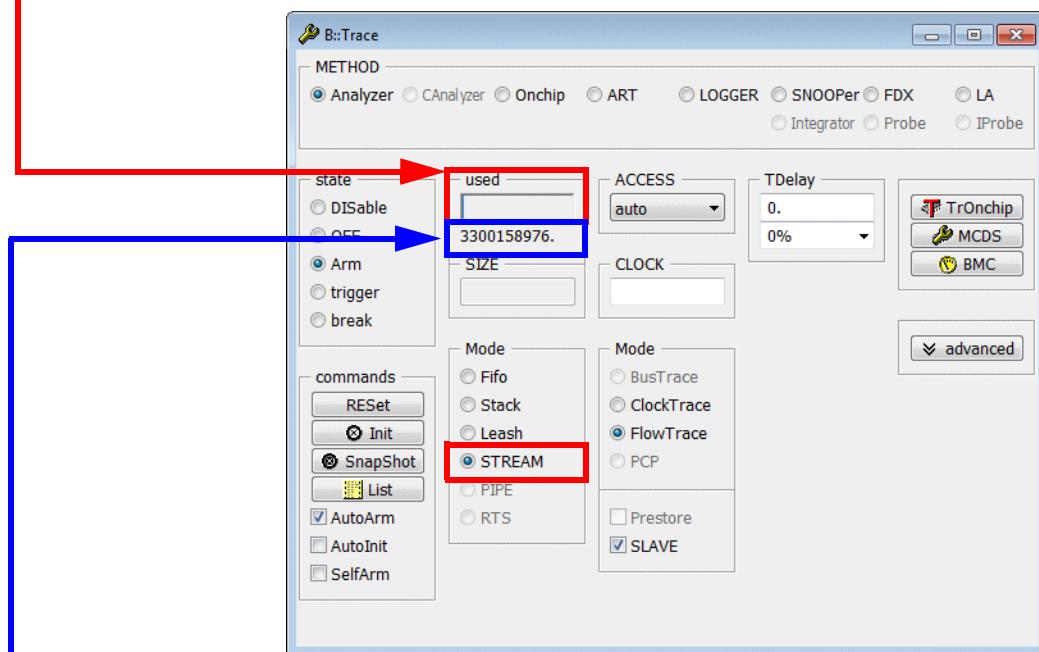
Please be aware that the streaming file is deleted as soon as you de-select the STREAM mode or when you exit TRACE32.

STREAM mode can only be used if the average data rate at the trace port does not exceed the maximum transmission rate of the host interface in use. Peak loads at the trace port are intercepted by the trace memory within the PowerTrace, which can be considered to be operating as a large FIFO.

```
Trace.Mode STREAM
; trace information is immediately
; streamed to a file on the host
; computer

; STREAM mode uses the same record
; numbering scheme as Stack mode
```

used graphically: number of records buffered by the trace memory in the PowerTrace



used numerically: Number of records saved to streaming file

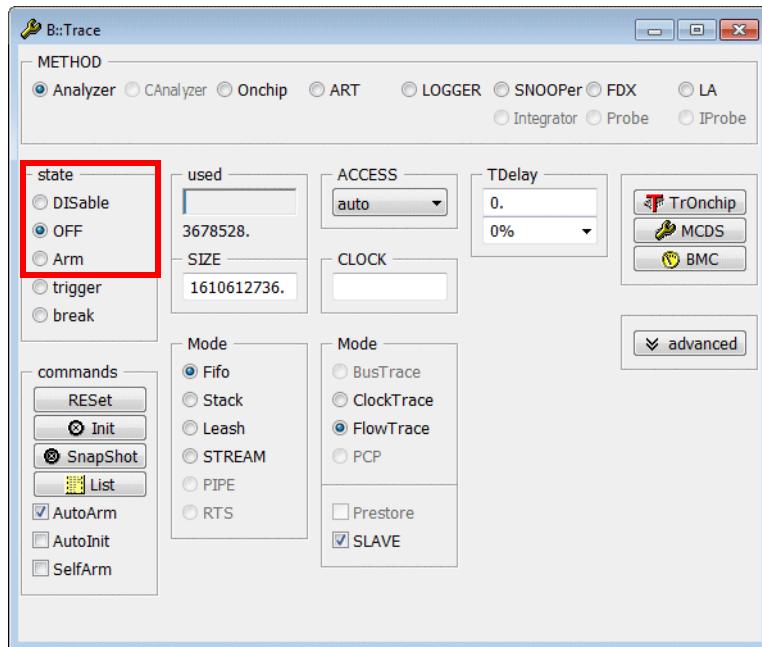
The screenshot shows the 'B:Trace.List' window displaying a list of trace records. A red arrow points from the text 'used numerically: Number of records saved to streaming file' to the list of records. The table shows the following data:

record	run	address	cycle	data	symbol	ti.back
692					primz = i + i + 3;	
693		add16	d15,#0x3		; primz,#3	
694		add	d1,d0,d15		k = i + primz;	
+00002162813965		j16	0x70101338		; d1,i,primz	
694		P:70101338	ptrace		while (k <= SIZE)	
694		mov	d3,#0x12		while (k <= SIZE)	
+00002162813969		jge	d3,d1,0x70101326		; d3,k,0x70101326	
		P:70101326	ptrace		\\"triboard-tc275_sieve_intmem\taskc\sieve+0x50	0.140us
					\\"triboard-tc275_sieve_intmem\taskc\sieve+0x3E	0.030us

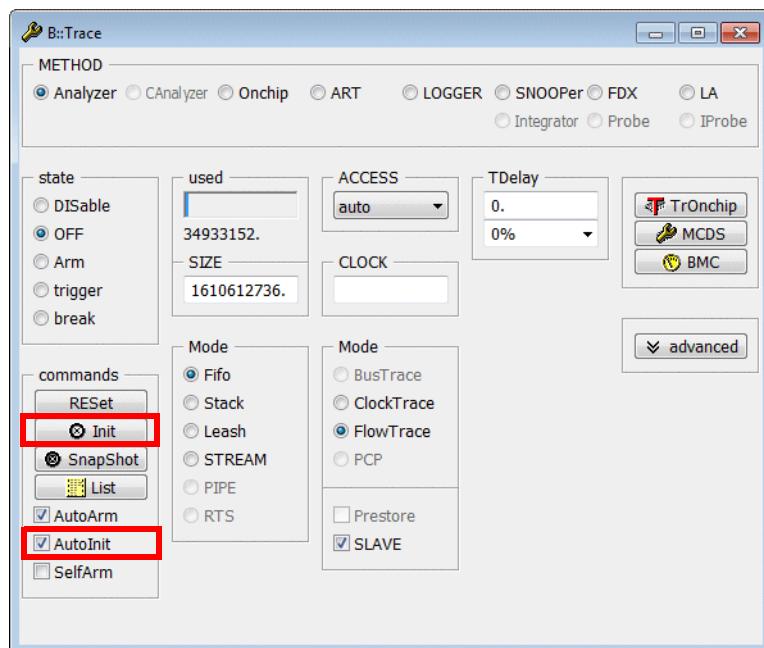
STREAM mode can generate very large record numbers

States of the Trace

The trace buffer can either record trace information or allows the read-out for information display.



States of the Trace	
DISable	The trace is disabled.
OFF	The trace is not recording. The trace contents can be displayed.
Arm	The trace is recording. The trace contents can not be displayed.



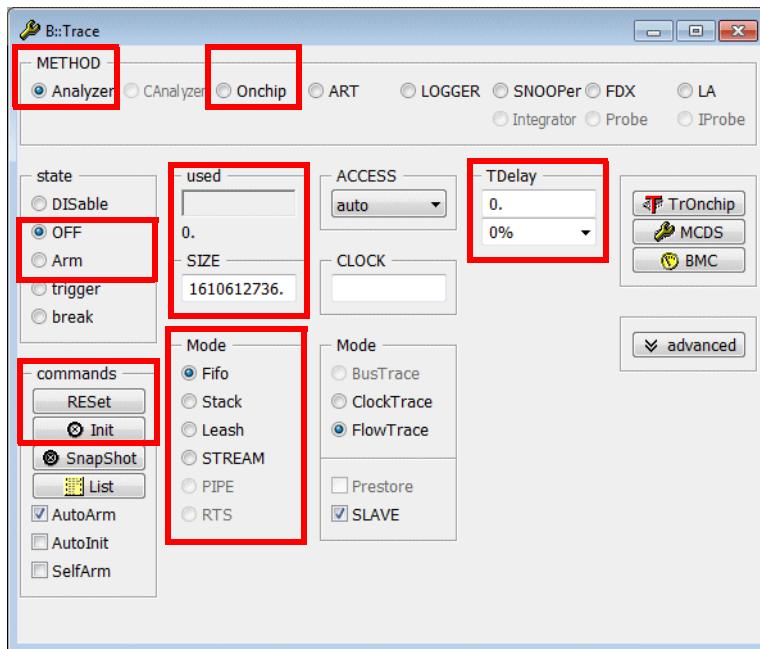
Init Button	Clear the trace memory. All other settings in the Trace Configuration window remain valid.
AutoInit CheckBox	<p>ON: The trace memory is cleared whenever the program execution is started (Go, Step).</p> <p>Please be aware that the onchip trace memory always start recording at the lowest address. As a result the AutoInit option is not required.</p>

Each TRACE32 instance has its own Trace Configuration window.

Since more than one TRACE32 instance can configure the trace (single source) the following rules apply:

1. Joint settings

The TRACE32 Resource Management maintains consistency between the TRACE32 instances.



Selection of the trace sink

Trace.METHOD Analyzer

Trace.METHOD Onchip

For the AURIX all cores can either use the on-chip trace or off-chip trace. Therefore, either the trace method Onchip or the trace method Analyzer has to be selected in all TRACE32 instances. As soon as an inconsistent selection is done in a TRACE32 instance, the TRACE32 Resource Management disables the traces in the other TRACE32 instances.

Trace state

Trace.OFF
Trace.Arm

Trace reset/init

Trace.RESet
Trace.Init

Trace size

Trace.SIZE *<size>*

Trace mode

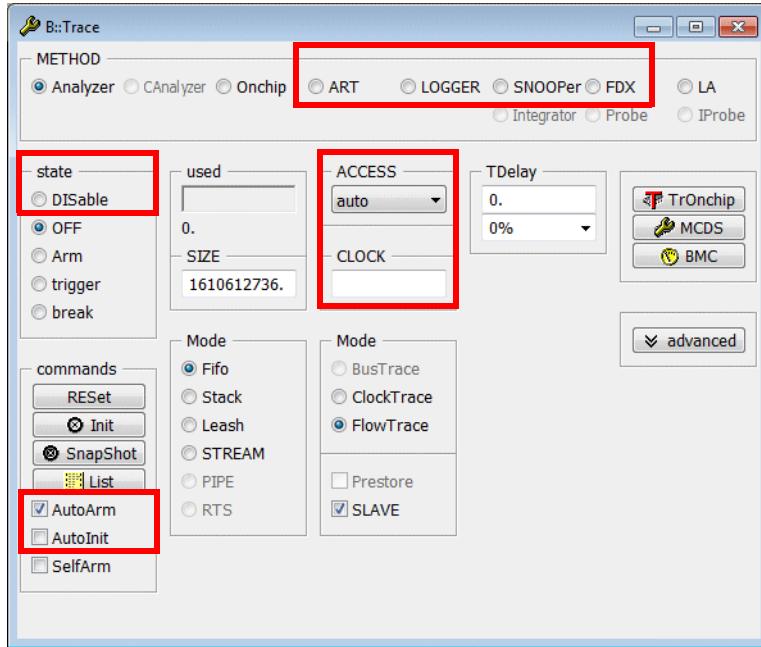
Trace.Mode Fifo | Stack | Leash | STREAM

Trigger delay

Trace.TDelay *<delay>*

2. Exclusive settings

These settings can be done by each TRACE32 instance individually.



Trace method

All TRACE32 software trace methods have their own resources in their TRACE32 instance.

Trace.Mode ART | LOGGER | SNOOPer | FDX

Trace disable

Trace.DISable

Trace AutoArm and AutoInit

Trace.AutoArm [ON | OFF]

Trace.AutoInit [ON | OFF]

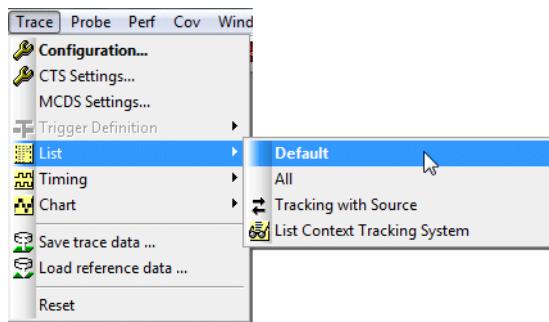
Access to source code/definition of the core clock

Trace.ACCESS <access>

Trace.CLOCK <core_clock>

Basic Display Commands

Default Listing



ptrace: Instruction Pointer Call Message/Instruction Pointer Message with timestamp

record	run	address	cycle	data	symbol	busmaster	ti.back
		mov16 d15,#0x12					
688		for (i = 0 ; i <= SIZE ; i++)					
-0000000172		jge d15,d0,0x7000140C ; i,0x7000140C			P:7000140C ptrace \\triboard-tc275_sieve_intmem\taskc\sieve+0x22	0.011us	
		if (flags[i])					
690		movh.a a15,#0x7000					
		lea a15,[a15]0x700					
690		{					
		if (flags[i])					
690		addsc.a a15,a15,d0,#0x0 ; a15,a15,i,#0					
		ld.b d15,[a15]0x0					
		{					
690		if (flags[i])					
		jz16 d15,0x70001444					
-0000000170		P:70001444 ptrace \\triboard-tc275_sieve_intmem\taskc\sieve+0x5A			0.008us		
688		for (i = 0 ; i <= SIZE ; i++)					
		add16 d0,#0x1 ; i,#1					
688		for (i = 0 ; i <= SIZE ; i++)					
		mov16 d15,#0x12					
688		for (i = 0 ; i <= SIZE ; i++)					
		jge d15,d0,0x7000140C ; d15,i,0x7000140C					
		}					
703		return anzahl;					
-0000000168		j16 0x7000144E			P:7000144E ptrace \\triboard-tc275_sieve_intmem\taskc\sieve+0x64	0.011us	
+*****							

Conditional
branch
not taken
(pastel)

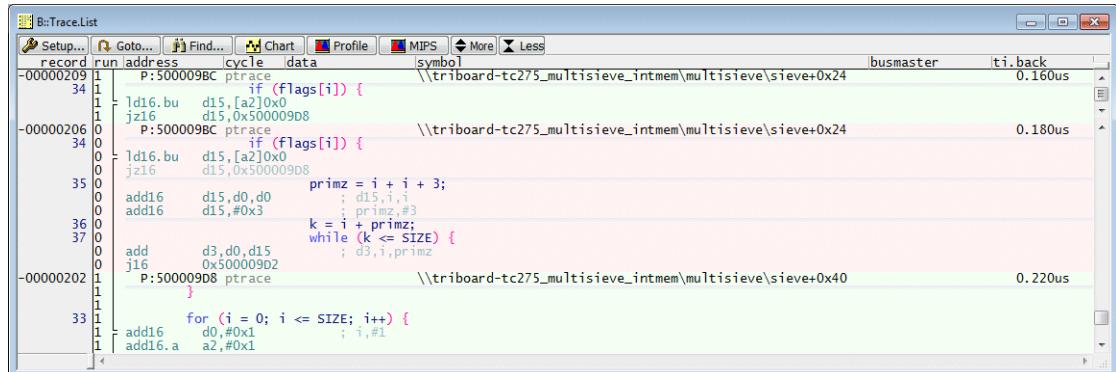
Conditional
branch taken

rd-data: Read Data Trace Message with timestamp (read-address only)

record	run	address	cycle	data	symbol	busmaster	ti.back
195		for regvar = 0; regvar < 5 ; regvar++)					
+000000178		i16 0x0000062					
+00000184		D:70000008 rd-data \\triboard-tc275_sieve_intmem\taskc\func2\fstatic			0.020us		
+00000195		D:70000008 wr-data 0742FE81 \\triboard-tc275_sieve_intmem\taskc\func2\fstatic			0.020us		
		P:70000062 ptrace \\triboard-tc275_sieve_intmem\taskc\func2+0x32			0.020us		
195		for regvar = 0; regvar < 5 ; regvar++)					
+00000198		jlt d15,#0x5,0x7000052 ; regvar, #5,0x7000052			P:70000052 ptrace \\triboard-tc275_sieve_intmem\taskc\func2+0x22	0.100us	

wr-data: Write Data Trace Message with timestamp

The trace information for all cores is displayed by default in the Trace.List window if you are working with an **SMP system**. The column run and the coloring of the trace information are used for core indication.

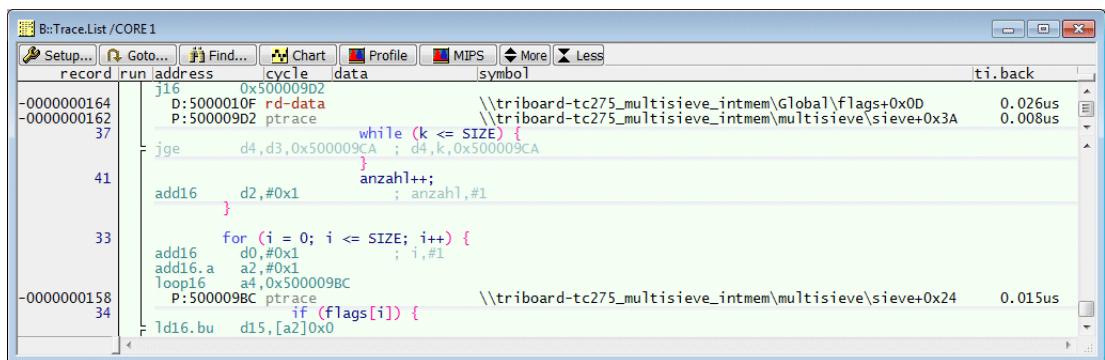


The screenshot shows the Trace.List window with the title 'B::Trace.List'. The window has a menu bar with 'Setup...', 'Goto...', 'Find...', 'Chart', 'Profile', 'MIPS', 'More', and 'Less'. The main table has columns: record, run, address, cycle, data, symbol, busmaster, and ti.back. The data is color-coded by core: green for core 0, pink for core 1, and light blue for core 2. The table shows three entries from different cores, with core 0 being the most active.

record	run	address	cycle	data	symbol	busmaster	ti.back
-00000209	1	P:500009BC	ptrace		\\triboard-tc275_multisieve_intmem\multisieve\sieve+0x24		0.160us
34	1	1d16.bu	d15,[a2]0x0				
1	1	1d16.bu	d15,0x50000908		\\triboard-tc275_multisieve_intmem\multisieve\sieve+0x24		0.180us
-00000206	34	0	1d16.bu	d15,[a2]0x0			
0	0	1d16.bu	d15,0x50000908				
35	0	1d16.bu	d15,[a2]0x0		primz = i + i + 3;		
0	0	add16	d15,d0,d0		; d15,i,i		
36	0	add16	d15,#0x3		; primz,#3		
37	0	add	d3,d0,d15		k = i + primz;		
0	0	116	0x50000902		while (k <= SIZE) {		
-00000202	1	P:50000908	ptrace		; d3,i,primz		0.220us
33	1	1d16.bu	d15,[a2]0x0		}		
1	1	1d16.a	a2,#0x1		for (i = 0; i <= SIZE; i++) {		
1	1	1d16.a	a2,#0x1		add16		

Trace.List /CORE<n>

The commands allows a per core display



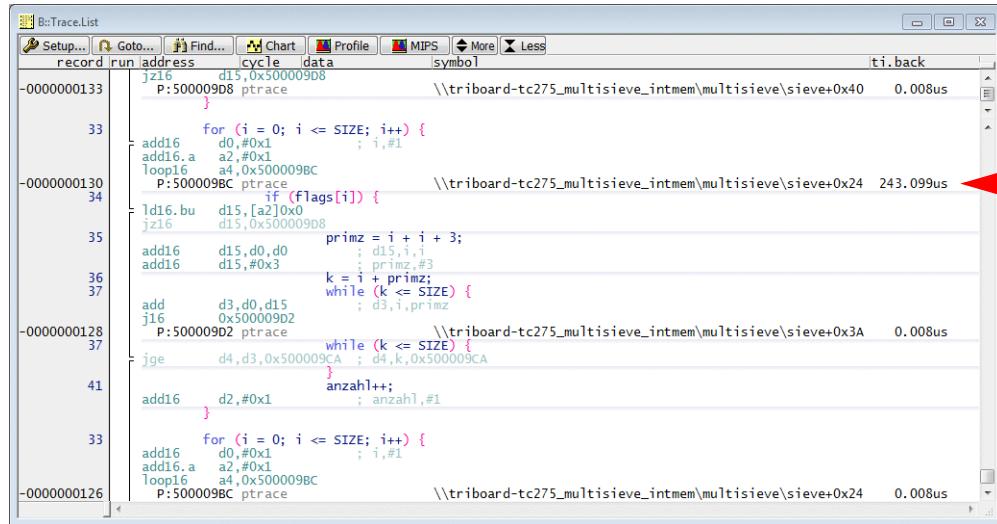
The screenshot shows the Trace.List window with the title 'B::Trace.List /CORE 1'. The window has a menu bar with 'Setup...', 'Goto...', 'Find...', 'Chart', 'Profile', 'MIPS', 'More', and 'Less'. The main table has columns: record, run, address, cycle, data, symbol, and ti.back. The data is color-coded by core: green for core 0, pink for core 1, and light blue for core 2. The table shows trace information for core 1, with core 0 being the most active.

record	run	address	cycle	data	symbol	ti.back
-0000000164	1	116	0x500009D2			0.026us
-0000000162	37	D:5000010F	rd-data		\\triboard-tc275_multisieve_intmem\Global\flags+0x0D	0.008us
37	1	P:500009D2	ptrace		\\triboard-tc275_multisieve_intmem\multisieve\sieve+0x3A	
37	1	1d16.bu	d4,d3,0x500009CA		while (k <= SIZE) {	
41	1	jge	d4,d3,0x500009CA		}; d4,k,0x500009CA	
41	1	add16	d2,#0x1		anzahl++;	
41	1				; anzahl, #1	
33	1	1d16.bu	d15,[a2]0x0		for (i = 0; i <= SIZE; i++) {	
33	1	add16	d0,#0x1		add16	
33	1	add16.a	a2,#0x1		d0,#0x1	
33	1	loop16	a4,0x500009BC		; i,#1	
-0000000158	34	P:500009BC	ptrace		\\triboard-tc275_multisieve_intmem\multisieve\sieve+0x24	0.015us
34	1	1d16.bu	d15,[a2]0x0		if (flags[i]) {	

Trace.List DEFault BusMaster

record	run	address	cycle	data	symbol	ti.back	busmaster
-0000000574		D:500000B0	rd-sr1	900040FC126010C2	\\triboard-d-tc275_multisieve_intmem\\multisieve\\sieve1+0x42	0.056us	CPU1 PMI
-0000000560		D:500000B8	rd-sr1	203BF50000910282	\\triboard-d-tc275_multisieve_intmem\\multisieve\\sieve2	0.057us	CPU1 PMI
-0000000546		D:500009B8	rd-sr1	00662F144001203B	\\triboard-d-tc275_multisieve_intmem\\multisieve\\sieve+0x20	0.052us	CPU2 PMI
-0000000532		D:500009A0	rd-sr1	04C5F24104002FFD9	\\triboard-d-tc275_multisieve_intmem\\multisieve\\sieve+0x8	0.057us	CPU2 PMI
-0000000518		D:500009A8	rd-sr1	00824FFC2F240012	\\triboard-d-tc275_multisieve_intmem\\multisieve\\sieve+0x10	0.057us	CPU2 PMI
-0000000504		D:500009B0	rd-sr1	001204C50102F240	\\triboard-d-tc275_multisieve_intmem\\multisieve\\sieve+0x18	0.059us	CPU2 PMI
-0000000490		D:500009C0	rd-sr1	3000F00B3FC2001A	\\triboard-d-tc275_multisieve_intmem\\multisieve\\sieve+0x28	0.052us	CPU0 PMI
-0000000476		D:500009C8	rd-sr1	F3425600F301053C	\\triboard-d-tc275_multisieve_intmem\\multisieve\\sieve+0x30	0.057us	CPU0 PMI
-0000000462		D:500009D0	rd-sr1	12C27FFC34/F5134	\\triboard-d-tc275_multisieve_intmem\\multisieve\\sieve+0x38	0.057us	CPU0 PMI

Off-chip trace: Please be aware that the AURIX chip buffers a bigger number of trace messages before they are sent out together via the serial interface. Since the TRACE32 timestamps the trace information when it is saved into the trace memory of the PowerTrace, the timestamps are imprecise and not suitable especially to measure the runtime of short program sections.



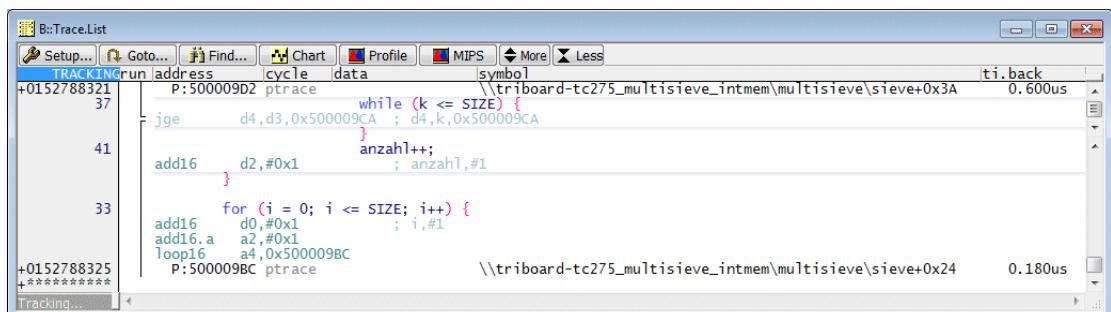
A red arrow points to the timestamp '243.099us' in the fourth record, which corresponds to the start of the serial trace.

A big timestamp indicates the point when the serial trace started to stream trace information

record	run	address	cycle	data	symbol	ti.back
-0000000133		jz16 d15,0x50000908			\triboard-tc275_multisieve_intmem\multisieve\sieve+0x40	0.008us
33		P:500009D8 ptrace				
		}				
		for (i = 0; i < SIZE; i++) {				
		add16 d0,#0x1				
		add16.a a2,#0x1				
		loop16 a4,0x500009BC				
-0000000130	34	P:500009BC ptrace			\triboard-tc275_multisieve_intmem\multisieve\sieve+0x24	243.099us
		if (flags[i]) {				
		1d16.bu d15,[a2]0x0				
		jz16 d15,0x50000908				
	35					
		primz = i + i + 3;				
		add16 d15,d0,d0				
	36	add16 d15,#0x3				
	37					
		k = i + primz;				
		while (k <= SIZE) {				
		add d3,d0,d15				
-0000000128	37	j16 P:500009D2 ptrace			\triboard-tc275_multisieve_intmem\multisieve\sieve+0x3A	0.008us
		while (k <= SIZE) {				
		jge d4,d3,0x500009CA ; d4,k,0x500009CA				
	41					
		anzahl++;				
		add16 d2,#0x1				
	33					
		for (i = 0; i < SIZE; i++) {				
		add16 d0,#0x1				
		add16.a a2,#0x1				
		loop16 a4,0x500009BC				
-0000000126		P:500009BC ptrace			\triboard-tc275_multisieve_intmem\multisieve\sieve+0x24	0.008us

If exact timestamps are important for your trace analysis, you have to enable Timestamp Messages. Please refer to “[2. Enable MCDS timestamp messages](#)”, page 27 for details.

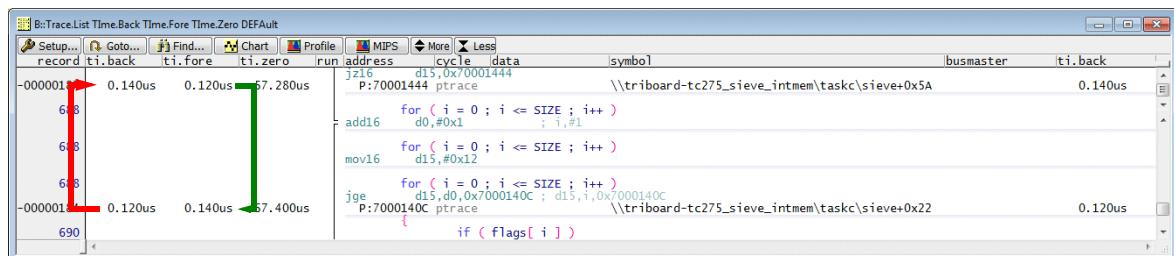
Enabling the Timestamp Messages has the caveat that the display of the trace information might need some time because TRACE32 has to process the trace information always from the start of the trace recording. As long as TRACE32 processes the trace information “Tracking” is displayed.



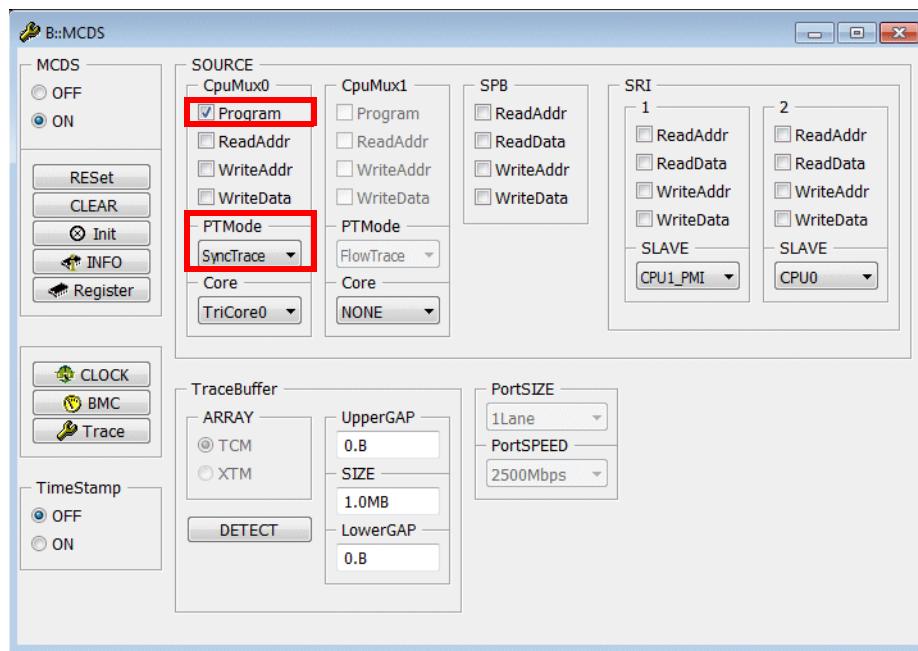
record	run	address	cycle	data	symbol	ti.back
+0152788321	37	P:500009D2 ptrace			\triboard-tc275_multisieve_intmem\multisieve\sieve+0x3A	0.600us
		while (k <= SIZE) {				
	41	jge d4,d3,0x500009CA ; d4,k,0x500009CA				
		anzahl++;				
	33	add16 d2,#0x1				
		anzahl, #1				
+0152788325		for (i = 0; i <= SIZE; i++) {				
		add16 d0,#0x1				
		add16.a a2,#0x1				
		loop16 a4,0x500009BC				
		P:500009BC ptrace			\triboard-tc275_multisieve_intmem\multisieve\sieve+0x24	0.180us

Time.BACK	Time relative to the previous record (left arrow in red)
Time.FORE	Time relative to the next record (right arrow in green).
Time.ZERO	<p>Timestamp Messages enabled: Time relative to the first record in the trace (zero point)</p> <p>PowerTrace/PREPROCESSOR SERIAL with Timestamp Messages disabled: Time relative to the TRACE32 global zero point</p> <p>The TRACE32 global zero point is established when the communication between the debugger and the master core is established by SYstem.Up.</p>

Trace.List TIme.BACK TIme.FORE TIme.ZERO DEFault



Information on the executed instructions is only generated on branches by default. The timestamps per instruction become more detailed, if Instruction Pointer Messages are enabled.



MCDS.SOURCE.Set CpuMux< n >.PTMode SyncTrace

But this reduces tracing time, since more trace packets are generated (about four times less).

Basic Formatting

1. `P:7000005215 ptrace 35D37498 \\\\triboard-tc275_sieve_intmem\\taskc\\mstatic1`

2. `ld16.w d0,[a1010x0]`

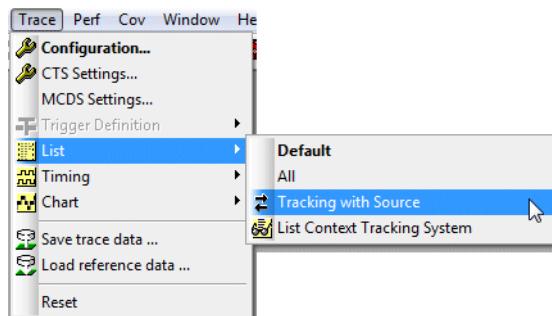
3. `D:70000030 wr-data 650398E2 \\\\triboard-tc275_sieve_intmem\\taskc\\mstatic1`

4. `for (regvar = 0; regvar < 5 ; regvar++)`

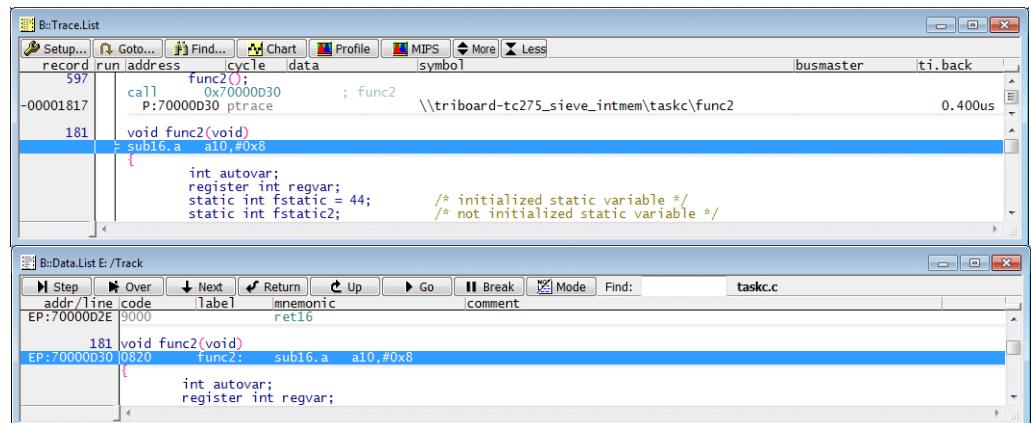
1. time Less	Suppress the display of the ptrace information (ptrace).
2. time Less	Suppress the display of the assembly code.
3. time Less	Suppress the data access information (e.g. wr-data cycles).

The **More** button works vice versa.

Correlating the Trace Listing with the Source Listing



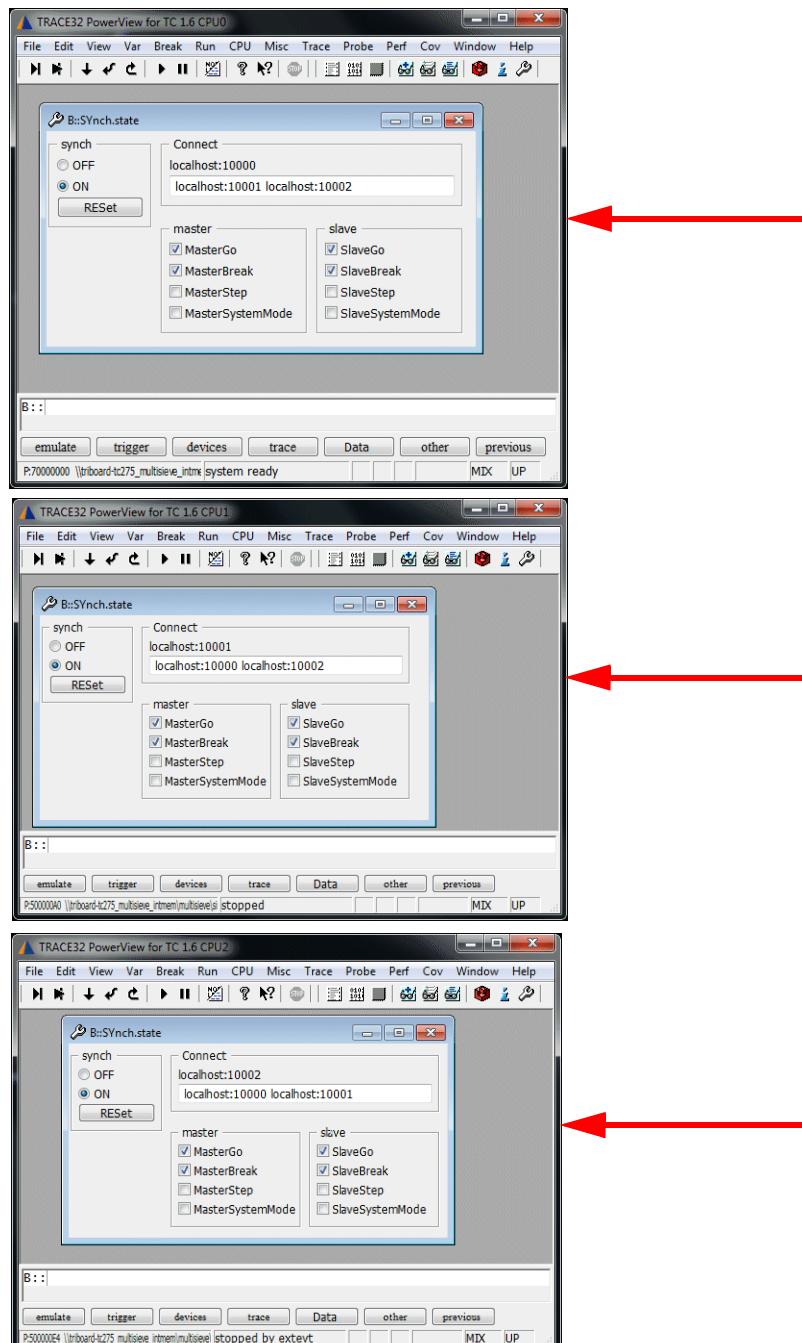
Active Window



All windows opened with the **/Track** option follow the cursor movements in the active window

Tracking between the Trace Listing and the Source Listing is based on the program address.

In an AMP configuration each TRACE32 instance displays the trace information for the core it controls. In order to analyze the interaction of the cores it is possible to establish a **Time Tracking** between the trace information in the different TRACE32 instances. Time Tracking between TRACE32 instances is established via the **SYnch.XTrack** command. If the start/stop synchronization for the cores is already established, establishing the **Time Tracking** is very simple.



MasterGo/MasterBreak
SlaveGo/SlaveBreak
are used to establish the
start/stop synchronization
in an AMP set-up

The following additionally settings are required to establish a **Time Tracking** between two or more TRACE32 instances:

SYnch.XTrack {<intercom_name>}	Establish time synchronization to another TRACE32 instance
---	--

```
; in TRACE32 instance for TC 1.6 CPU0
SYnch.XTrack localhost:10001 localhost:10002

; in TRACE32 instance for TC 1.6 CPU1
SYnch.XTrack localhost:10000 localhost:10002

; in TRACE32 instance for TC 1.6 CPU2
SYnch.XTrack localhost:10000 localhost:10001
```

Tracking points are:

Off-chip trace Timestamp Messages disabled	TRACE32 global zero time
On-chip trace Timestamp Messages disabled	Record number
Timestamp Messages enabled	AURIX zero time: Time relative to the first record in the trace

TRACE32 PowerView for TC 1.6 CPU0

File Edit View Var Break Run CPU Misc Trace Perf Cov TC2xT Window Help

B:TraceList %TimeFixed Time.Zero DEFault /Track

Setup... Goto... Find... Chart Profile MIPS More Less symbol

record	ti.zero	run	address	cycle	data	symbol	ti.back
35						for (i = 0; i <= SIZE; i++) {	
						mov16 d15,#0x12	
						}	
35						for (i = 0; i <= SIZE; i++) {	
-00555388	0.013554400s	P:70100DD2	ptrace			d15,d0,0x70100DD2 ; d15,i,0x70100DD2	\\\triboard-tc275_multisieve_intmem\multisieve\sieve+0x2C 0.000000260s
						if (flags[i]) {	
36						movh.a a15,#0x7000	
						lea a15,[a15]0xc2C	
36						if (flags[i]) {	
						addsc.a a15,a15,d0,#0x0 ; a15,a15,i,#0	
						ld.b d15,[a15]0x0	

B::

trigger devices trace Data Var List PERF SYSTEM Step Go Break symbol Frame other previous

C-T: -00555344 -15.501ms | C-Z: +13.554ms stopped MDX UP

TRACE32 PowerView for TC 1.6 CPU1

File Edit View Var Break Run CPU Misc Trace Perf Cov TC2xT Window Help

B:TraceList %TimeFixed Time.Zero DEFault /Track

Setup... Goto... Find... Chart Profile MIPS More Less symbol

record	ti.zero	run	address	cycle	data	symbol	ti.back
130						add d1,d0,d15	1 d1,i,primz
-00555350	0.013554080s	P:70100CAA	ptrace			while (k <= SIZE) {	
130						j16 0x70100CAA	
						while (k <= SIZE) {	
130						jge d3,d1,0x70100C98 ; d3,k,0x70100C98	
-00555342	0.013554220s	P:70100C98	ptrace			\\\\triboard-tc275_multisieve_intmem\multisieve\sieve1+0x52	0.000000540s
						flags1[k] = FALSE;	
131						movh.a a15,#0x0000	
						lea a15,[a15]0x18	
131						flags1[k] = FALSE;	

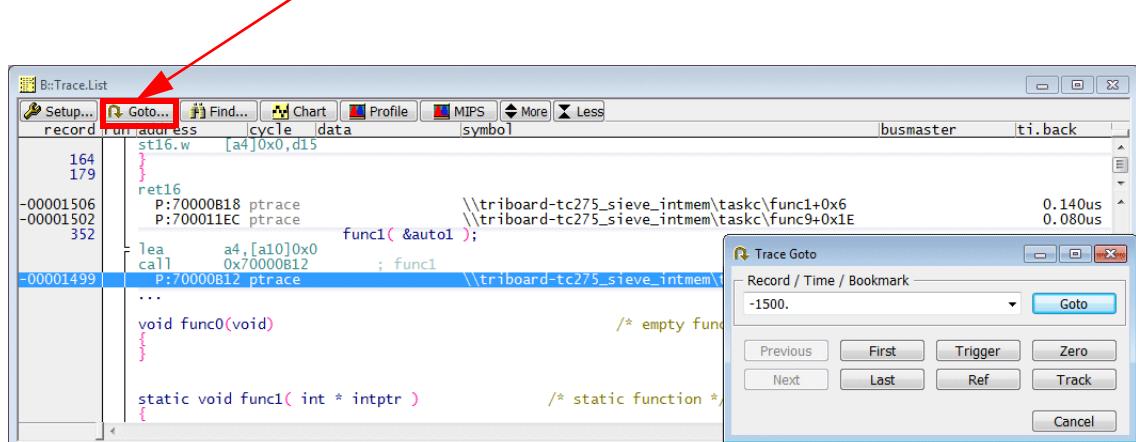
B::

trigger devices trace Data Var List PERF SYSTEM Step Go Break symbol Frame other previous

C-T: -00555342 -15.500ms | C-Z: +13.554ms stopped MDX UP

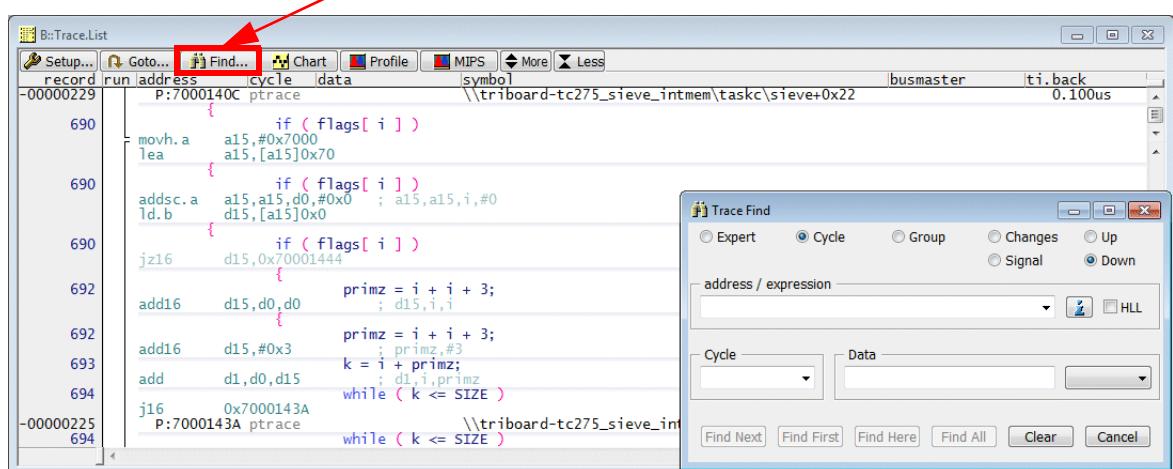
Time tracking between the Trace Listings of two TRACE32 instances

Browsing through the Trace Buffer

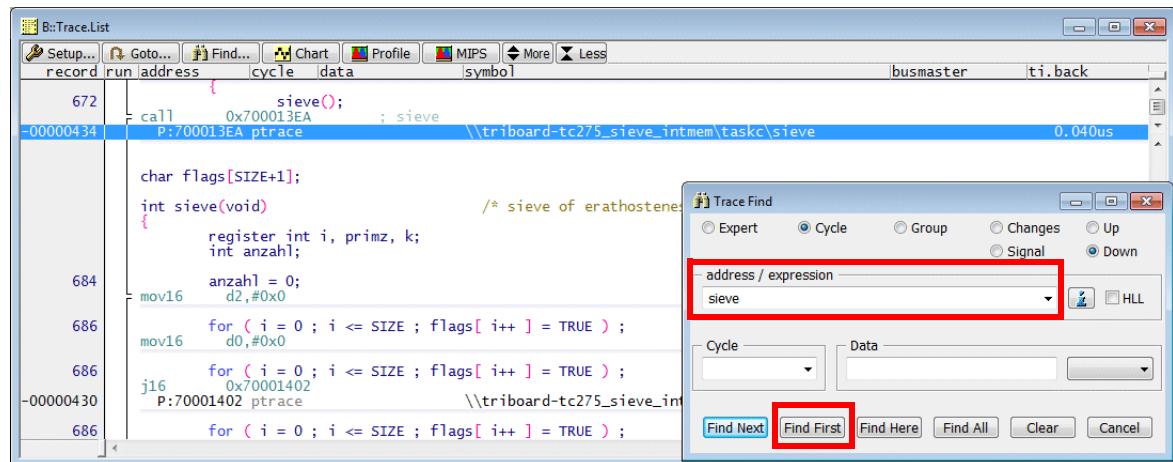


Pg ↑	Scroll page up.
Pg ↓	Scroll page down.
Ctrl - Pg ↑	Go to the first record sampled in the trace buffer.
Ctrl - Pg ↓	Go to the last record sampled in the trace buffer.

Find a Specific Event



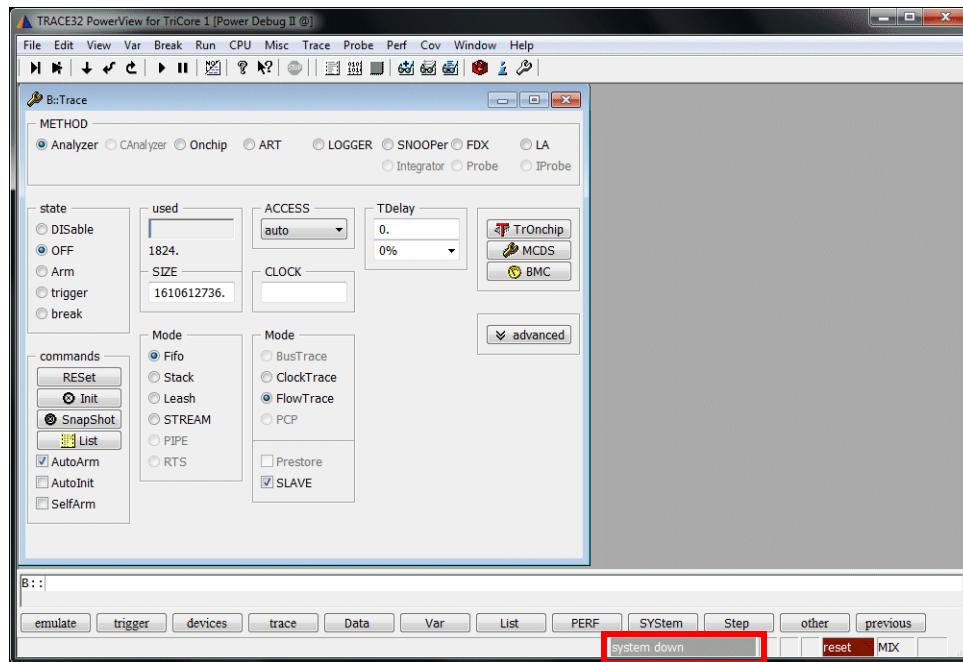
Example: Find a specific symbol address.



A more detail description on how to find specific events in the trace is given in "[Application Note for Trace.Find](#)" (app_trace_find.pdf).

Post Mortem Trace Analysis (PowerTrace only)

Trace decompression and display requires by default, that the program code is read from the target memory via JTAG/DAP. If the communication to the target is lost (system down) an alternative way to read the program code can be provided.



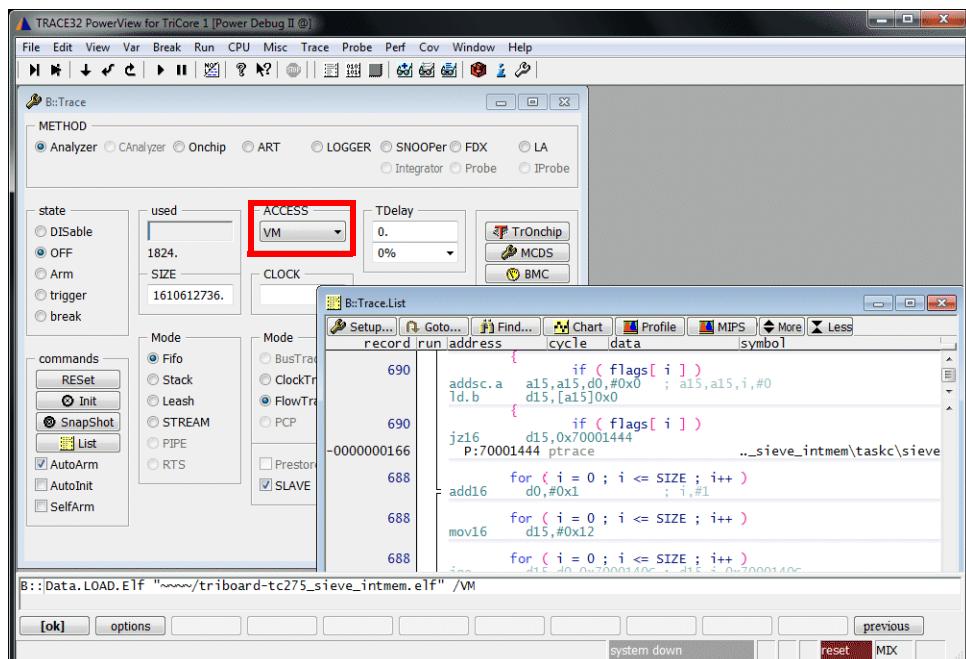
In order to decompress and display trace information after the communication to the target is lost proceed as follows:

1. Load the program code to the **TRACE32 Virtual Memory**.

```
Data.LOAD.Elf triboard-tc275_sieve_intmem.elf /VM
```

2. Advise TRACE32 to read program code from TRACE32 Virtual Memory.

```
Trace.ACCESS VM
```



Belated Trace Analysis

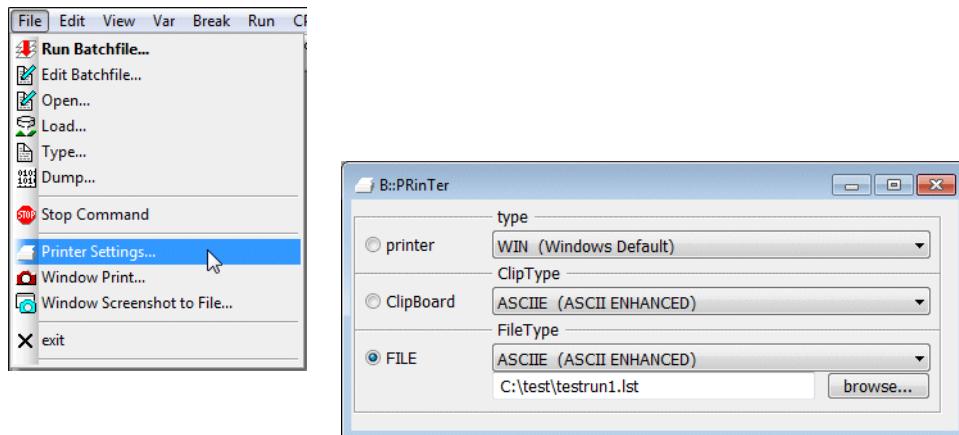
There are several ways for a belated trace analysis:

1. Save a part of the trace contents into an ASCII file and analyze this trace contents by reading.
2. Save the trace contents in a compact format into a file. Load the trace contents at a subsequent date into a TRACE32 Instruction Set Simulator and analyze it there.

Save the Trace Information to an ASCII File

Saving a part of the trace contents to an ASCII file requires the following steps:

1. Select **Printer Settings ...** in the **File** menu to specify the file name and the output format.



```
PRinTer.FileType ASCIIIE ; specify output format
                           ; here enhanced ASCII
PRinTer.FILE testrun1.lst ; specify the file name
```

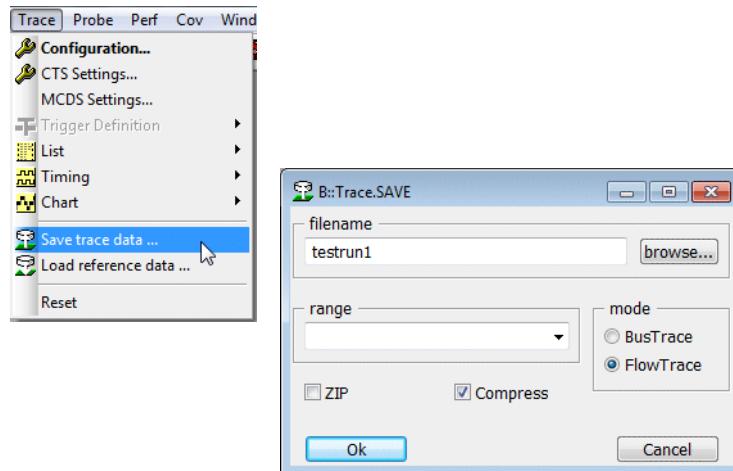
2. It only makes sense to save a part of the trace contents into an ASCII-file. Use the record numbers to specify the trace part you are interested in.

TRACE32 provides the command prefix **WinPrint.** to redirect the result of a display command into a file.

```
; save the trace record range (-8976.)--(-2418.) into the
; specified file
WinPrint.Trace.List (-8976.)--(-2418.)
```

3. Use an ASCII editor to display the result.

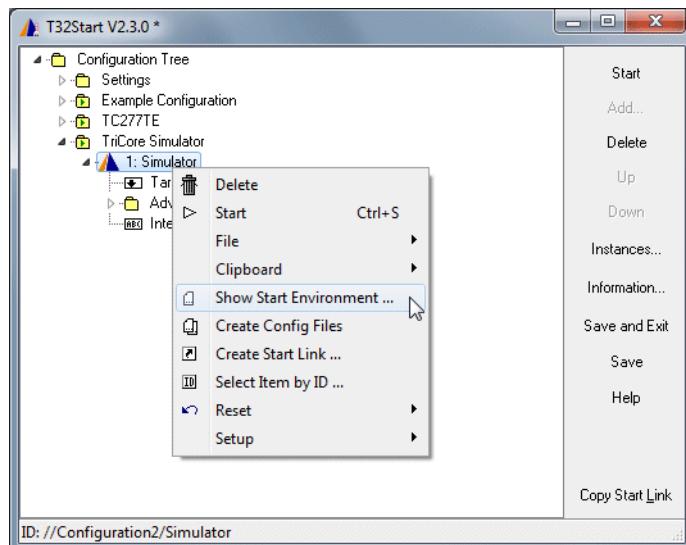
1. Save the contents of the trace memory into a file.



The default extension for the trace file is **.ad**.

```
Trace.SAVE testrun1.ad
```

2. Start a TRACE32 Instruction Set Simulator (PBI=SIM).



The screenshot shows the 'Start Environment Viewer - TriCore Simulator/Simulator' window. It displays a configuration file with the following content:

```
C:
cd C:\T32_TriCore\demo\tricore\hardware\triboard-tc2x7
C:\T32_TriCore\bin\windows64\t32mtc.exe -c C:\Users\amartin\AppData\Local\Temp\andT32_1000053.t32

T32 Configuration File C:\Users\amartin\AppData\Local\Temp\andT32_1000053.t32
;This configuration file is generated with T32Start2
;=====

;Environment Variables
OS=
ID=T32_1000053
TMP=C:\Users\amartin\AppData\Local\Temp
SYS=C:\T32_TriCore
HELP=C:\T32_TriCore\pdf

; Standard License File used

;T32 API Access
; not used

;T32 Intercom
IC=NETASSIST
PORT=10000
PACKLEN=1024

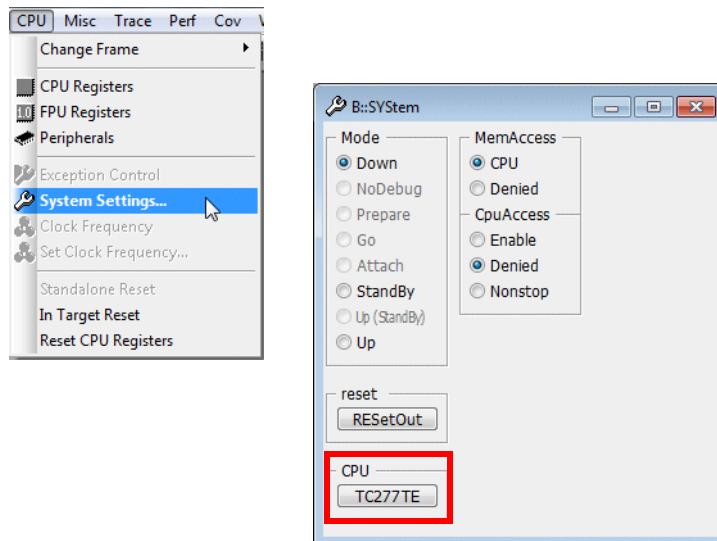
;T32 GDB
; not used

;Connection to Host
PBI=SIM

;Screen Settings
SCREEN=
```

At the bottom of the window, there are buttons for 'Edit History Settings...', 'Save Batch Job As...', 'Save Config As...', and 'Close'.

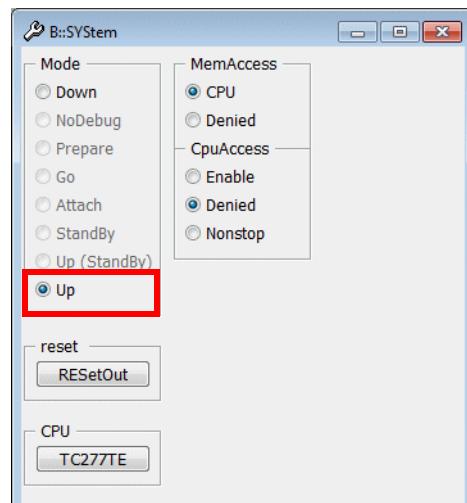
3. Select your target CPU within the simulator.



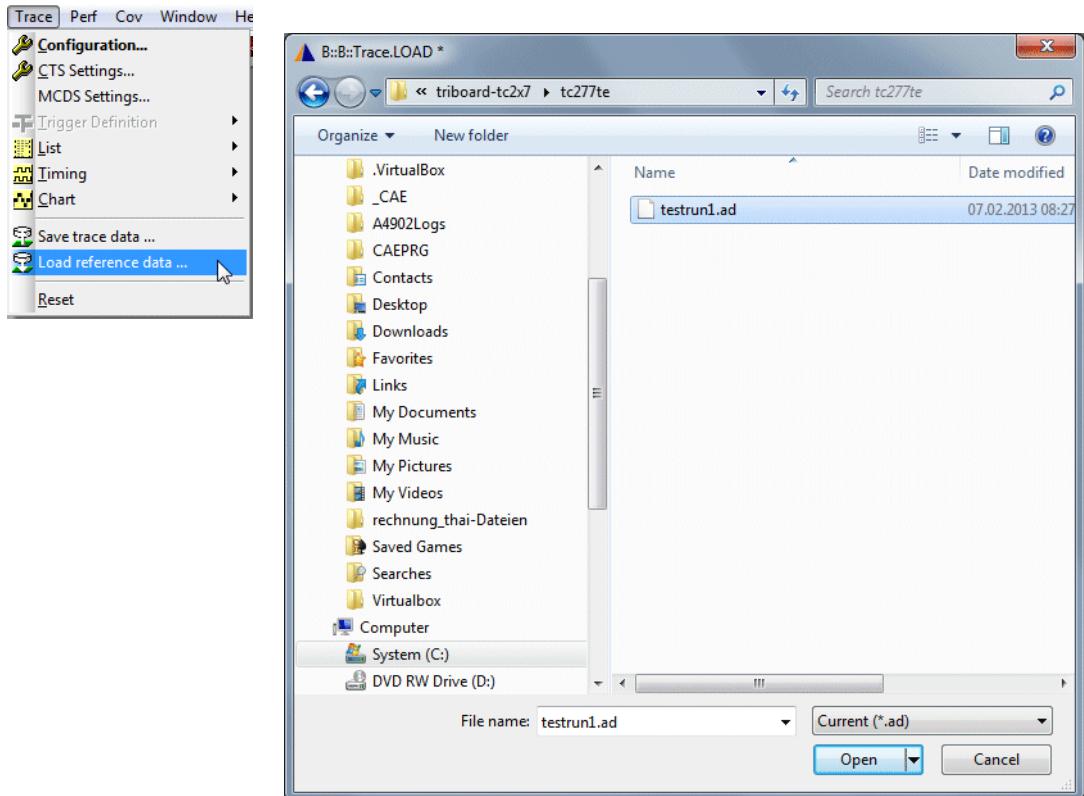
4. If you are debugging an SMP system, inform the simulator which cores form the SMP system.

```
CORE.ASSIGN 1. 2. 3.
```

5. Then establish the communication between TRACE32 and the simulator.

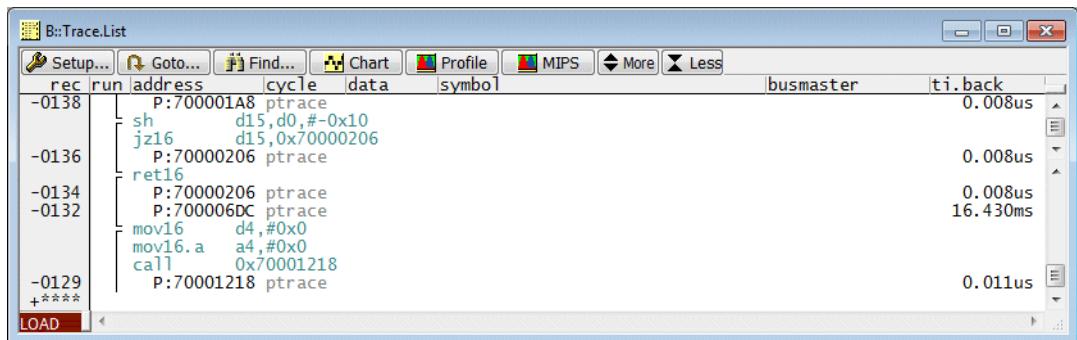


6. Load the trace file.



```
Trace.LOAD testrun1
```

7. Display the trace contents.



rec	run	address	cycle	data	symbol	busmaster	ti.back
-0138		P:700001A8	ptrace				0.008us
		sh	d15,d0,#-0x10				
		jz16	d15,0x70000206				
-0136		P:70000206	ptrace				0.008us
-0134		ret16					0.008us
-0132		P:70000206	ptrace				16.430ms
		P:700006DC	ptrace				
		mov16	d4,#0x0				
		mov16.a	a4,#0x0				
		call	0x70001218				
-0129		P:70001218	ptrace				0.011us
+***							
		LOAD					

LOAD indicates that the source for the trace information is the loaded file.

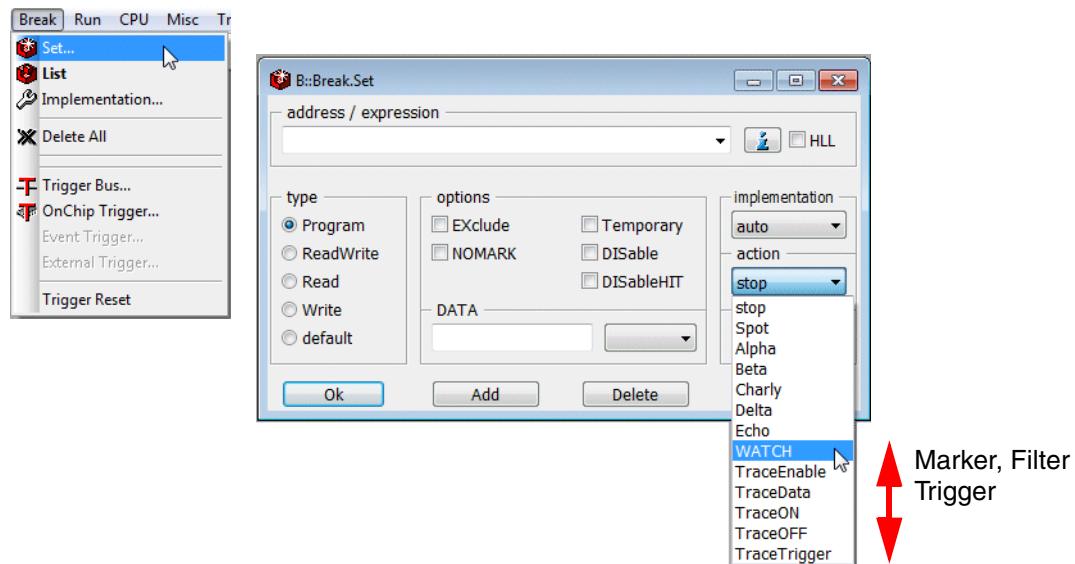
8. Load symbol and debug information if you need it.

```
Data.LOAD.Elf triboard-tc275_sieve_intmem.elf /NoCODE
```

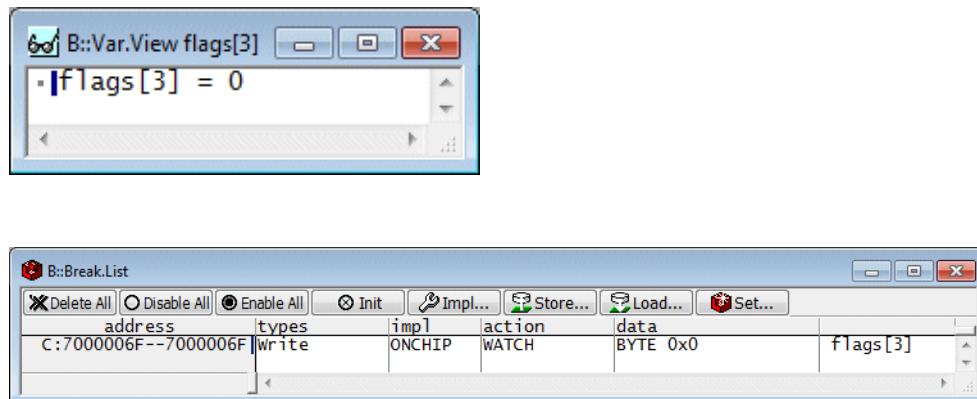
The TRACE32 Instruction Set Simulator provides the same trace display and analysis commands as the TRACE32 debugger.

Trace Control by Filter and Trigger - Overview

The **action** field in the Break.Set dialog provides Marker, Filter and Trigger.



Marker, filter and trigger get a **blue breakpoint indicator** within TRACE32.



Marker

WATCH is a so-called marker. It is used to indicate the occurrence of an event in the trace display.

Filter

A Processor Observation Block is the hardware within MCDS that generates trace messages out of the activities of a core. Filters are used to advise the Processor Observation Block to reduce the generation of trace messages to the information of interest. Please be aware, that Filters have no effect on the Bus Observation Blocks (SPB/SRI).

Filters are **TraceEnable**, **TraceData**, **TraceOn** and **TraceOFF**.

Trigger

TraceTrigger is a so-called trigger. Triggers are used to advise MCDS to stop the generation of trace messages.

Available Resources

The MCDS provides complex qualification- and trigger mechanism. TRACE32 uses these mechanisms as effectively as possible. Due to the complexity of the qualification and trigger mechanisms it is not possible to provide detailed numbers for the available resources.

Filter and Trigger - Single-Core and AMP

Fundamental behavior for AMP systems:

- Filters and Triggers are programmed for the core that is controlled by the TRACE32 instance.
- Filter advise the Processor Observation Block of the core controlled by the TRACE32 instance to generate the trace information of interest.
- Marker/Trigger advise the Processor Observation Block of the core controlled by the TRACE32 instance to indicate the occurrence of an event.

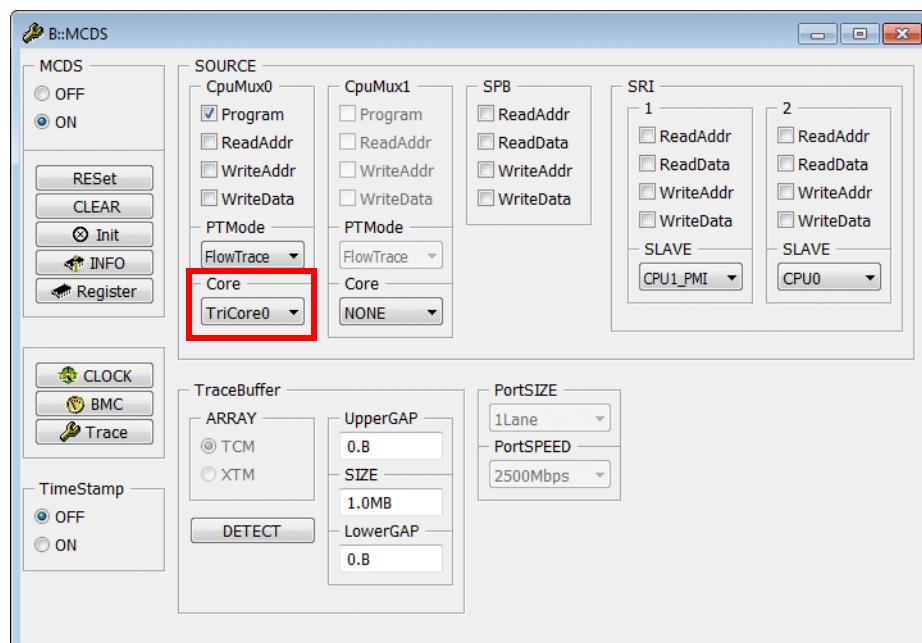
WATCH Marker

Advise Processor Observation Block to indicate the occurrence of an event.

Example: Indicate that 0x0 was written to the variable flags[3].

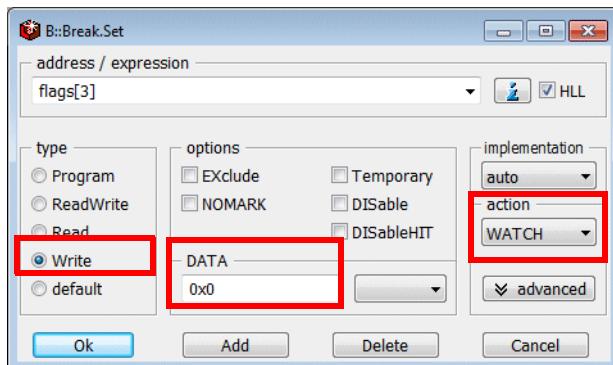
- **Core under debug:** TC 1.6.1 CPU0.
- **Event of interest:** Write of 0x0 to variable flags[3].
- **Requested messages:** Instruction Pointer Call Messages, Timestamp Messages.

1. Configure the trace multiplexer.



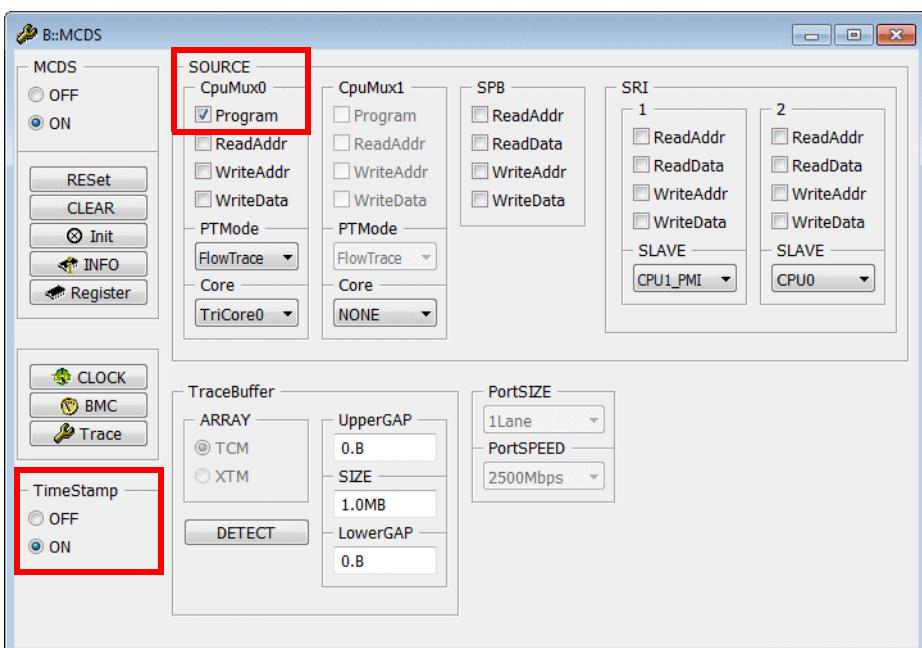
```
; enable TC 1.6.1 CPU0 as trace source
MCDS.SOURCE.Set CpuMux0.Core TriCore0
```

2. Specify the event.



```
Var.Break.Set flags[3] /Write /DATA.Byte 0x0 /WATCH
```

3. Configure which trace messages are generated.

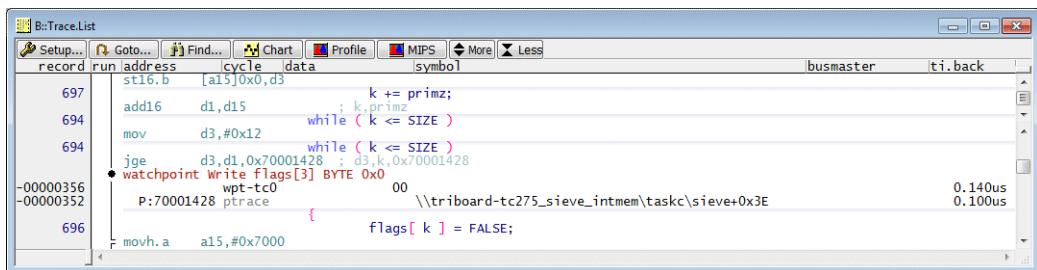
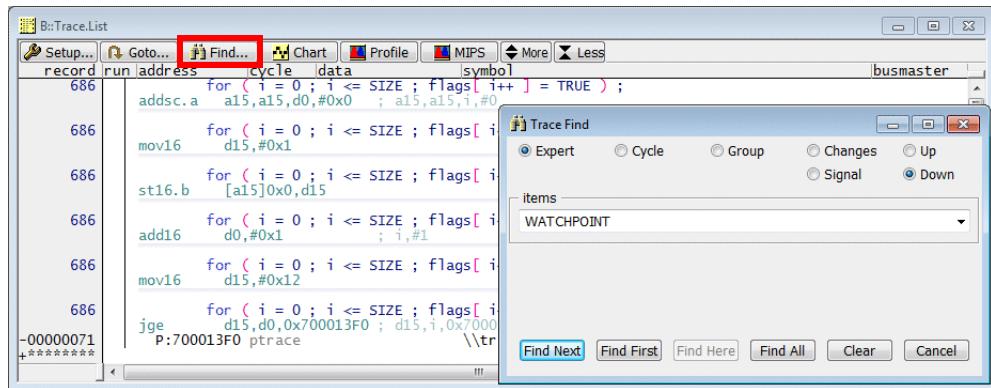


```
MCDS.TimeStamp ON ; enable Timestamp Messages  
CLOCK.ON  
MCDS.SOURCE.Set CpuMux0.Program ON ; enable Instruction Pointer  
; Call Messages for  
; TC 1.6.1 CPU0
```

4. Start and stop the program execution.

5. Display the result.

It might be necessary to search for the result.

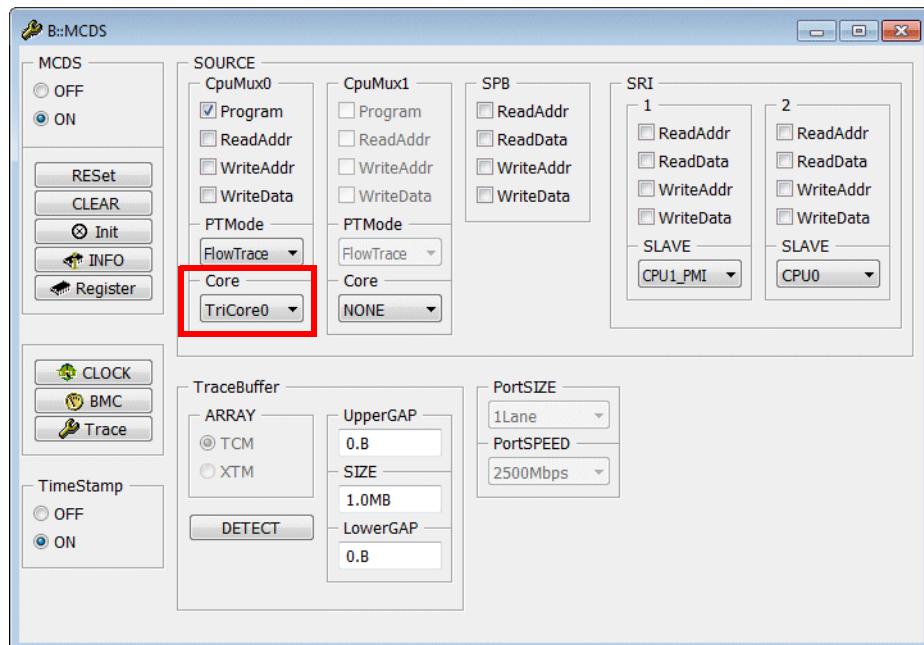


Advise the Processor Observation Block to generate trace messages for the enabled SOURCES when the specified event is true.

Example 1: Restrict the generated trace information to the entries to the function sieve.

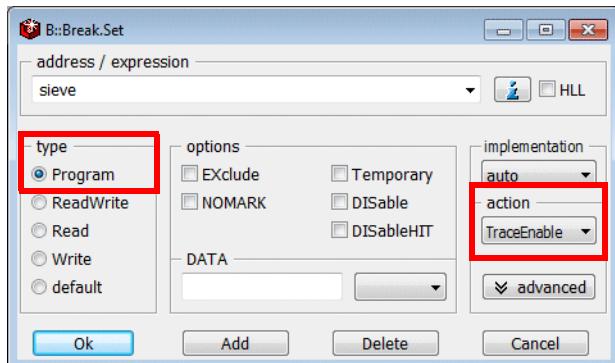
- **Core under debug:** TC 1.6.1 CPU0.
- **Event of interest:** Entry to function sieve.
- **Requested messages:** Instruction Pointer Call Messages, Timestamp Messages.

1. Configure the trace multiplexer.



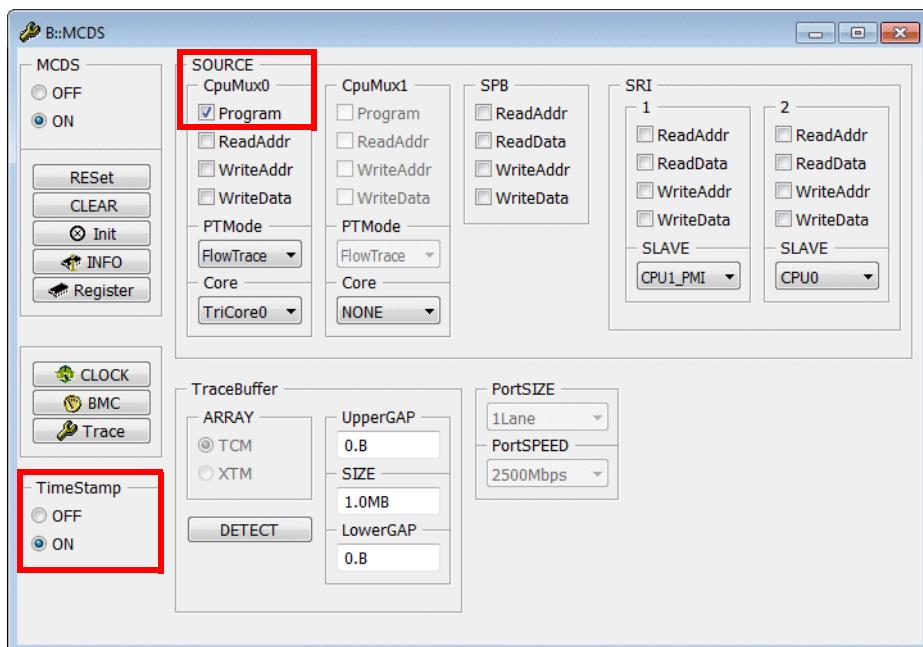
```
; enable TC 1.6.1 CPU0 as trace source
MCDS.SOURCE.Set CpuMux0.Core TriCore0
```

2. Specify the event.



```
Break.Set    sieve /Program /TraceEnable
```

3. Configure which trace messages are generated while the event is true.

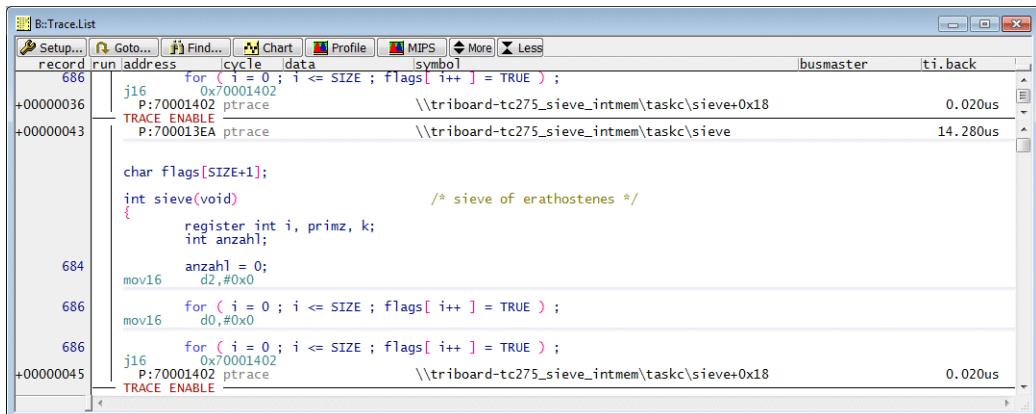


```
MCDS.TimeStamp ON ; enable Timestamp Messages  
CLOCK.ON  
MCDS.SOURCE.Set CpuMux0.Program ON ; enable Instruction Pointer  
; Call Messages for  
; TC 1.6.1 CPU0
```

4. Start the program execution and stop it.

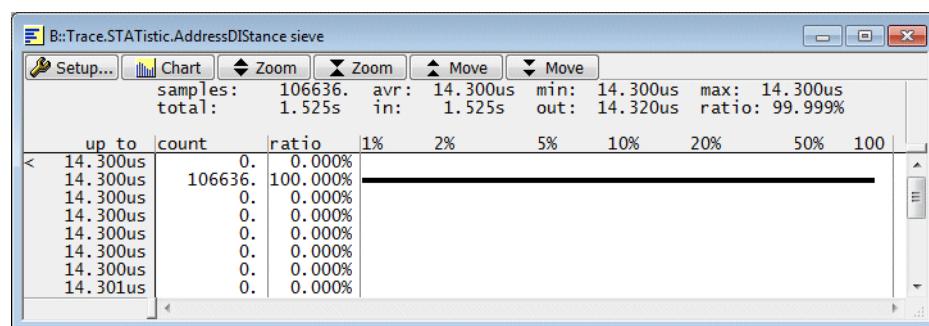
5. Display the result.

The trace contains only small code sections generated for the entries to the function sieve (TRACE ENABLE).



The following **Trace.STATistic** command calculates the time intervals for a program address event. The program address event is here the entry to the function sieve:

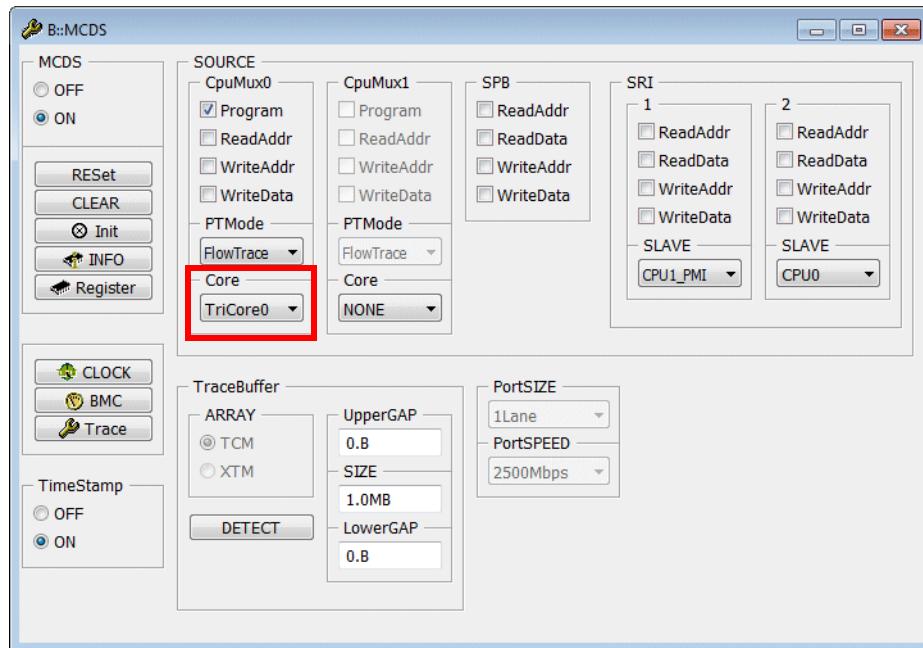
```
Trace.STATistic.AddressDISTance sieve
```



Example 2: Restrict the generated trace information to the entries to the function sieve and the exits from the function sieve.

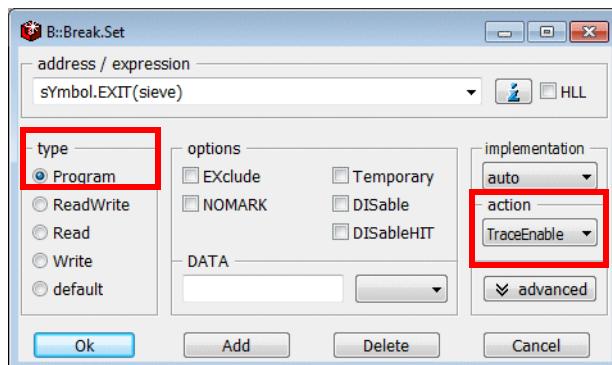
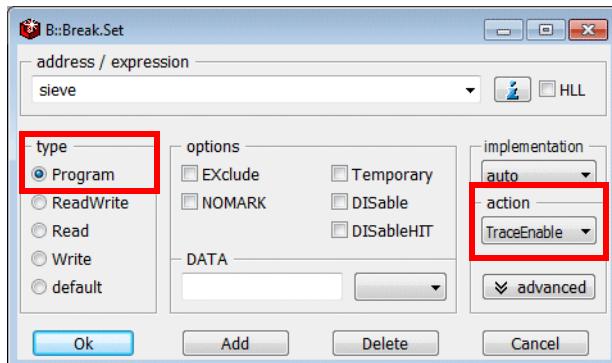
- **Core under debug:** TC 1.6.1 CPU0.
- **Events of interest:** Entry to function sieve and exit of function sieve
- **Requested messages:** Instruction Pointer Call Messages, Timestamp Messages.

1. Configure the trace multiplexer.



```
; enable TC 1.6.1 CPU0 as trace source
MCDS.SOURCE.Set CpuMux0.Core TriCore0
```

2. Specify the events.



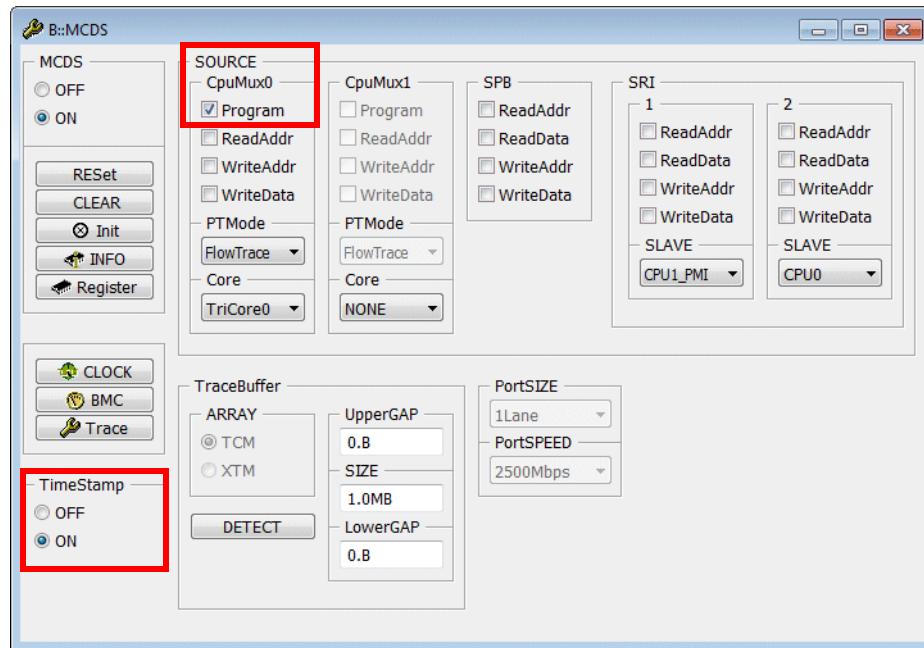
sYmbol.EXIT(<symbol>)

Returns the exit address of the specified function

```
Break.Set    sieve /Program /TraceEnable
```

```
Break.Set    sYmbol.EXIT(sieve) /Program /TraceEnable
```

3. Configure which trace messages are generated while the events are true.



```
MCDS.TimeStamp ON ; enable Timestamp Messages

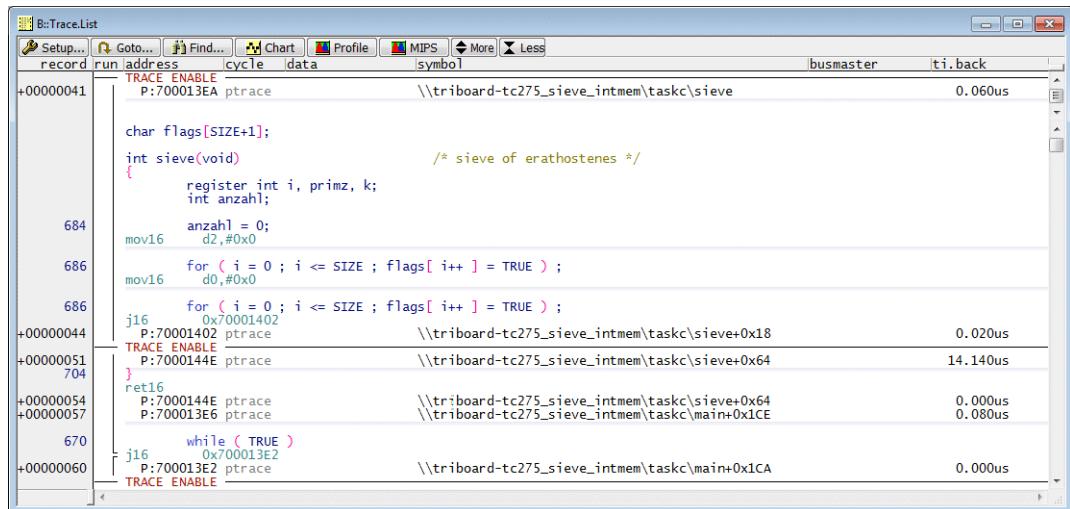
CLOCK.ON

MCDS.SOURCE.Set CpuMux0.Program ON ; enable Instruction Pointer
; Call Messages for
; TC 1.6.1 CPU0
```

4. Start the program execution and stop it.

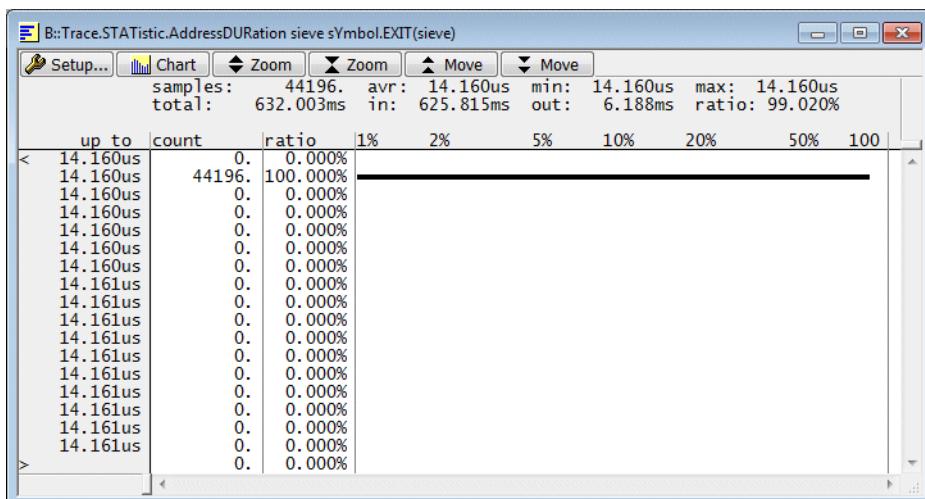
5. Display the result.

The trace contains only small code sections generated for the entries to the function sieve (TRACE ENABLE) and for the exits of the function sieve (TRACE ENABLE).



The following **Trace.STATistic** command calculates the time intervals between two program address events A and B. The entry to the function sieve is A in this example, the exit from the function is B.

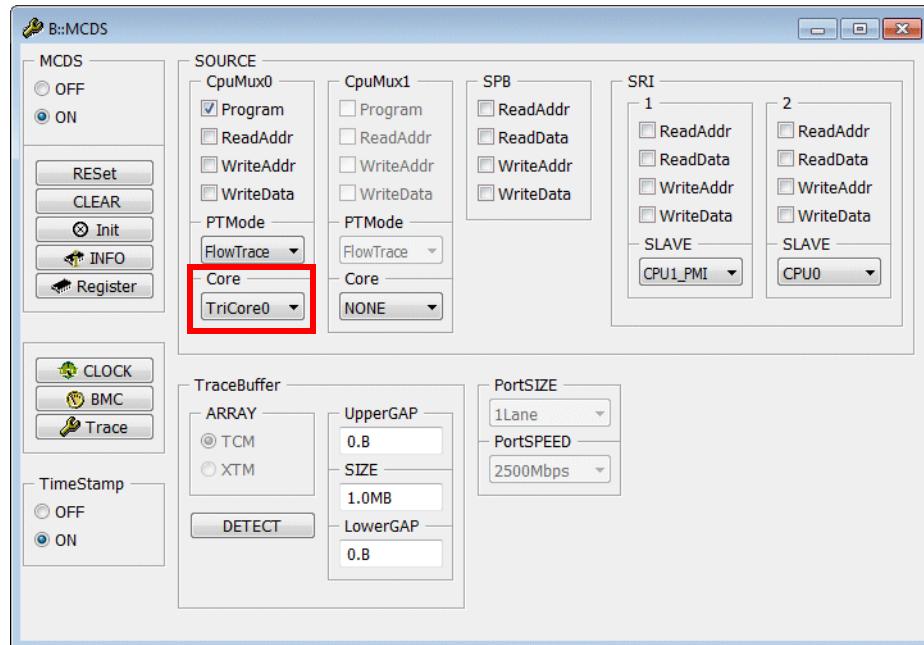
```
Trace.Statistic.AddressDuration sieve symbol.EXIT(sieve)
```



Example 3: Restrict the generated trace information to write accesses to the variable flags[3].

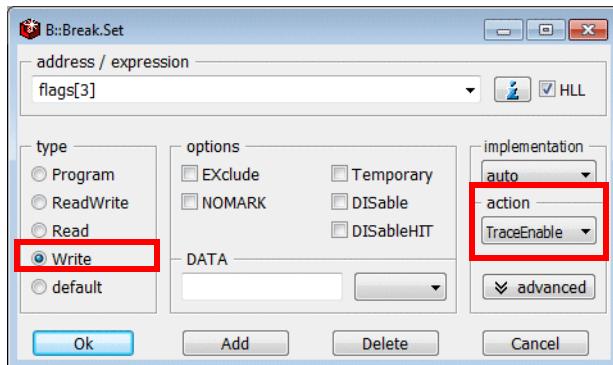
- **Core under debug:** TC 1.6.1 CPU0.
- **Event of interest:** Write access to variable flags[3].
- **Requested messages:** Write Data Trace Messages, Timestamp Messages.

1. Configure the trace multiplexer.



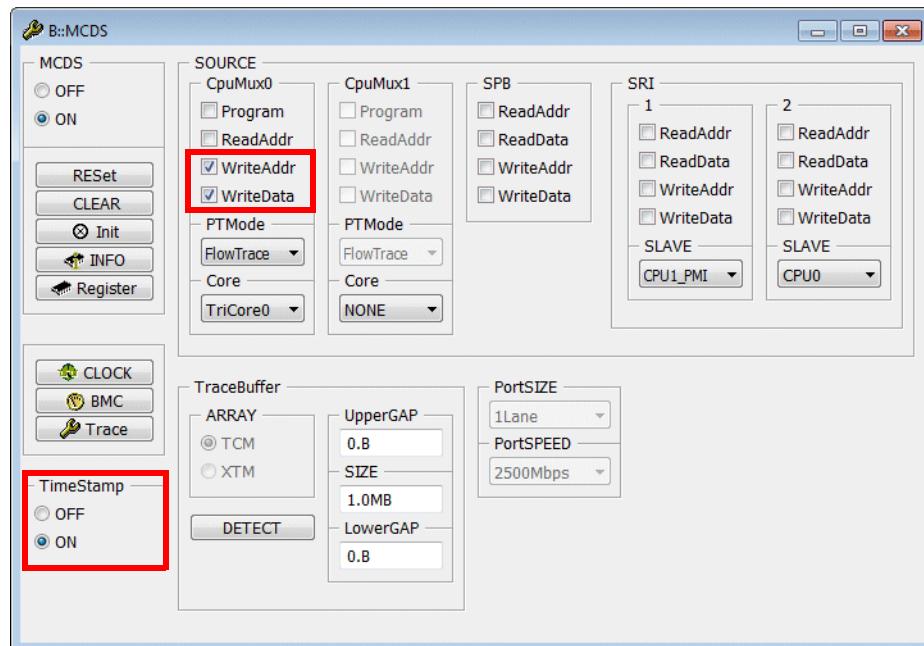
```
; enable TC 1.6.1 CPU0 as trace source
MCDS.SOURCE.Set CpuMux0.Core TriCore0
```

2. Specify the events.



```
Var.Break.Set flags[3] /Write /TraceEnable
```

3. Configure which trace messages are generated while the event is true.



```
MCDS.TimeStamp ON ; enable Timestamp Messages

CLOCK.ON

MCDS.SOURCE.Set CpuMux0.Program OFF ; disable Instruction Pointer
; Call Messages for
; TC 1.6.1 CPU0

MCDS.SOURCE.Set CpuMux0.WriteAddr ON ; enable Write Data Trace
; Messages for TC 1.6.1 CPU0

MCDS.SOURCE.Set CpuMux0.WriteData ON
```

4. Start the program execution and stop it.

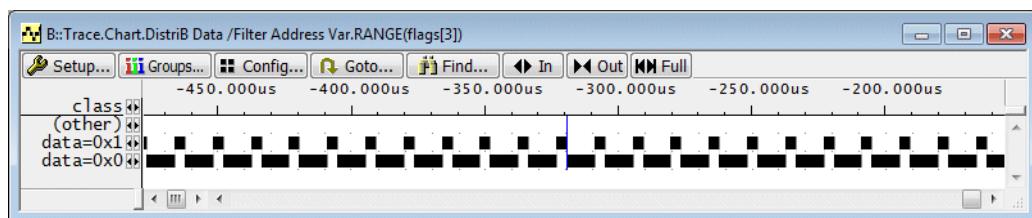
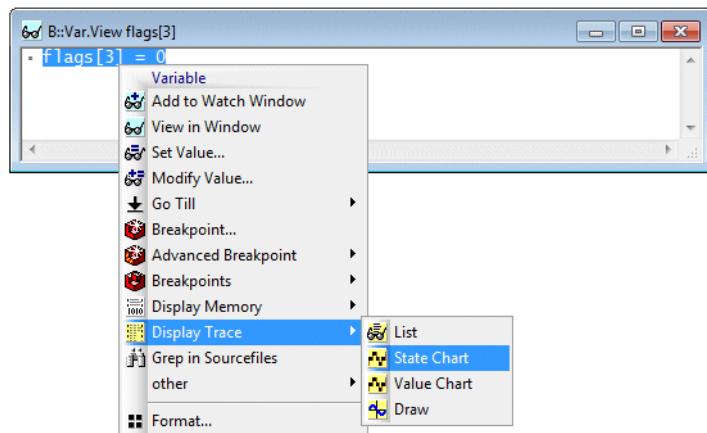
5. Display the result.

The trace contains only information on the write accesses to the variable flags[3].

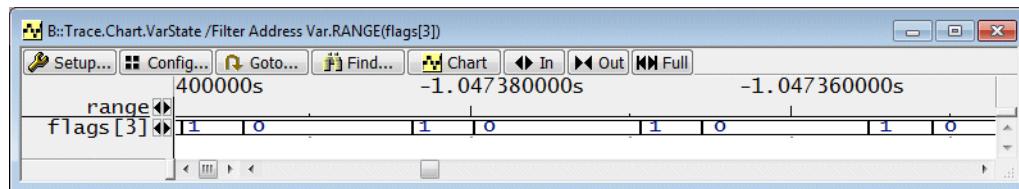
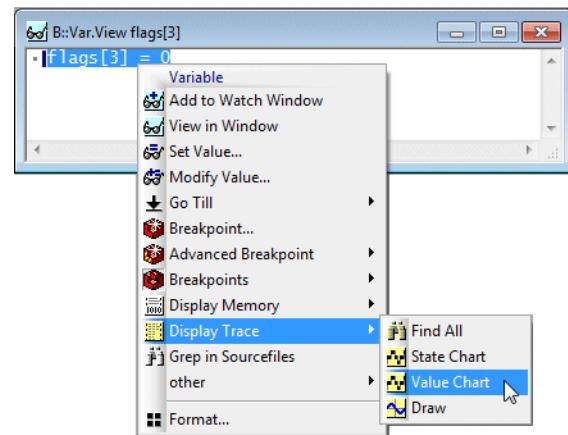
record	run	address	cycle	data	symbol	busmaster	ti.back
-00000124		D:70000073	wr-data	01	..._sieve_intmem\Global\flags+0x3		10.440us
-00000120		D:70000073	wr-data	00	..._sieve_intmem\Global\flags+0x3		3.860us
-00000113		D:70000073	wr-data	01	..._sieve_intmem\Global\flags+0x3		10.440us
-00000109		D:70000073	wr-data	00	..._sieve_intmem\Global\flags+0x3		3.860us
-00000102		D:70000073	wr-data	01	..._sieve_intmem\Global\flags+0x3		10.440us
-00000098		D:70000073	wr-data	00	..._sieve_intmem\Global\flags+0x3		3.860us
-00000091		D:70000073	wr-data	01	..._sieve_intmem\Global\flags+0x3		10.440us
-00000087		D:70000073	wr-data	00	..._sieve_intmem\Global\flags+0x3		3.860us
-00000080		D:70000073	wr-data	01	..._sieve_intmem\Global\flags+0x3		10.440us
-00000076		D:70000073	wr-data	00	..._sieve_intmem\Global\flags+0x3		3.860us

The Variable pull-down provides various way to analyze the variable contents over the time.

```
; open a window to display the variable
Var.View flags[3]
```



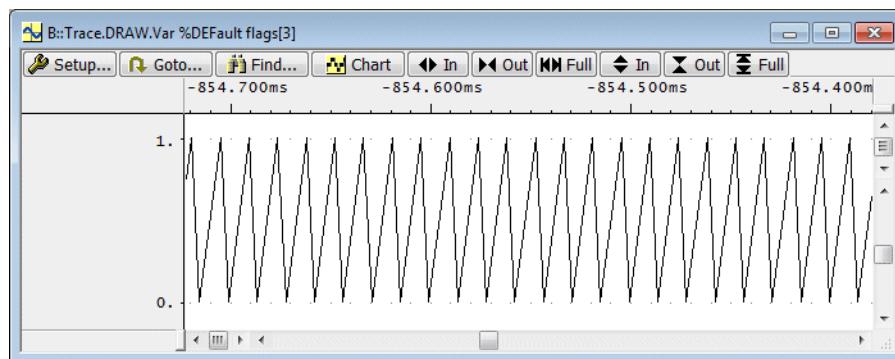
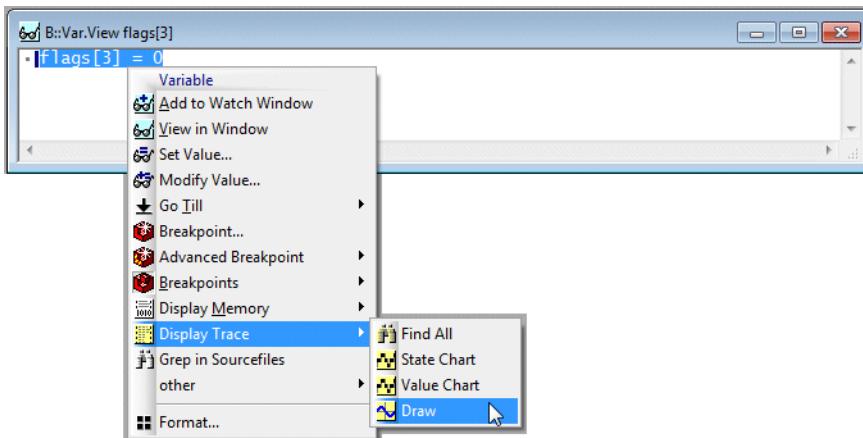
Display the value changes of a variable graphically
Trace.Chart.DistriB Data /Filter Address Var.RANGE(<var>)



Display variable contents over the time numerically
Trace.Chart.VarState /Filter Address Var.RANGE(<var>)

Var.RANGE(<var>)

Returns the address range in which the content of a variable is stored.



Display variable contents over the time graphically

Trace.DRAW.Var %DEFault <var>

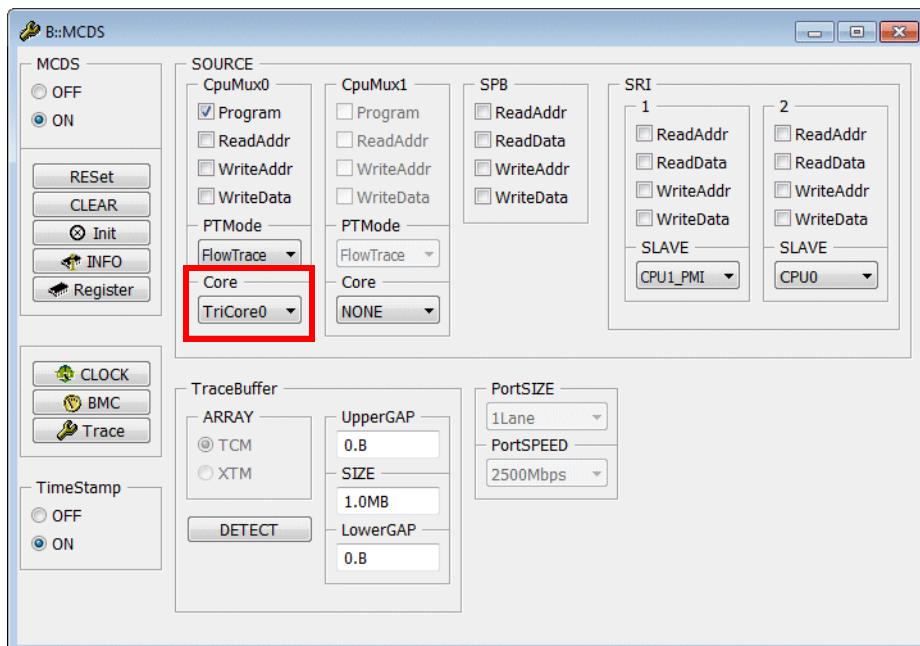
Advise the Processor Observation Block to generate trace messages for the instruction flow and for the specified events.

Motivation: The TraceData filter is of great importance for the nesting function run-time analysis if an operating system is used.

Example: Generate trace information for the complete instruction flow and for all write accesses to flags[12].

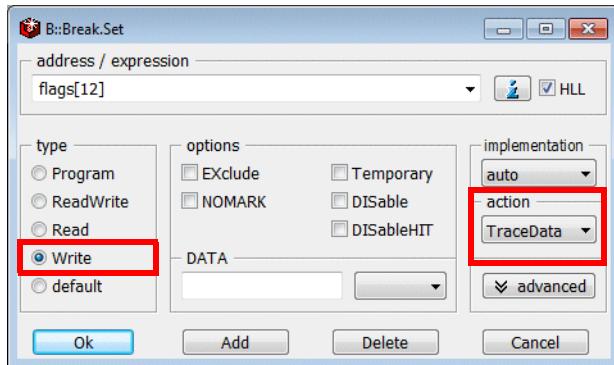
- **Core under debug:** TC 1.6.1 CPU0.
- **Event of interest:** Write access to flags[12].
- **Requested Messages:** Timestamp Messages.

1. Configure the trace multiplexer.



```
; enable TC 1.6.1 CPU0 as trace source
MCDS.SOURCE.Set CpuMux0.Core TriCore0
```

2. Specify the event.



```
Var.Break.Set flags[12] /Write /TraceData
```

3. TRACE32 PowerView takes care of the trace message generation.

4. Start the program execution and stop it.

5. Display the result.

The trace contains the complete program flow and all write accesses to the variable flags[12].

record	run	address	cycle	data	symbol	busmaster	ti.back
		st16.b [a15]0x0,d15					
686		for (i = 0 ; i <= SIZE ; flags[i++] = TRUE) ;					
686		add16 d0,#0x1 ; i,#1					
686		for (i = 0 ; i <= SIZE ; flags[i++] = TRUE) ;					
+00000059	+00000068	jge d15,d0,0x700013F0 ; d15,i,0x700013F0	01		\\triboard-tc275_sieve_intmem\Global\fflags+0x0C	0.140us	
		D:7000007C wr-data			P:700013F0 ptrace		\\triboard-tc275_sieve_intmem\taskc\sieve+0x6 0.080us
686		for (i = 0 ; i <= SIZE ; flags[i++] = TRUE) ;					
		movh.a a15,#0x7000					
		lea a15,[a15]0x70					
686		for (i = 0 ; i <= SIZE ; flags[i++] = TRUE) ;					
		addsc.a a15,a15,d0,#0x0 ; a15,a15,i,#0					
686		for (i = 0 ; i <= SIZE ; flags[i++] = TRUE) ;					
		mov16 d15,#0x1					

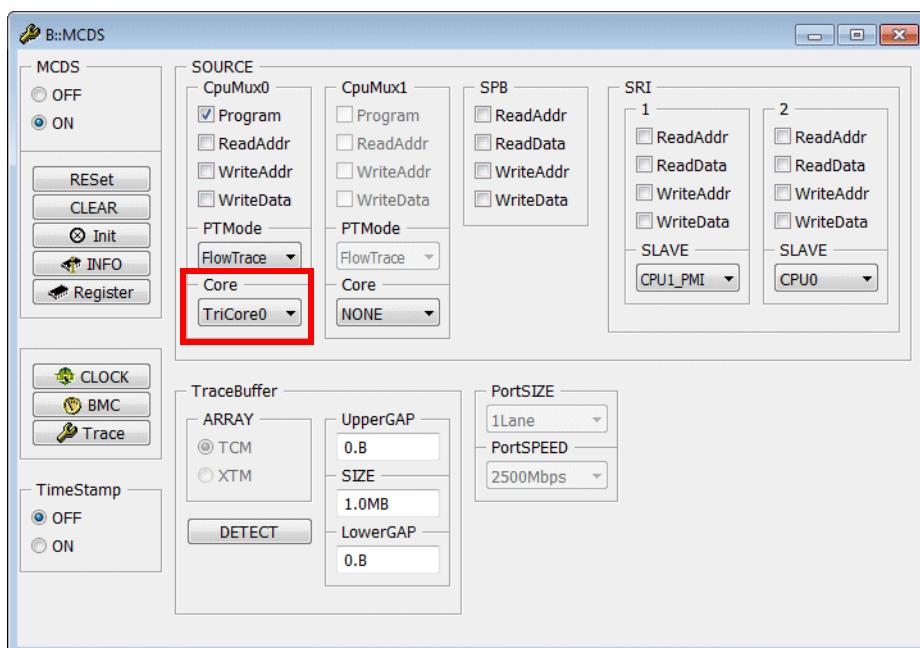
TraceON: Advise the Processor Observation Block to start the generation of trace messages for the enabled SOURCES.

TraceOFF: Advise the Processor Observation Block to stop the generation of trace messages.

Example: Restrict the generation of trace messages to the function func2.

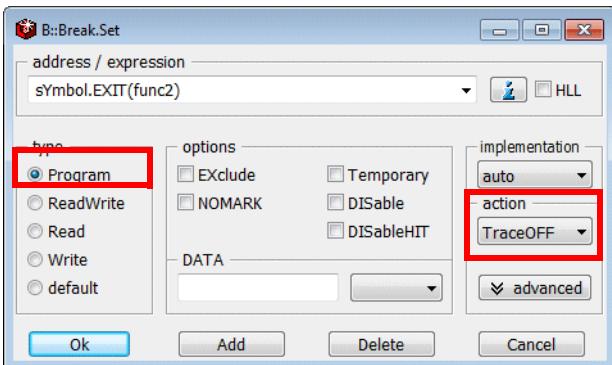
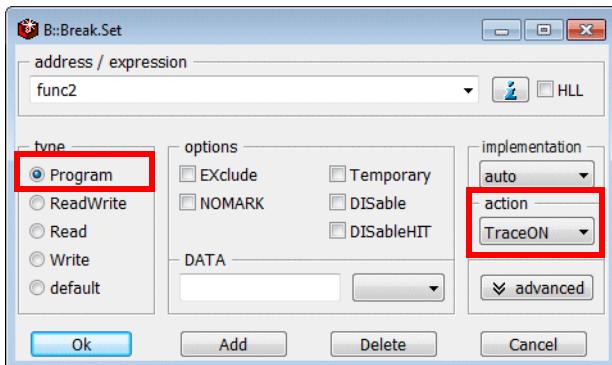
- **Core under debug:** TC 1.6.1 CPU0.
- **Events of interest:** Entry to function func2, exit of function func2
- **Requested Messages:** Instruction Pointer Call Messages, Write Data Trace Messages, Read Data Trace Messages, Timestamp Messages.

1. Configure the trace multiplexer.



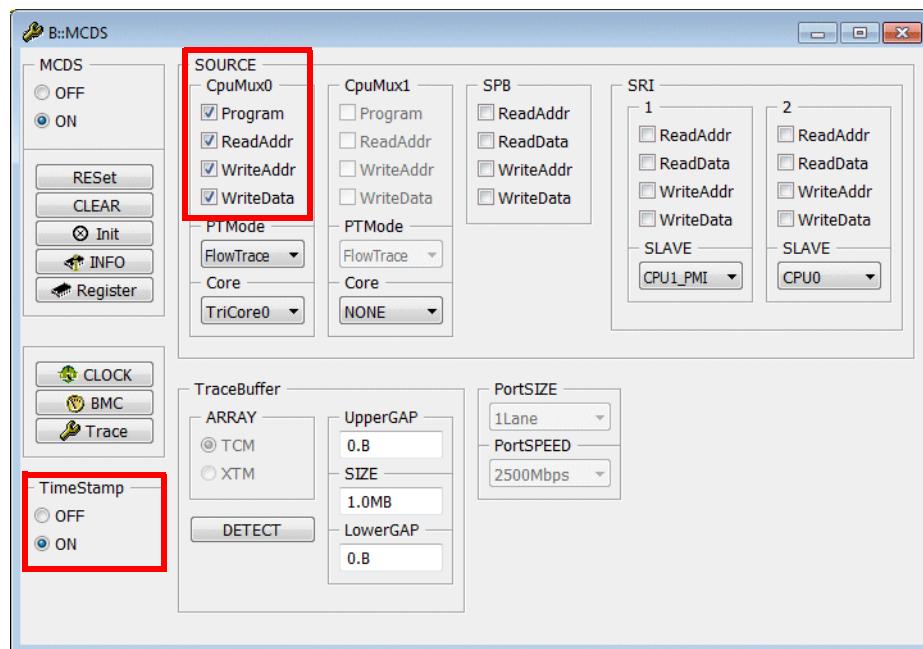
```
MCDS.SOURCE.Set CpuMux0.Core TriCore0 ; enable TC 1.6.1 CPU0 as
                                         ; trace source
```

2. Specify the events.



```
Break.Set    func2 /Program /TraceON
Break.Set    sYmbol.EXIT(func2) /Program /TraceOFF
```

3. Configure which trace messages are generated when the message generation is active (after TraceON event).



```
MCDS.TimeStamp ON ; enable Timestamp Messages

CLOCK.ON

MCDS.SOURCE.Set CpuMux0.Program ON ; enable Instruction
; Pointer Call Messages for
; TC 1.6.1 CPU0

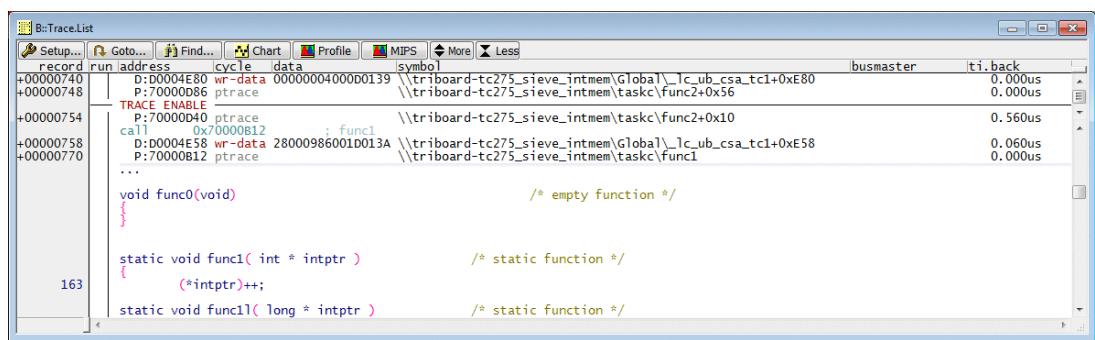
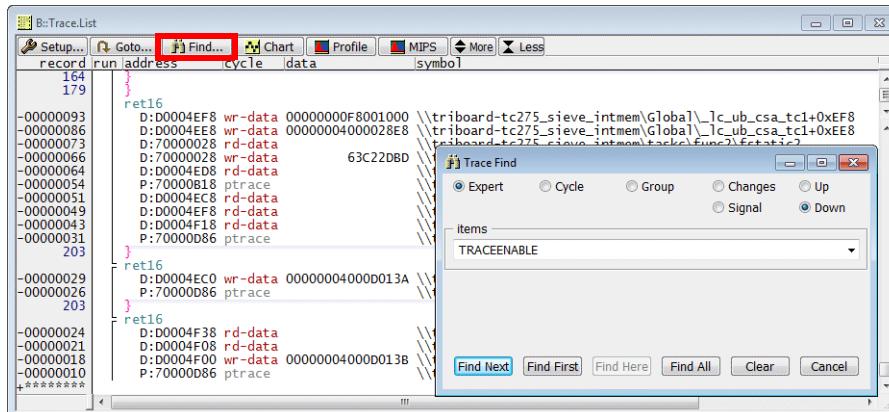
MCDS.SOURCE.Set CpuMux0.ReadAddr ON ; enable Read Data Trace
; Messages for TC 1.6.1 CPU0

MCDS.SOURCE.Set CpuMux0.WriteAddr ON ; enable Write Data Trace
MCDS.SOURCE.Set CpuMux0.WriteData ON ; Messages for TC 1.6.1 CPU0
```

4. Start and stop the program execution.

5. Display the result.

TRACE ENABLE indicates the start of the message generation after the TraceON event occurred. It might be necessary to search for it.



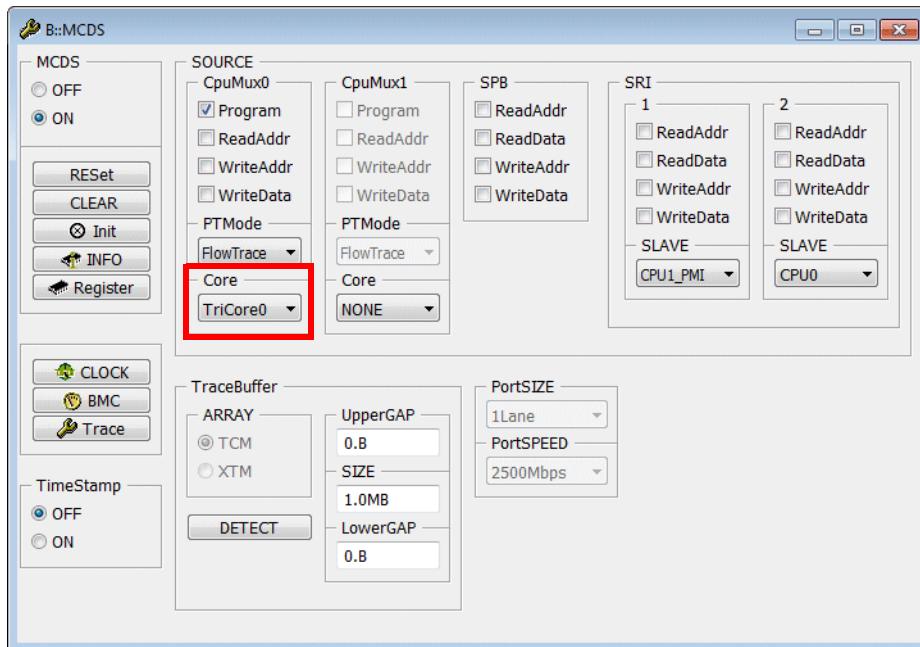
Trace message generation is started after the TraceON event occurred. As a result the event itself is not visible in the trace.

Advise the Processor Observation Block to end the message generation at the specified event.

Example 1: Stop the trace recording when 0x0 was written to the variable flags[3].

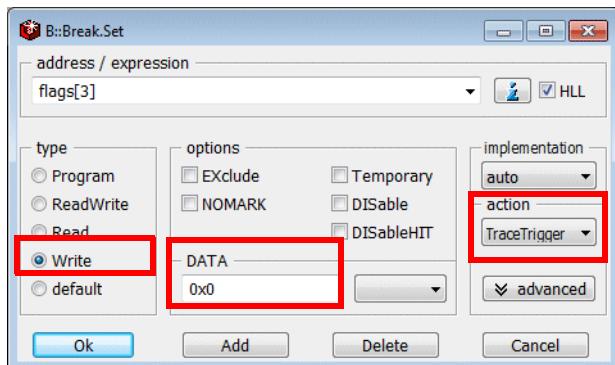
- **Core under debug:** TC 1.6.1 CPU0.
- **Event of interest:** Write of 0x0 to variable flags[3].
- **Requested Messages:** Instruction Pointer Call Messages, Write Data Trace Messages, Timestamp Messages.

1. Configure the trace multiplexer.



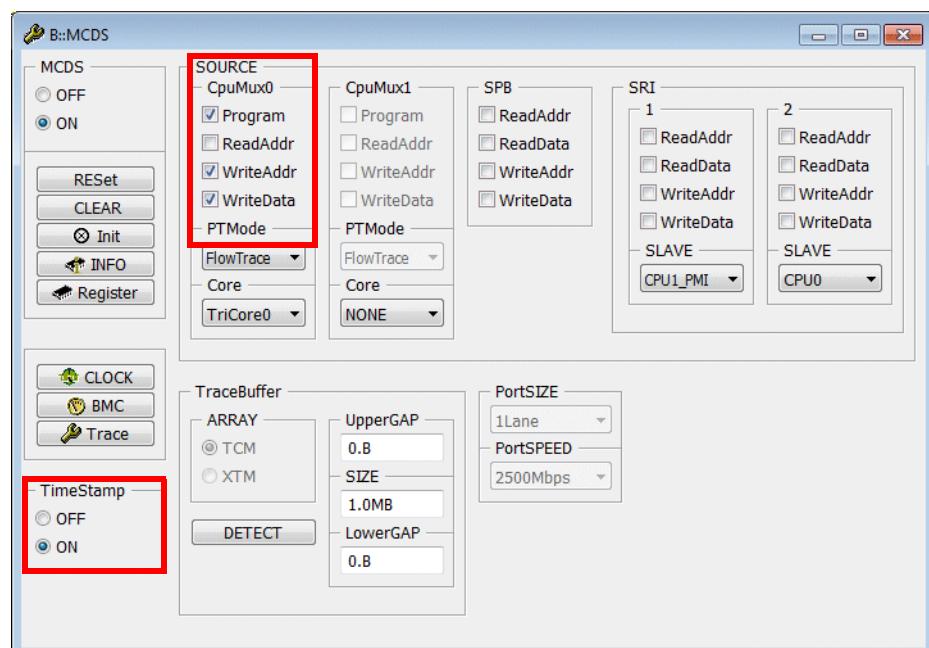
```
; enable TC 1.6.1 CPU0 as trace source
MCDS.SOURCE.Set CpuMux0.Core TriCore0
```

2. Specify the event.



```
Var.Break.Set flags[3] /Write /DATA.Byte 0x0 /TraceTrigger
```

3. Configure which trace messages are generated until the trigger event occurs.



```
MCDS.TimeStamp ON
```

```
; enable Timestamp Messages
```

```
CLOCK.ON
```

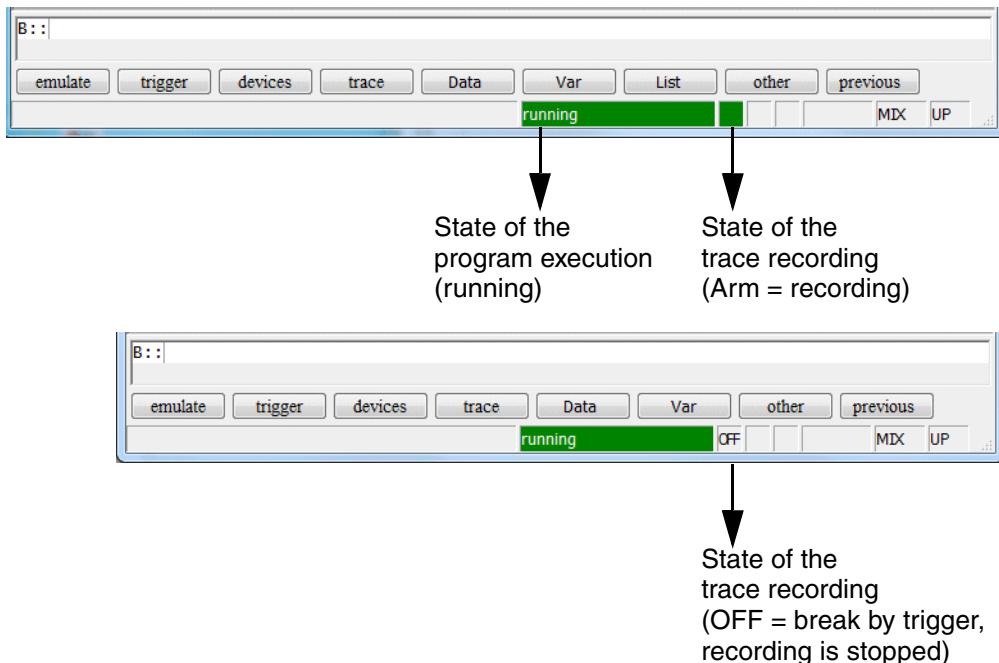
```
MCDS.SOURCE.Set CpuMux0.Program ON
```

```
; enable Instruction  
; Pointer Call Messages for  
; TC 1.6.1 CPU0
```

```
MCDS.SOURCE.Set CpuMux0.WriteAddr ON  
MCDS.SOURCE.Set CpuMux0.WriteData ON
```

```
; enable Write Data Trace  
; Messages for TC 1.6.1 CPU0
```

4. Start the program execution.



5. Display the result.

MCDS ends the generation of trace messages and flushes all internal buffer when the specified event occurs. TRACE32 automatically generates a **watchpoint TraceTrigger** message when the trigger event occurs. This helps you to find the actual trigger event in the trace.

The screenshot shows the B:Trace.List window with the following trace results:

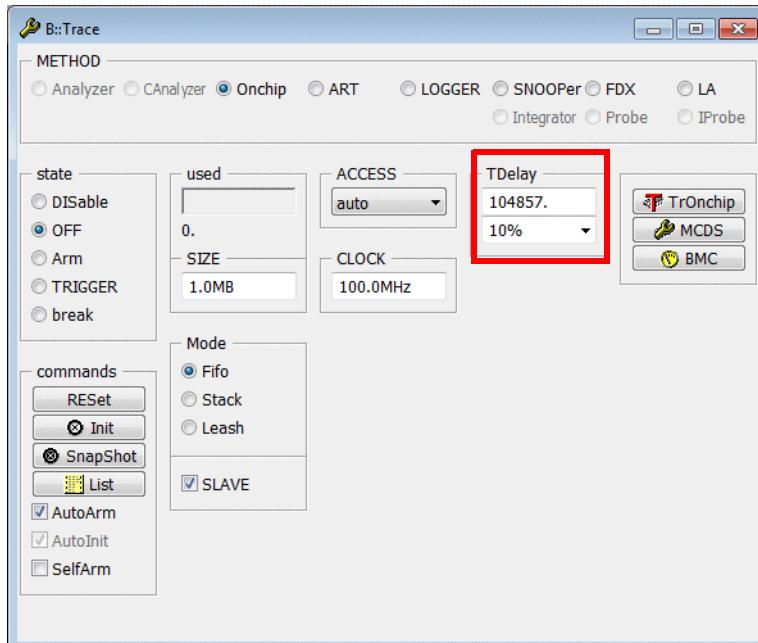
record	run	address	cycle	data	symbol	ti.back
694				while (k <= SIZE)		
694		mov d3,#0x12		while (k <= SIZE)		
-00000038		jge d3,d1,0x70101326 ; d3,k,0x70101326			D:7000006F wr-data 00 \\triboard-tc275_sieve_intmem\Global\flags+0x3	0.060us
-00000033		• watchpoint TraceTrigger				
-00000031		wpt-mcx	00		\\triboard-tc275_sieve_intmem\taskc\sieve+0x3E	0.040us
		P:70101326 ptrace				0.000us

Example 2: Stop the trace recording after another 10% of the trace memory was filled when 0x0 was written to the variable flags[3].

- **Core under debug:** TC 1.6.1 CPU0.
- **Event of interest:** Write of 0x0 to variable flags[3].
- **Requested Messages:** Instruction Pointer Call Messages, Write Data Trace Messages, Timestamp Messages.

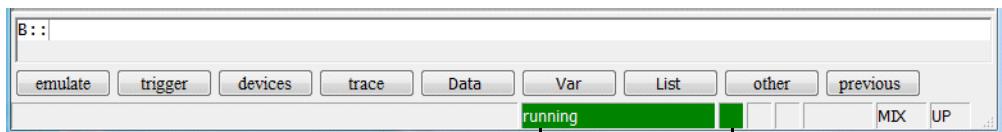
1. to 3. as in example 1.

4. Specific the delay.



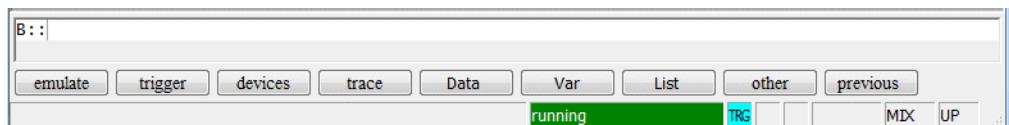
Trace.TDelay 10%

5. Start the program execution.

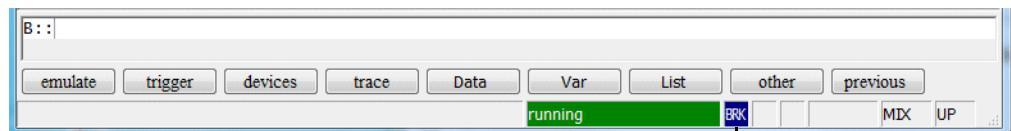


State of the
program execution
(running)

State of the
trace recording
(Arm = recording)

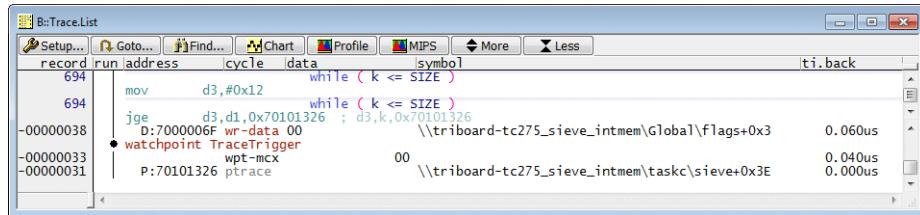


State of the
trace recording
(TRG = trigger occurred,
delay counter started)



State of the
trace recording
(BRK = delay counter elapsed,
recording is stopped)

6. Display the result.



The screenshot shows the B:Trace.List window with the following content:

record	run	address	cycle	data	symbol	ti.back
694					while (k <= SIZE)	
694		mov d3,#0x12			while (k <= SIZE)	
-00000038		jge D:7000006F	wr-data	00	\triboard-tc275_sieve_intmem\Global\flags+0x3	0.060us
-00000033		• watchpoint TraceTrigger	wpt-mcx	00	\triboard-tc275_sieve_intmem\taskc\sieve+0x3E	0.040us
-00000031		P:70101326	ptrace			0.000us

Fundamental behavior for SMP systems:

- Filters and Triggers are programmed for all cores that are connected to the trace multiplexer.
- Filters advise the Processor Observation Blocks of the connected cores to generate the trace information of interest.
- Marker/Trigger advise the Processor Observation Blocks of the connected cores to indicate the occurrence of an event.

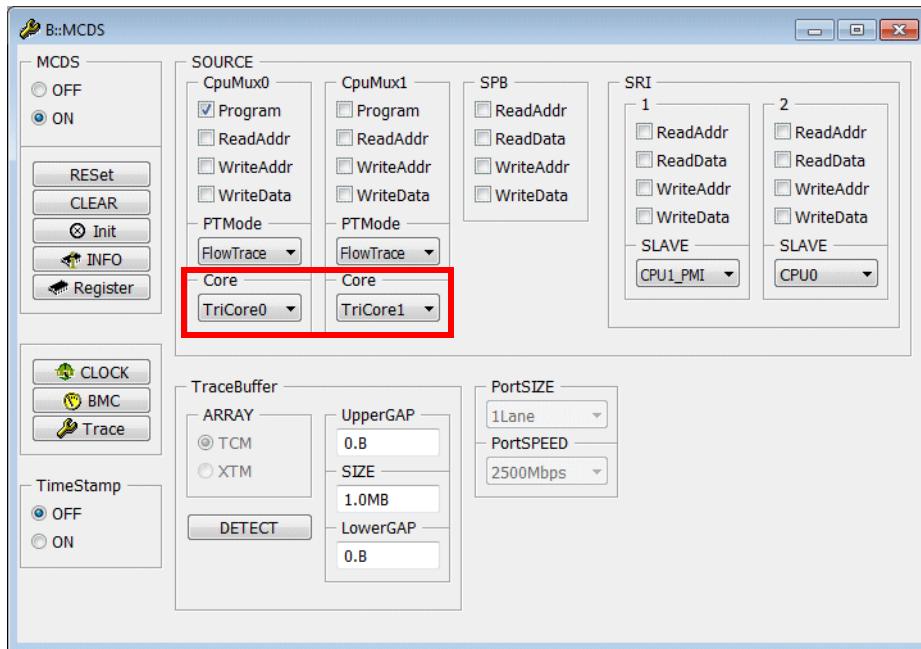
WATCH Marker

Advise Processor Observation Blocks of all cores to indicate the occurrence of an event.

Example: Indicate that 0x0 was written to the variable flags[3].

- **System under debug:** SMP system with 3 TriCore cores.
- **Cores connected to the trace multiplexer:** TC 1.6.1 CPU0 and TC 1.6.1 CPU1.
- **Event of interest:** Write of 0x0 to variable flags[3]
- **Requested trace messages:** Instruction Pointer Call Messages for both cores, Timestamp Messages.

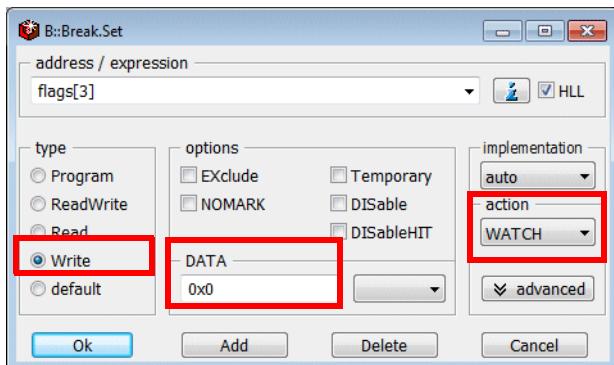
1. Configure the trace multiplexer.



```
MCDS.SOURCE.Set CpuMux0.Core TriCore0 ; enable TC 1.6.1 CPU0 as
; trace source
```

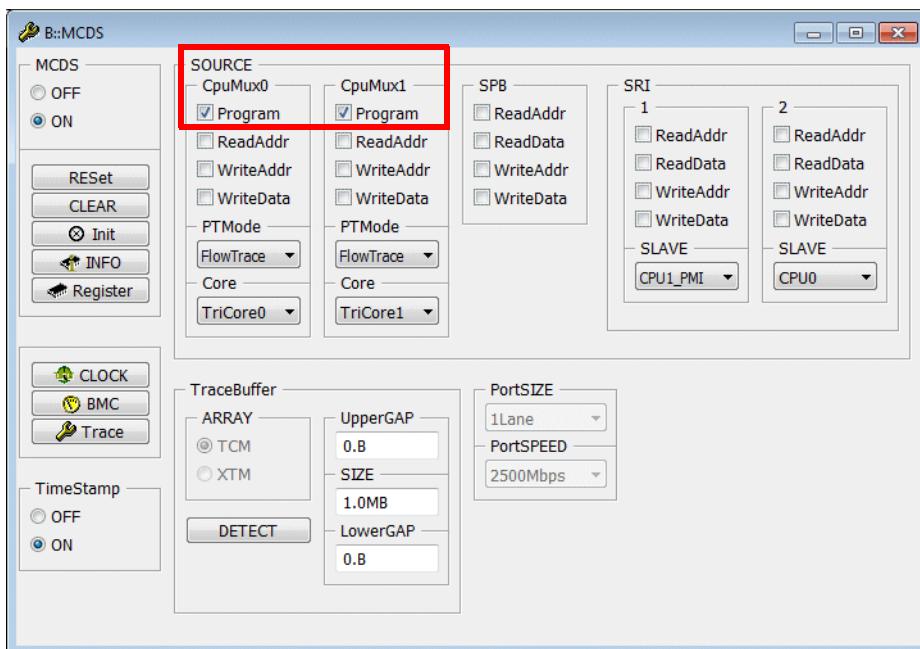
```
MCDS.SOURCE.Set CpuMux1.Core TriCore1 ; enable TC 1.6.1 CPU1 as
; trace source
```

2. Specify the event.



```
Var.Break.Set flags[3] /Write /DATA.Byte 0x0 /WATCH
```

3. Configure which trace messages are generated.

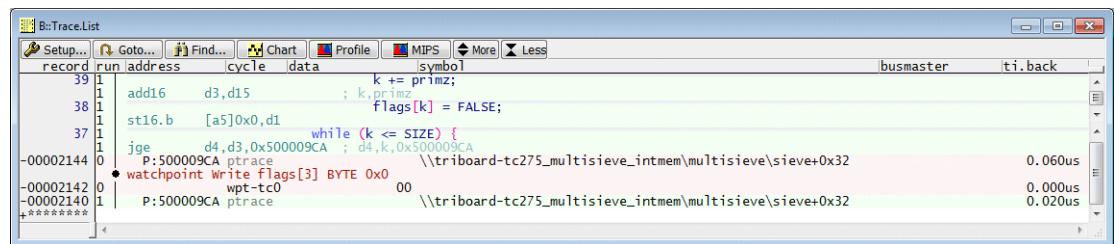
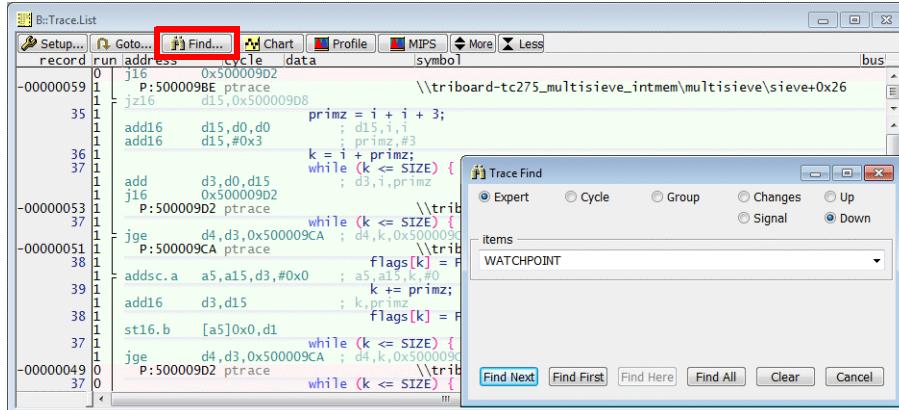


```
MCDS.TimeStamp ON ; enable Timestamp Messages  
CLOCK.ON  
MCDS.SOURCE.Set CpuMux0.Program ON ; enable Instruction Pointer  
; Call Messages for  
; TC 1.6.1 CPU0  
MCDS.SOURCE.Set CpuMux1.Program ON ; enable Instruction Pointer  
; Call Messages for  
; TC 1.6.1 CPU1
```

4. Start and stop the program execution.

5. Display the result.

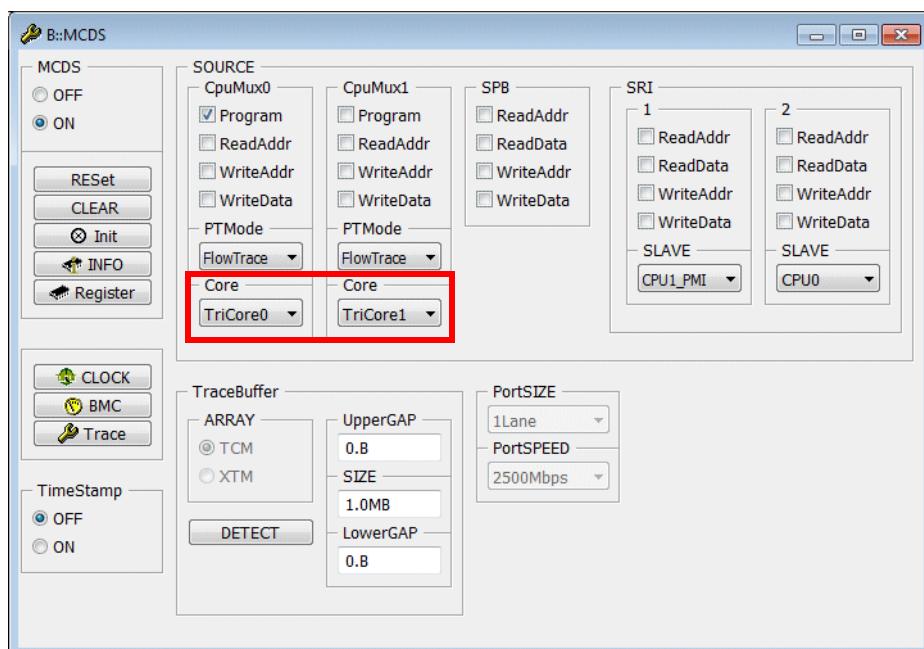
It might be necessary to search for the result.



Advise the Processor Observation Block to generate trace messages for the enabled SOURCES when the specified event is true.

Example 1: Restrict the generated trace information to the entries to the function sieve.

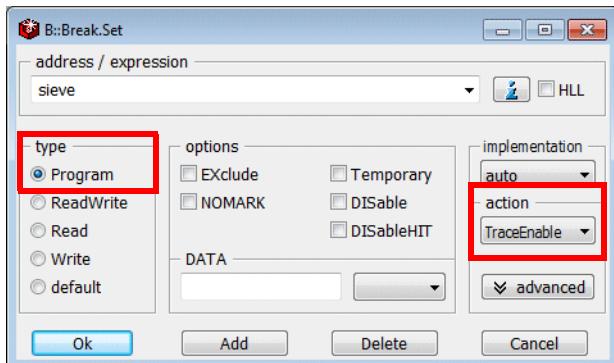
- **System under debug:** SMP system with 3 TriCore cores.
- **Cores connected to the trace multiplexer:** TC 1.6.1 CPU0 and TC 1.6.1 CPU1.
- **Event of interest:** Entry to function sieve.
- **Requested trace messages:** Instruction Pointer Call Messages for both cores, Timestamp Messages.



```
MCDS.SOURCE.Set CpuMux0.Core TriCore0 ; enable TC 1.6.1 CPU0 as
; trace source

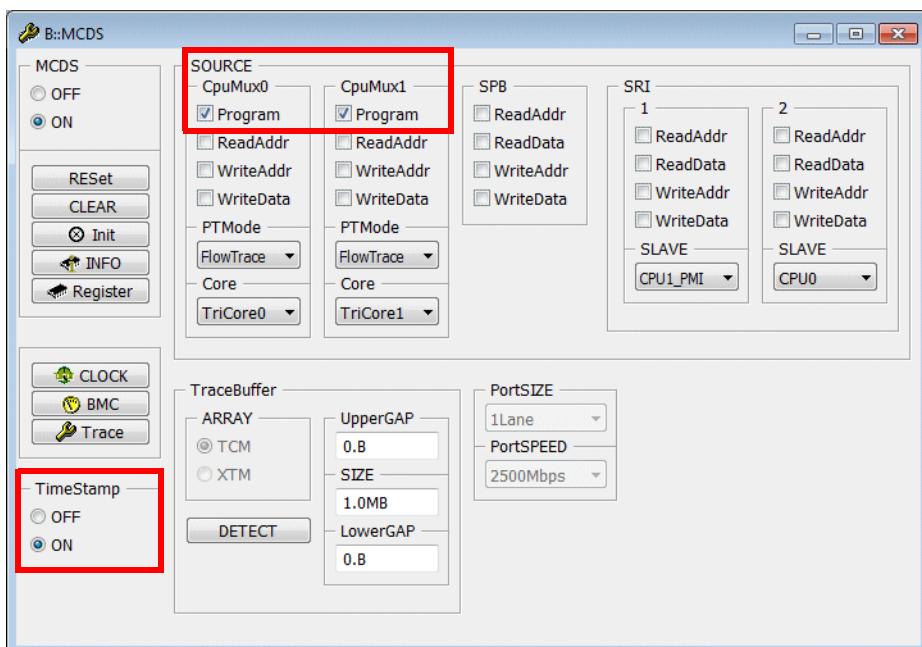
MCDS.SOURCE.Set CpuMux1.Core TriCore1 ; enable TC 1.6.1 CPU1 as
; trace source
```

2. Specify the event.



```
Break.Set    sieve /Program /TraceEnable
```

3. Configure which trace messages are generated while the event is true.



```
MCDS.TimeStamp ON ; enable Timestamp Messages

CLOCK.ON

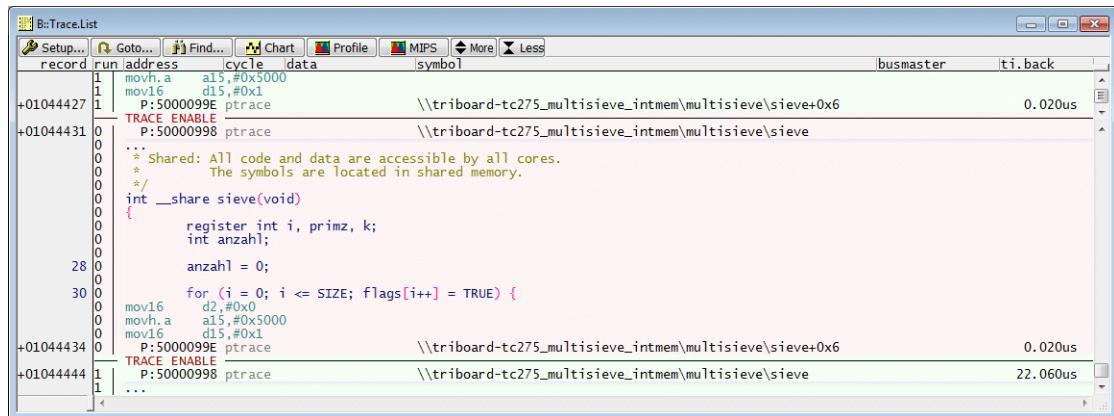
MCDS.SOURCE.Set CpuMux0.Program ON ; enable Instruction Pointer
; Call Messages for
; TC 1.6.1 CPU0

MCDS.SOURCE.Set CpuMux1.Program ON ; enable Instruction Pointer
; Call Messages for
; TC 1.6.1 CPU1
```

4. Start the program execution and stop it.

5. Display the result.

The trace contains only small code sections generated for the entries to the function sieve (TRACE ENABLE).

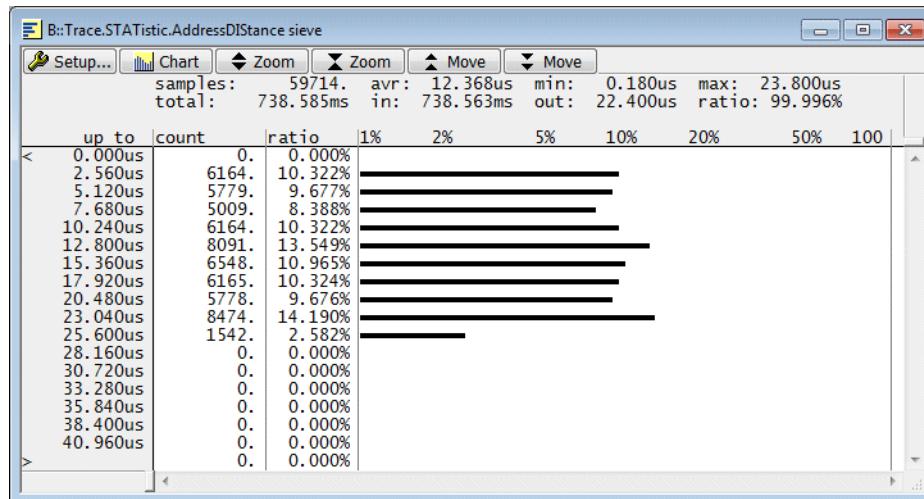


The screenshot shows the B:Trace.List window with the following trace data:

record	run	address	Cycle	data	symbol	busmaster	ti.back
+01044427	1	movh.a a15,#0x5000					
	1	mov16 d15,#0x1					
	1	P:5000099E ptrace			\\triboard-tc275_multisieve_intmem\multisieve\sieve+0x6		0.020us
+01044431	0	TRACE ENABLE					
	0	P:50000998 ptrace			\\triboard-tc275_multisieve_intmem\multisieve\sieve		
	0	** Shared: All code and data are accessible by all cores.					
	0	* The symbols are located in shared memory.					
	0	*/					
	0	int __share_sieve(void)					
	0	{					
	0	register int i, primz, k;					
	0	int anzahl;					
	28						
	28	anzahl = 0;					
	30						
	30	for (i = 0; i <= SIZE; flags[i++] = TRUE) {					
	0	mov16 d2,#0x0					
	0	movh.a a15,#0x5000					
	0	mov16 d15,#0x1					
+01044434	0	P:5000099E ptrace			\\triboard-tc275_multisieve_intmem\multisieve\sieve+0x6		0.020us
+01044444	1	TRACE ENABLE					
	1	P:50000998 ptrace			\\triboard-tc275_multisieve_intmem\multisieve\sieve		22.060us
	1	...					

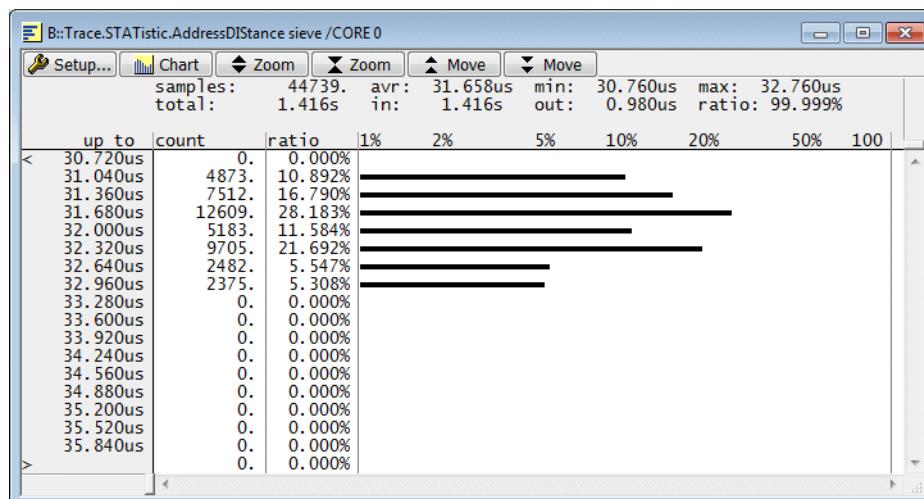
The following **Trace.STATistic** command calculates the time intervals for a program address event. The program address event is here the entry to the function sieve. The core information is discarded for this calculation.

```
Trace.STATistic.AddressDISTance sieve [/JoinCORE]
```



If you need the result per core, use the following command:

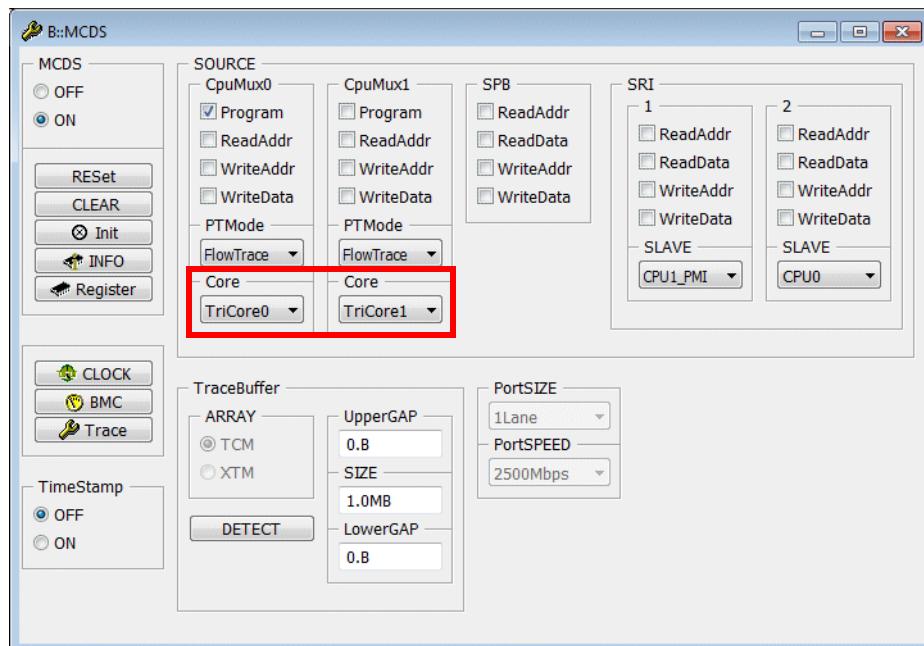
```
Trace.STATistic.AddressDISTance sieve /CORE 0
```



Example 2: Restrict the generated trace information to the entries to the function sieve and the exits from the function sieve.

- **System under debug:** SMP system with 3 TriCore cores.
- **Cores connected to the trace multiplexer:** TC 1.6.1 CPU0 and TC 1.6.1 CPU1.
- **Event of interest:** Entry to function sieve and exit of function sieve.
- **Requested trace messages:** Instruction Pointer Call Messages for both cores, Timestamp Messages.

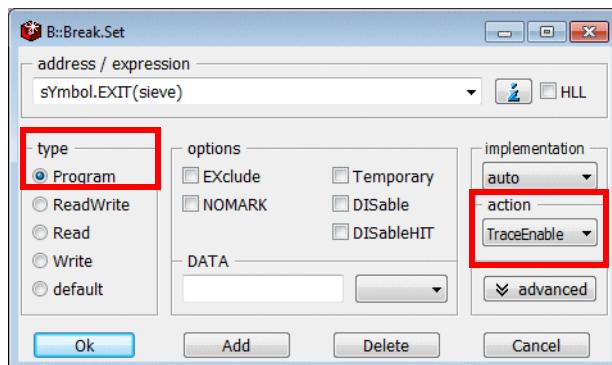
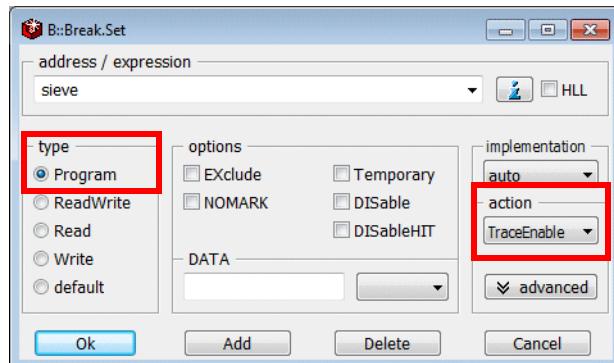
1. Configure the trace multiplexer.



```
MCDS.SOURCE.Set CpuMux0.Core TriCore0 ; enable TC 1.6.1 CPU0 as
                                         ; trace source

MCDS.SOURCE.Set CpuMux1.Core TriCore1 ; enable TC 1.6.1 CPU1 as
                                         ; trace source
```

2. Specify the events.

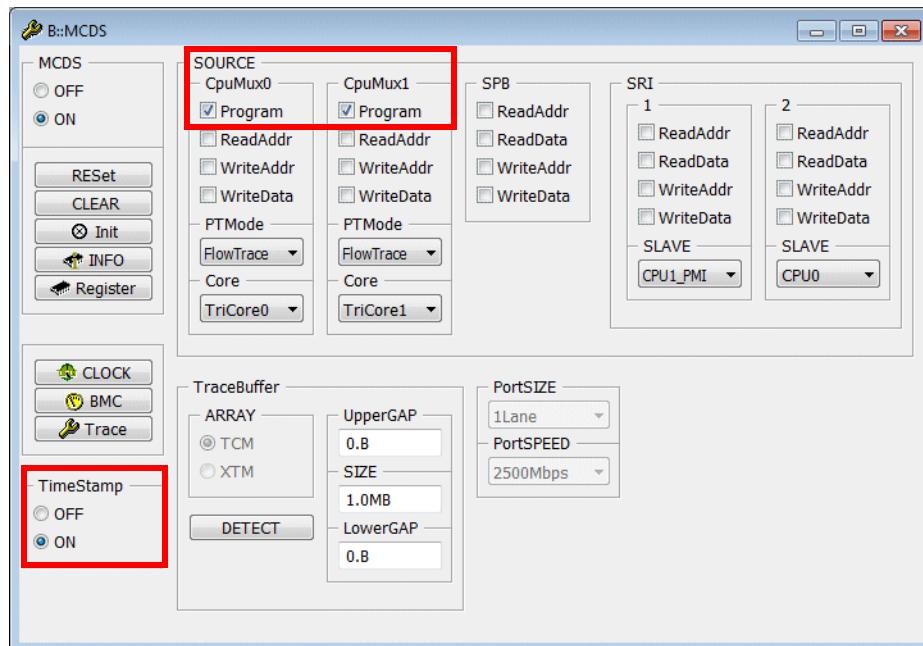


sYmbol.EXIT(<symbol>) Returns the exit address of the specified function

```
Break.Set    sieve /Program /TraceEnable
```

```
Break.Set    sYmbol.EXIT(sieve) /Program /TraceEnable
```

3. Configure which trace messages are generated while the events are true.



```
MCDS.TimeStamp ON ; enable Timestamp Messages

CLOCK.ON

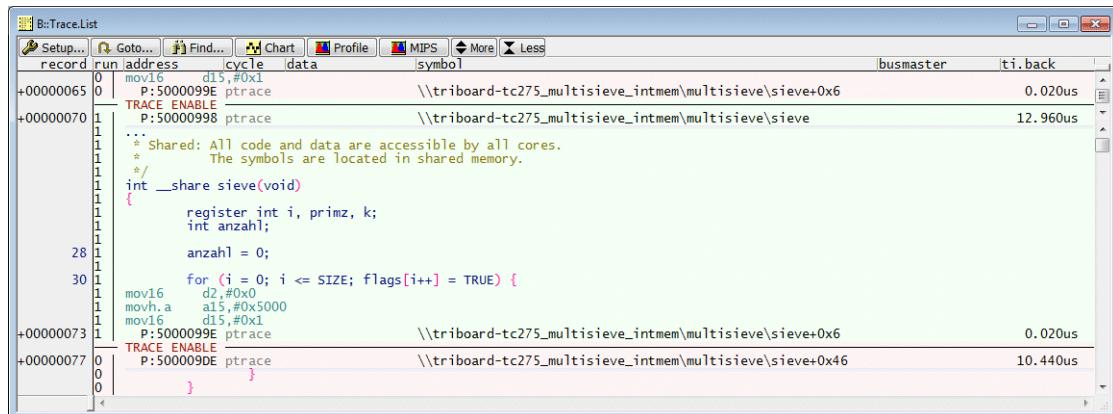
MCDS.SOURCE.Set CpuMux0.Program ON ; enable Instruction Pointer
; Call Messages for
; TC 1.6.1 CPU0

MCDS.SOURCE.Set CpuMux1.Program ON ; enable Instruction Pointer
; Call Messages for
; TC 1.6.1 CPU1
```

4. Start the program execution and stop it.

5. Display the result.

The trace contains only small code sections generated for the entries to the function sieve (TRACE ENABLE) and for the exits of the function sieve (TRACE ENABLE).

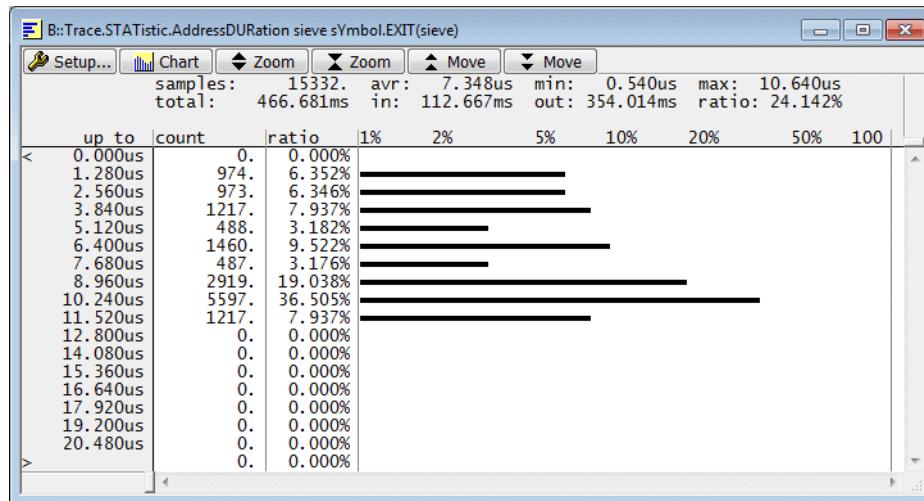


The screenshot shows the B:Trace.List window with the following data:

record	run	address	cycle	data	symbol	busmaster	ti.back	
+00000065	0	0	mov16 d15,#0x1	P:5000099E	ptrace	\\triboard-tc275_multisieve_intmem\multisieve\sieve+0x6	0.020us	
+00000070	1	1	TRACE ENABLE	P:50000998	ptrace	\\triboard-tc275_multisieve_intmem\multisieve\sieve	12.960us	
	1	1	...			# Shared: All code and data are accessible by all cores.		
	1	1				The symbols are located in shared memory.		
	1	1				/*		
	1	1				int __share sieve(void)		
	1	1				{		
	1	1				register int i, primz, k;		
	1	1				int anzahl;		
	28	1				anzahl = 0;		
	30	1				for (i = 0; i <= SIZE; flags[i++] = TRUE) {		
	1	1				mov16 d2,#0x0		
	1	1				movh.a a15,#0x5000		
	1	1				mov16 d15,#0x1		
+00000073	1	1			P:5000099E	ptrace	\\triboard-tc275_multisieve_intmem\multisieve\sieve+0x6	0.020us
+00000077	0	0	TRACE ENABLE	P:500009DE	ptrace	\\triboard-tc275_multisieve_intmem\multisieve\sieve+0x46	10.440us	
	0	0				}		
	0	0						

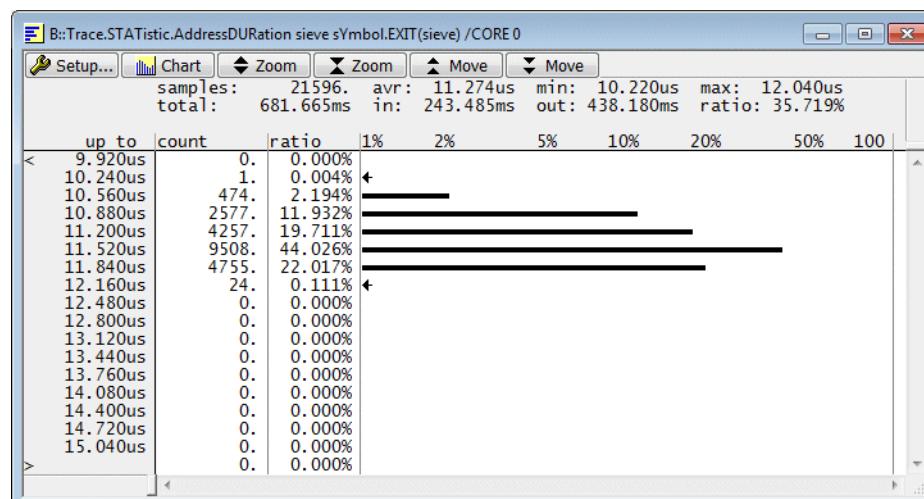
The following **Trace.STATistic** command calculates the time intervals between two program address events A and B. The entry to the function sieve is A in this example, the exit from the function is B. The core information is discarded for this calculation.

```
Trace.STATistic.AddressDURation sieve sYmbol.EXIT(sieve) [/JoinCORE]
```



If you need the result per core, use the following command:

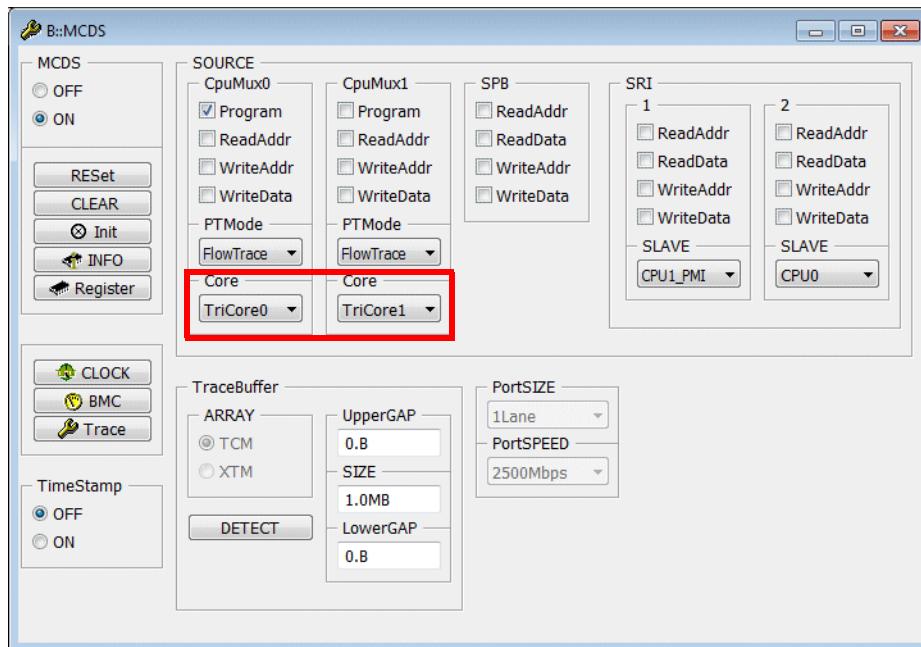
```
Trace.STATistic.AddressDURation sieve sYmbol.EXIT(sieve) /CORE 0
```



Example 3: Restrict the generated trace information to write accesses to the variable flags[3].

- **System under debug:** SMP system with 3 TriCore cores.
- **Cores connected to the trace multiplexer:** TC 1.6.1 CPU0 and TC 1.6.1 CPU1.
- **Event of interest:** Write access to variable flags[3].
- **Requested trace messages:** Write Data Trace Messages for both cores, Timestamp Messages.

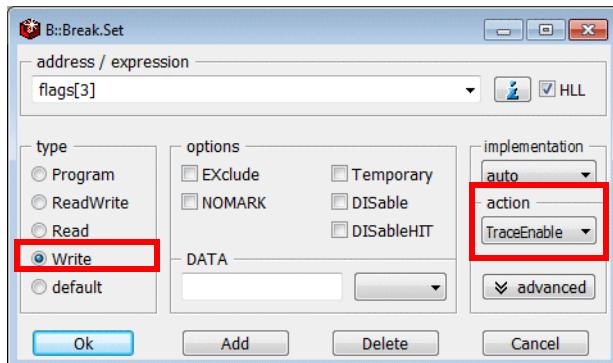
1. Configure the trace multiplexer.



```
MCDS.SOURCE.Set CpuMux0.Core TriCore0 ; enable TC 1.6.1 CPU0 as
                                         ; trace source

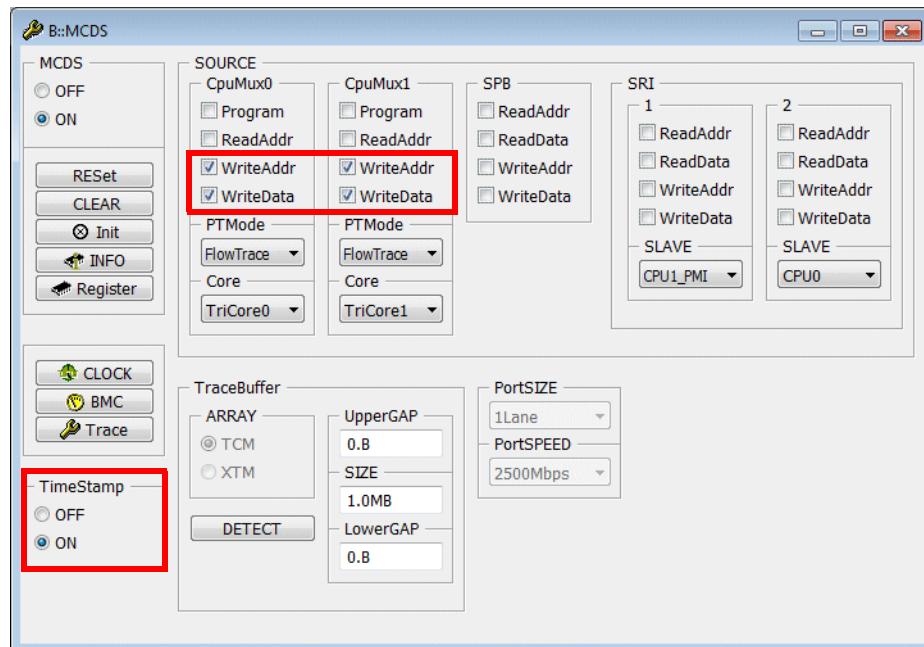
MCDS.SOURCE.Set CpuMux1.Core TriCore1 ; enable TC 1.6.1 CPU1 as
                                         ; trace source
```

2. Specify the events.



```
Var.Break.Set flags[3] /Write /TraceEnable
```

3. Configure which trace messages are generated while the event is true.



```
MCDS.TimeStamp ON ; enable Timestamps  
Messages  
  
CLOCK.ON  
  
MCDS.SOURCE.Set CpuMux0.Program OFF ; disable Instruction  
; Pointer Call Messages for  
; TC 1.6.1 CPU0  
  
MCDS.SOURCE.Set CpuMux1.Program OFF ; disable Instruction  
; Pointer Call Messages for  
; TC 1.6.1 CPU1  
  
MCDS.SOURCE.Set CpuMux0.WriteAddr ON ; enable Write Data Trace  
; Messages for TC 1.6.1 CPU0  
  
MCDS.SOURCE.Set CpuMux0.WriteData ON ; enable Write Data Trace  
; Messages for TC 1.6.1 CPU0  
  
MCDS.SOURCE.Set CpuMux1.WriteAddr ON ; enable Write Data Trace  
; Messages for TC 1.6.1 CPU1  
  
MCDS.SOURCE.Set CpuMux1.WriteData ON ; enable Write Data Trace  
; Messages for TC 1.6.1 CPU1
```

4. Start the program execution and stop it.

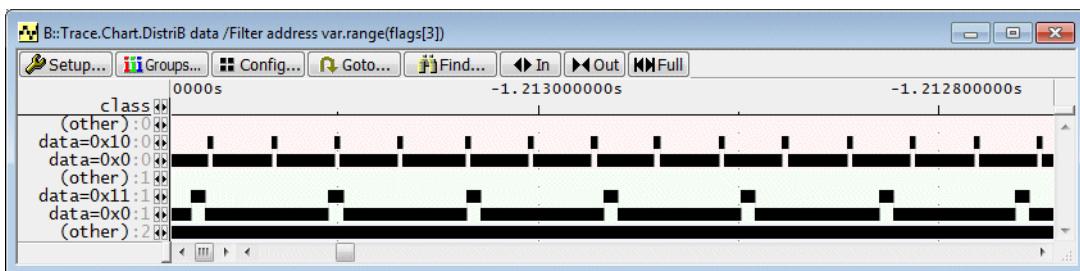
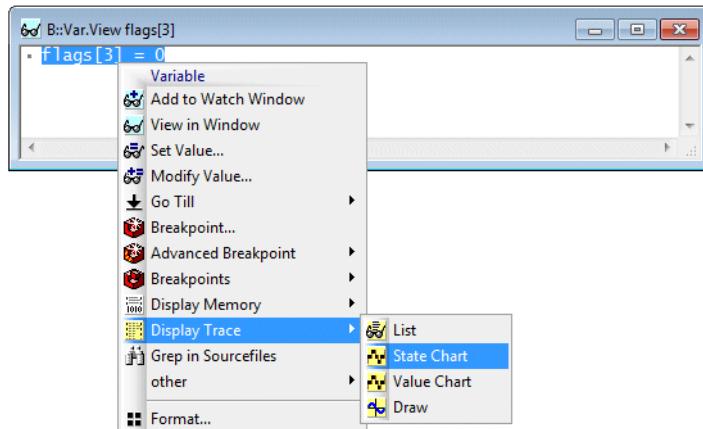
5. Display the result.

The trace contains only write accesses to the variable flags[3].

record	run	address	cycle	data	symbol	ti.back
-000000105	0	D:70000C2F	wr-data	10	\\triboard-tc275_multisieve_intmem\Global\flags+0x3	28.980us
-00000099	0	D:70000C2F	wr-data	00	\\triboard-tc275_multisieve_intmem\Global\flags+0x3	2,800us
-00000005	0	D:70000C2F	wr-data	10	\\triboard-tc275_multisieve_intmem\Global\flags+0x3	30,560us
-00000079	0	D:70000C2F	wr-data	00	\\triboard-tc275_multisieve_intmem\Global\flags+0x3	20,700us
-00000072	1	D:70000C2F	wr-data	11	\\triboard-tc275_multisieve_intmem\Global\flags+0x3	60,740us
-00000064	1	D:70000C2F	wr-data	00	\\triboard-tc275_multisieve_intmem\Global\flags+0x3	7,260us
-00000054	0	D:70000C2F	wr-data	10	\\triboard-tc275_multisieve_intmem\Global\flags+0x3	29,100us
-00000048	0	D:70000C2F	wr-data	00	\\triboard-tc275_multisieve_intmem\Global\flags+0x3	2,800us
-00000034	0	D:70000C2F	wr-data	10	\\triboard-tc275_multisieve_intmem\Global\flags+0x3	28,840us
-00000028	0	D:70000C2F	wr-data	00	\\triboard-tc275_multisieve_intmem\Global\flags+0x3	2,800us
-00000019	1	D:70000C2F	wr-data	11	\\triboard-tc275_multisieve_intmem\Global\flags+0x3	60,940us

The Variable pull-down provides various way to analyze the variable contents over the time.

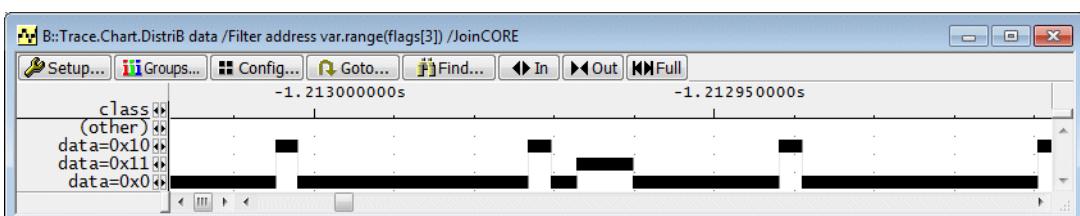
```
; open a window to display the variable  
Var.View flags[3]
```



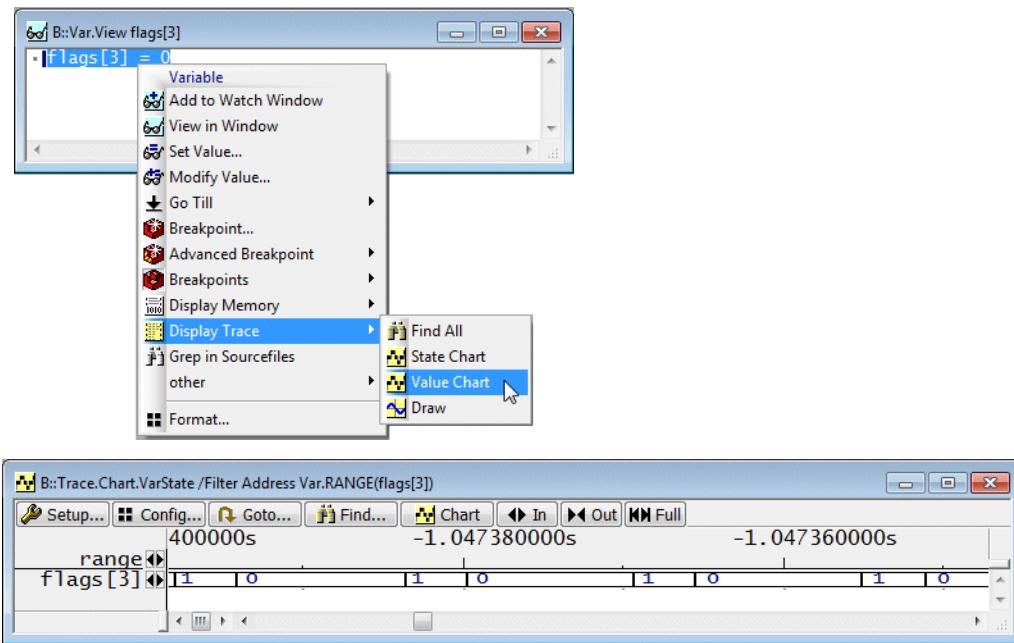
Display the value changes of a variable graphically - split the result per core
Trace.Chart.DistrIB Data /Filter Address Var.RANGE(<var>) [/SplitCORE]

Var.RANGE(<var>)

Returns the address range in which the content of a variable is stored.

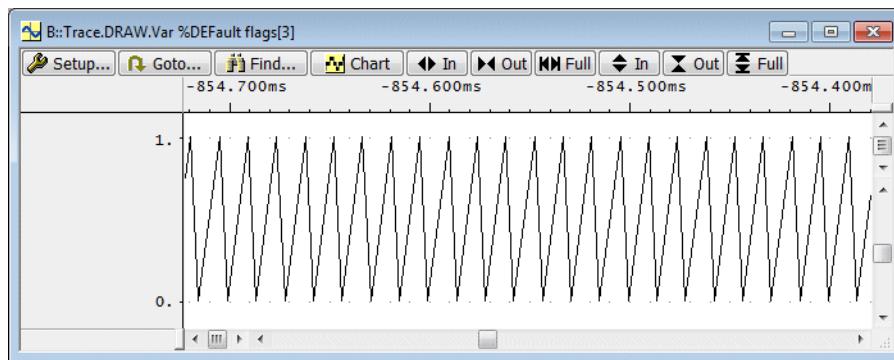
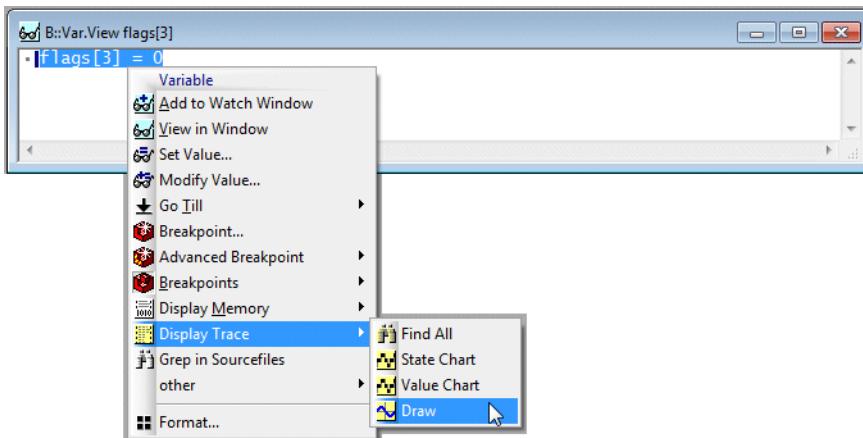


Display the value changes of a variable graphically - ignore core information
Trace.Chart.DistrIB Data /Filter Address Var.RANGE(<var>) /JoinCORE



Display variable contents over the time numerically - the core information is discarded
Trace.Chart.VarState /Filter Address Var.RANGE(<var>) [/JoinCORE]

Display variable contents over the time numerically - the core information is discarded
Trace.Chart.VarState /Filter Address Var.RANGE(<var>) /CORE <n>



Display variable contents over the time graphically - the core information is discarded
Trace.DRAW.Var %DEFault <var> [/JoinCORE]

Display variable contents over the time graphically - the core information is discarded
Trace.DRAW.Var %DEFault <var> /CORE <n>

Advise the Processor Observation Block to generate trace messages for the instruction flow and for the specified events.

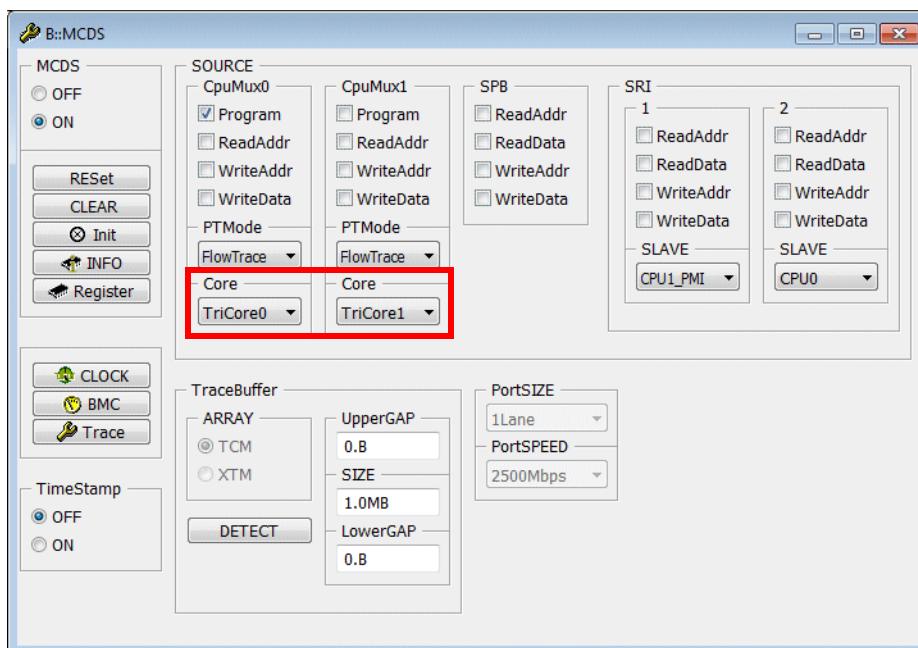
NOTE:

The TraceData filter is of great importance for the nesting function run-time analysis if an operating system is used.

Example: Generate trace information for the complete program flow and for all write accesses to flags[12].

- **System under debug:** SMP system with 3 TriCore cores.
- **Cores connected to the trace multiplexer:** TC 1.6.1 CPU0 and TC 1.6.1 CPU1.
- **Event of interest:** Write access to variable flags[12].
- **Requested trace messages:** Timestamp Messages.

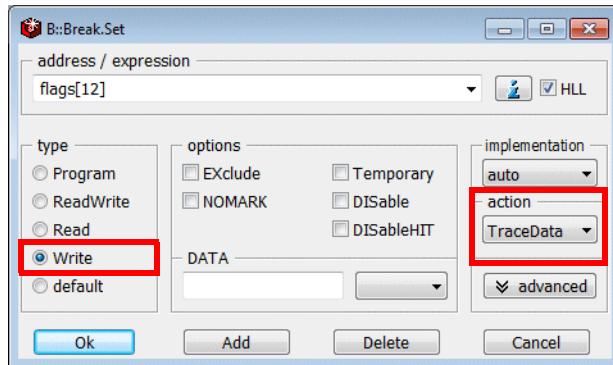
1. Configure the trace multiplexer.



```
MCDS.SOURCE.Set CpuMux0.Core TriCore0 ; enable TC 1.6.1 CPU0 as
                                         ; trace source

MCDS.SOURCE.Set CpuMux1.Core TriCore1 ; enable TC 1.6.1 CPU1 as
                                         ; trace source
```

2. Specify the event.



```
Var.Break.Set flags[12] /Write /TraceData
```

3. TRACE32 takes care of the message generation.

4. Start the program execution and stop it.

5. Display the result.

The trace contains the complete program flow and all write accesses to the variable flags[12].

record	run	address	cycle	data	symbol	busmaster	ti.back
+00000954	1	jge d4,d3,0x500009CA		; d4,k,0x500009CA	\\\triboard-tc275_multisieve_intmem\multisieve\sieve+0x32		0.060us
38		P:500009CA	ptrace		flags[k] = FALSE;		
39	0	addsc.a a5,a15,d3,#0x0		; a5,a15,k#0			
38	0	add16 d3,d15		; k_prime			
37	0	st16.b [a5]0x0,d1			flags[k] = FALSE;		
37	0	jge d4,d3,0x500009CA		; d4,k,0x500009CA	\\\triboard-tc275_multisieve_intmem\Global\flags+0x0C		0.060us
+00000957	1	D:5000010E wr-data 00			\\\triboard-tc275_multisieve_intmem\multisieve\sieve+0x32		0.000us
+00000960	1	P:500009CA	ptrace		flags[k] = FALSE;		
38	1	addsc.a a5,a15,d3,#0x0		; a5,a15,k#0			
39	1	add16 d3,d15		; k_prime			
38	1	st16.b [a5]0x0,d1			flags[k] = FALSE;		
37	1	jge d4,d3,0x500009CA		; d4,k,0x500009CA	\\\triboard-tc275_multisieve_intmem\multisieve\sieve+0x32		0.060us
+00000962	0	P:500009CA	ptrace		flags[k] = FALSE;		
38	0						

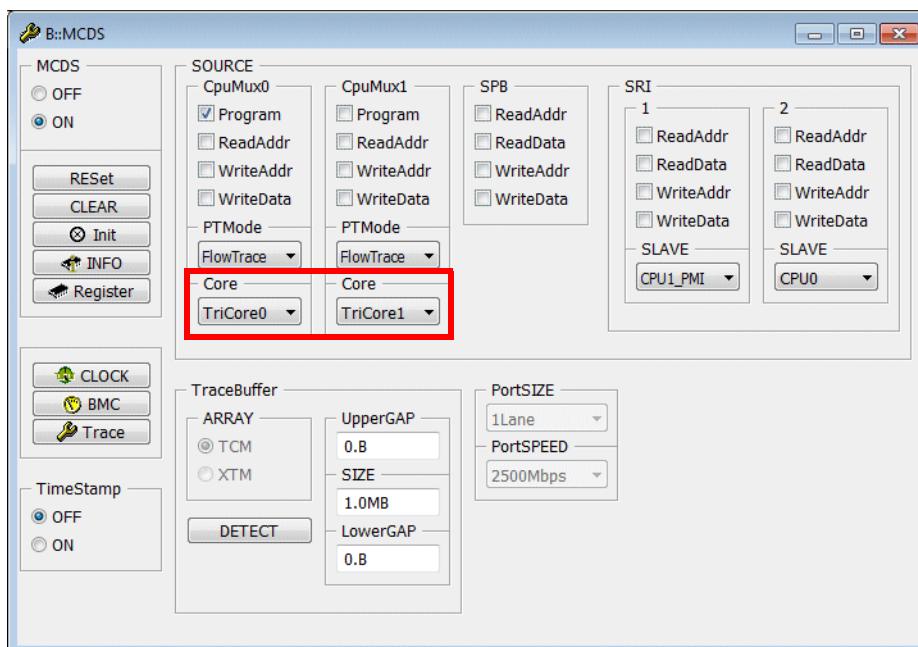
TraceON: Advise the Processor Observation Blocks to start the generation of trace messages for the enabled SOURCEs at the specified event.

TraceOFF: Advise the Processor Observation Blocks to stop the generation of trace messages at the specified event.

Example: Restrict the generation of trace messages to the function sieve1.

- **System under debug:** SMP system with 3 TriCore cores.
- **Cores connected to the trace multiplexer:** TC 1.6.1 CPU0 and TC 1.6.1 CPU1.
- **Event of interest:** Entry to function sieve1 and exit of function sieve1.
- **Requested trace messages:** Instruction Pointer Call Messages, Write Data Trace Messages, Read Data Trace Messages, Timestamp Messages.

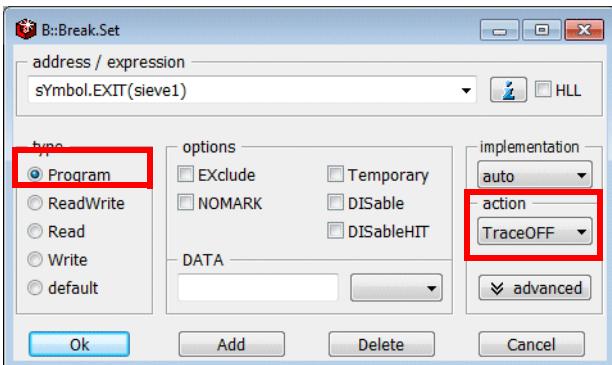
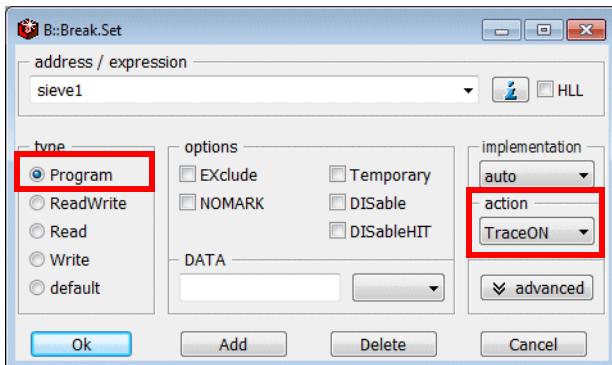
1. Configure the trace multiplexer.



```
MCDS.SOURCE.Set CpuMux0.Core TriCore0 ; enable TC 1.6.1 CPU0 as
                                         ; trace source

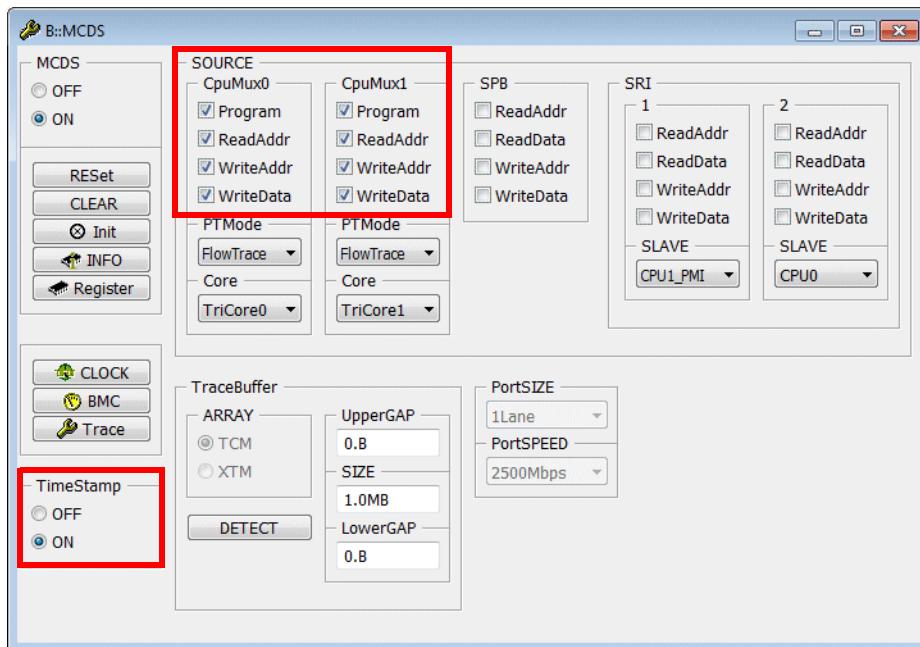
MCDS.SOURCE.Set CpuMux1.Core TriCore1 ; enable TC 1.6.1 CPU1 as
                                         ; trace source
```

2. Specify the events.



```
Break.Set    sieve1 /Program /TraceON
Break.Set    sYmbol.EXIT(sieve1) /Program /TraceOFF
```

3. Configure which messages are generated when the message generation is active (after TraceON event).



```

MCDS.TimeStamp ON ; enable Timestamp Messages

CLOCK.ON

MCDS.SOURCE.Set CpuMux0.Program ON ; enable Instruction Pointer
MCDS.SOURCE.Set CpuMux0.ReadAddr ON ; Call Messages for
MCDS.SOURCE.Set CpuMux0.WriteAddr ON ; TC 1.6.1 CPU0
MCDS.SOURCE.Set CpuMux0.WriteData ON ; enable Write Data Trace

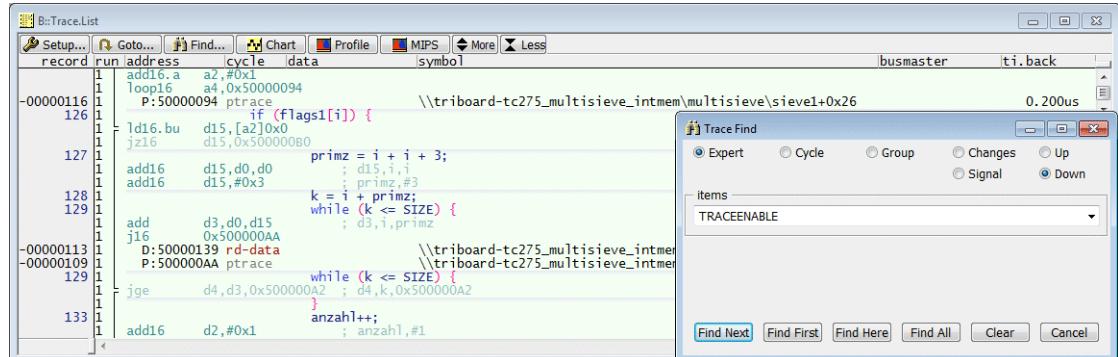
MCDS.SOURCE.Set CpuMux1.Program ON ; enable Instruction Pointer
MCDS.SOURCE.Set CpuMux1.ReadAddr ON ; Call Messages for
MCDS.SOURCE.Set CpuMux1.WriteAddr ON ; TC 1.6.1 CPU1
MCDS.SOURCE.Set CpuMux1.WriteData ON ; enable Write Data Trace

```

4. Start and stop the program execution.

5. Display the result.

TRACE ENABLE indicates the start of the message generation after the TraceON event occurred. It might be necessary to search for it.



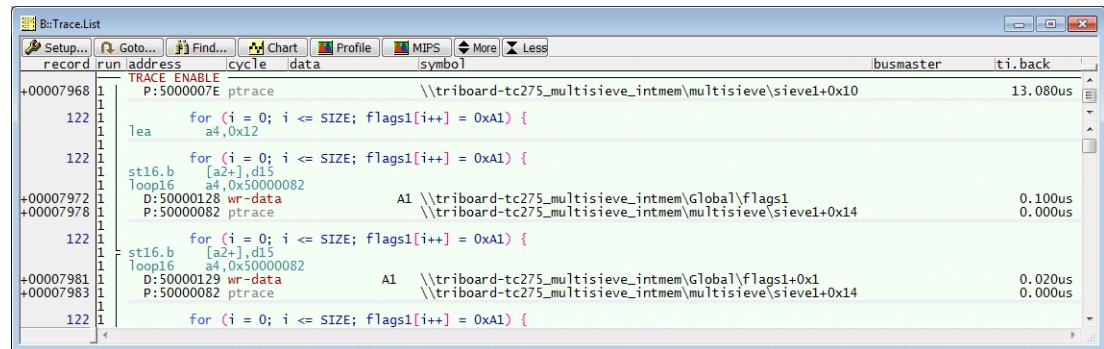
The screenshot shows the B:Trace.List window with assembly code. The assembly code is as follows:

```

record run address cycle data symbol busmaster ti.back
-00000116 1 add16 a a2,#0x0
1 loop16 a4,0x50000094
1 P:50000094 ptrace \\triboard-tc275_multisieve_intmem\multisieve\sieve1+0x26 0.200us
1 if (flags1[i]) {
1 ldi16 bu d15,[a2]0x0
1 jz16 d15,0x500000B0
126 1 add16 d15,d0,d0 primz = i + i + 3;
1 add16 d15,#0x3 : primz,#3
128 1 k = i + primz;
129 1 while (k <= SIZE) {
1 add d3,d0,d15 : d3,i,primz
1 j16 d:0x50000139 rd-data \\triboard-tc275_multisieve_intmem
1 P:500000AA ptrace \\triboard-tc275_multisieve_intmem
129 1 jge d4,d3,0x500000A2 ; d4,k,0x500000A2
133 1 anzahl++; : anzahl,#1
1 add16 d2,#0x1

```

A search dialog box titled "Trace Find" is overlaid on the window. The search criteria are set to "Expert" mode, with "Down" selected. The search term "TRACEENABLE" is entered in the "items" field. The search buttons at the bottom are "Find Next", "Find First", "Find Here", "Find All", "Clear", and "Cancel".



The screenshot shows the B:Trace.List window with assembly code. The assembly code is as follows:

```

record run address cycle data symbol busmaster ti.back
+00007968 1 TRACE ENABLE P:5000007E ptrace \\triboard-tc275_multisieve_intmem\multisieve\sieve1+0x10 13.080us
1 122 for (i = 0; i <= SIZE; flags1[i++] = 0xA1) {
1 122 1 lea a4,0x12
1 122 1 for (i = 0; i <= SIZE; flags1[i++] = 0xA1) {
1 122 1 st16_b [a2+],d15
1 122 1 loop16 a4,0x50000082 A1 \\triboard-tc275_multisieve_intmem\Global\flags1
+00007972 1 D:500000128 wr-data \\triboard-tc275_multisieve_intmem\multisieve\sieve1+0x14 0.100us
+00007978 1 P:50000082 ptrace 0.000us
1 122 for (i = 0; i <= SIZE; flags1[i++] = 0xA1) {
1 122 1 st16_b [a2+],d15
1 122 1 loop16 a4,0x50000082 A1 \\triboard-tc275_multisieve_intmem\Global\flags1+0x1
+00007981 1 D:500000129 wr-data A1 \\triboard-tc275_multisieve_intmem\multisieve\sieve1+0x14 0.020us
+00007983 1 P:50000082 ptrace 0.000us
1 122 for (i = 0; i <= SIZE; flags1[i++] = 0xA1) {

```

A search dialog box titled "Trace Find" is overlaid on the window. The search criteria are set to "Expert" mode, with "Down" selected. The search term "TRACEENABLE" is entered in the "items" field. The search buttons at the bottom are "Find Next", "Find First", "Find Here", "Find All", "Clear", and "Cancel".

Trace message generation is started after the TraceON event occurred. As a result this event is not visible in the trace.

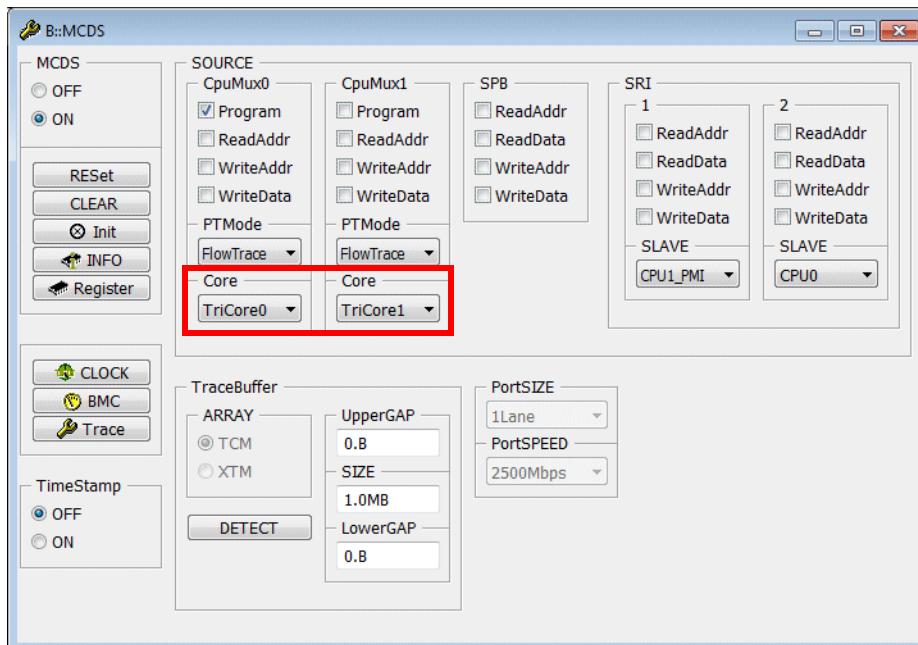
Trace Trigger (Onchip Trace Only)

Advise the Processor Observation Blocks to end the message generation at the specified event.

Example 1: Stop the trace recording when 161. was written to the variable flagsc[8].

- **System under debug:** SMP system with 3 TriCore cores.
- **Cores connected to the trace multiplexer:** TC 1.6.1 CPU0 and TC 1.6.1 CPU1.
- **Event of interest:** Write of 161. to variable flagsc[8].
- **Requested trace messages:** Instruction Pointer Call Messages, Write Data Trace Messages, Timestamp Messages.

1. Configure the trace multiplexer.



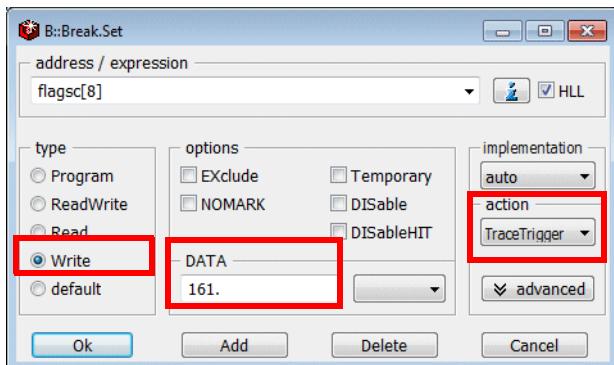
```
MCDS.SOURCE CpuMux0 Core TriCore0 ; enable TC 1.6.1 CPU0 as
```

```
; trace source
```

```
MCDS.SOURCE CpuMux1 Core TriCore1 ; enable TC 1.6.1 CPU1 as
```

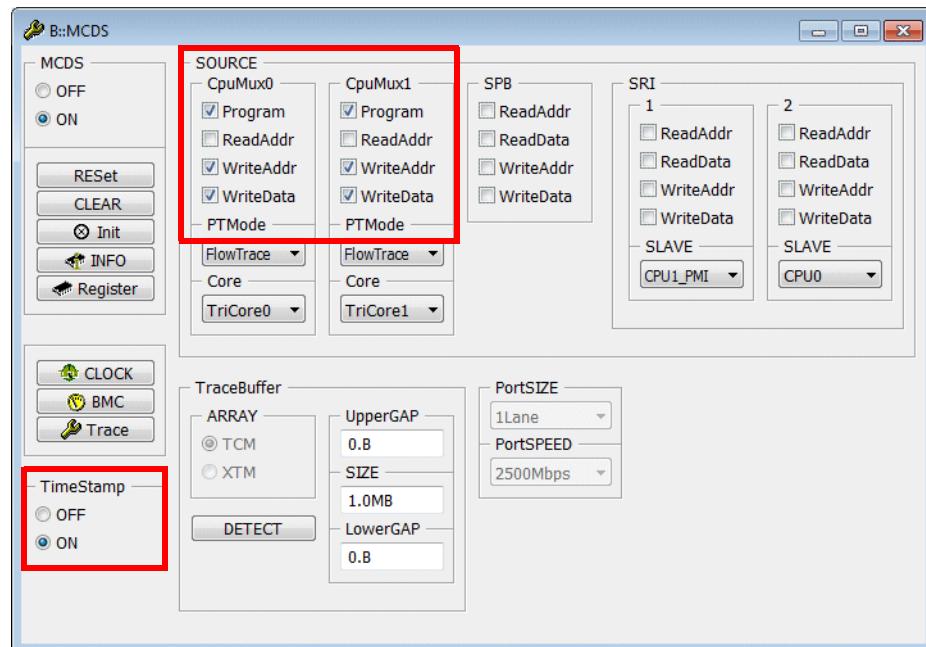
```
; trace source
```

2. Specify the event.



```
Var.Break.Set flagsc[8] /Write /DATA.Byte 0xA1 /TraceTrigger
```

3. Configure which trace messages are generated until the trigger event occurs.



```

MCDS.TimeStamp ON ; enable Timestamp Messages

CLOCK.ON

MCDS.SOURCE CpuMux0 Program ON ; enable Instruction Pointer
; Call Messages for
; TC 1.6.1 CPU0

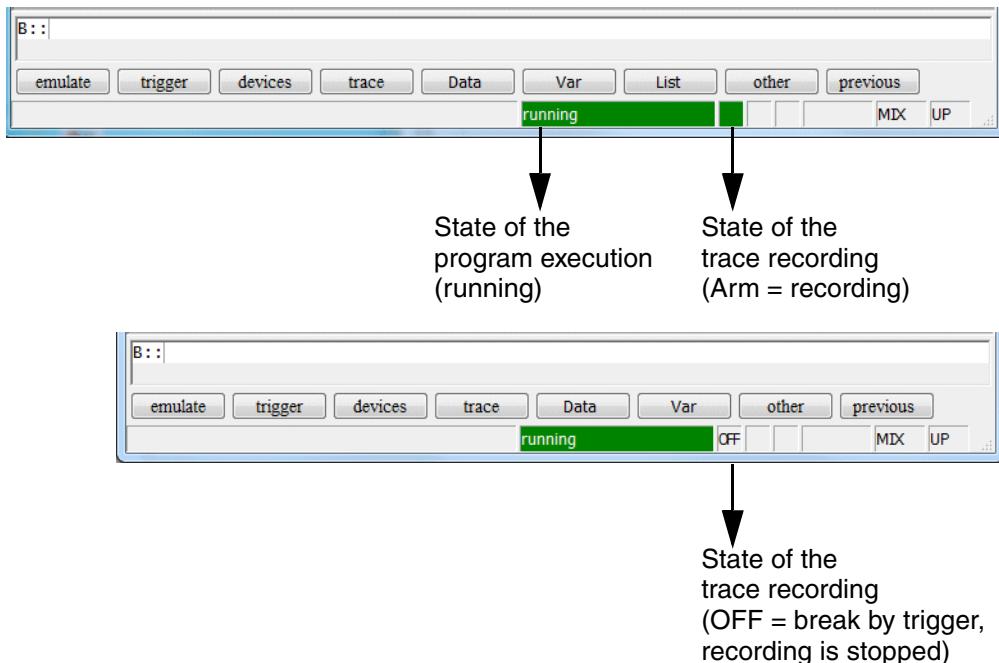
MCDS.SOURCE CpuMux0 WriteAddr ON ; enable Write Data Trace
MCDS.SOURCE CpuMux0 WriteData ON ; Messages for TC 1.6.1 CPU0

MCDS.SOURCE CpuMux1 Program ON ; enable Instruction Pointer
; Call Messages for
; TC 1.6.1 CPU1

MCDS.SOURCE CpuMux1 WriteAddr ON ; enable Write Data Trace
MCDS.SOURCE CpuMux1 WriteData ON ; Messages for TC 1.6.1 CPU1

```

4. Start the program execution.



5. Display the result.

MCDS ends the generation of trace messages and flushes all internal buffer when the specified event occurs. TRACE32 automatically generates a **watchpoint TraceTrigger** message when the trigger event occurs. This helps you to find the actual trigger event in the trace.

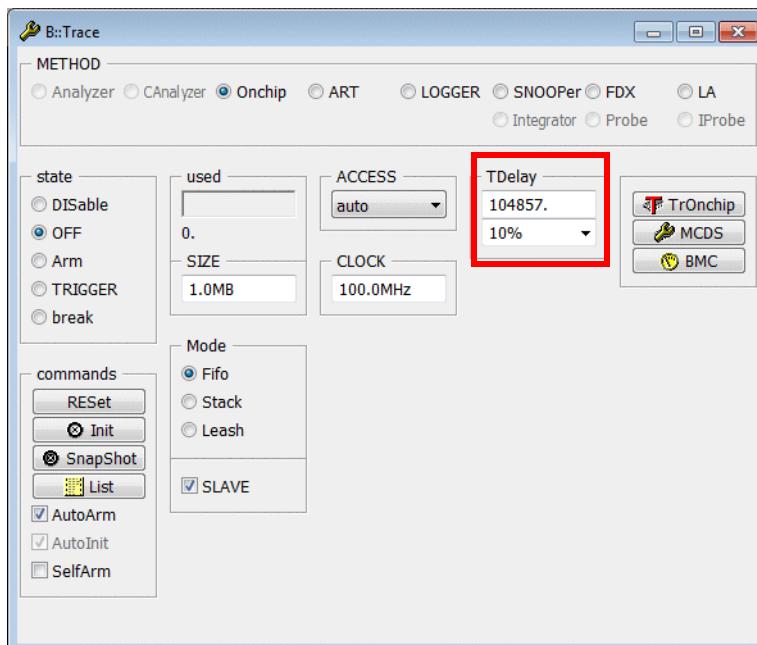
record	run	address	cycle	data	symbol	ti.back
-00000043	0	mov16 d15,#0x12				
		jge d15,d0,0x70100BEA ; d15,i,0x70100BEA				
-00000041	0	P:70100D2A ptrace				
-00000037	0	D:00000C49 wr-data	A0		\\triboard-tc275_multisieve_intmem\multisieve\sievevec+0x0E	0.140us
		P:70100BEA ptrace			\\triboard-tc275_multisieve_intmem\Global\flags0+0x9	0.100us
		movh.a a15,#0x0000			\\triboard-tc275_multisieve_intmem\multisieve\sieve0+0x6	0.040us
		lea a15,[a15]0xC40				
		addsc.a a15,d0,#0x0 ; a15,a15,i,#0				
		mov d15,#0x60				
		st16.b [a15]0x0,d15				
		add16 d0,#0x1 ; i,#1				
		mov16 d15,#0x12				
		jge d15,d0,0x70100BEA ; d15,i,0x70100BEA				
-00000034	0	D:00000C4A wr-data	A0		\\triboard-tc275_multisieve_intmem\Global\flags0+0x0A	0.080us
-00000030	0	P:70100BEA ptrace			\\triboard-tc275_multisieve_intmem\multisieve\sieve0+0x6	0.060us
-00000027	1	D:0000000C wr-data	A1		\\triboard-tc275_multisieve_intmem\Global\flagsc+0x8	0.220us
-00000024		• watchpoint TraceTrigger		wpt-mcx	01	

Example 2: Stop the trace recording after another 10% of the trace memory was filled when 161. was written to the variable flagsc[8].

- **System under debug:** SMP system with 3 TriCore cores.
- **Cores connected to the trace multiplexer:** TC 1.6.1 CPU0 and TC 1.6.1 CPU1.
- **Event of interest:** Write of 161. to variable flagsc[8].
- **Requested trace messages:** Instruction Pointer Call Messages, Write Data Trace Messages, Timestamp Messages.

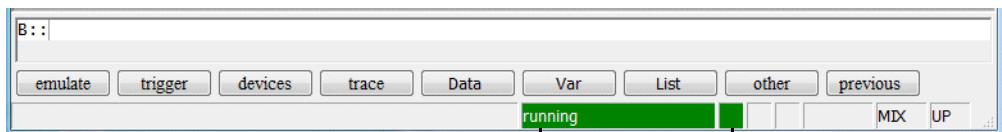
1. to 3. as in example 1.

4. Specific the delay.



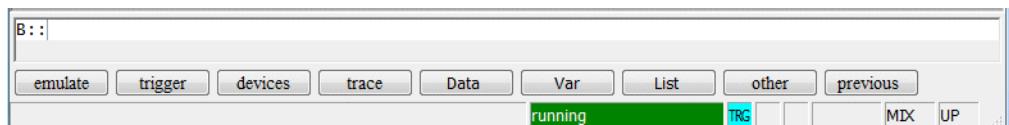
Trace.TDelay 10%

5. Start the program execution.

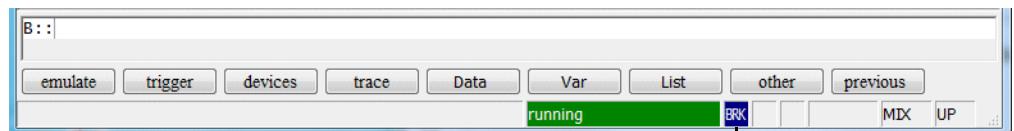


State of the
program execution
(running)

State of the
trace recording
(Arm = recording)

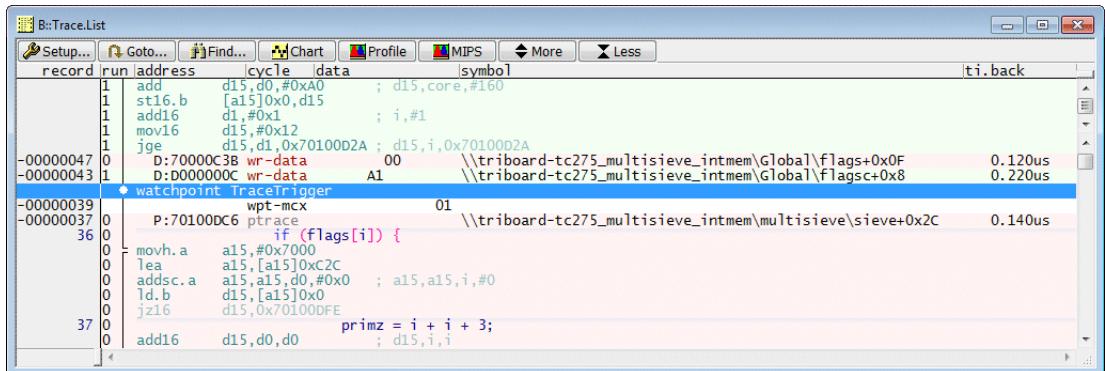


State of the
trace recording
(TRG = trigger occurred,
delay counter started)



State of the
trace recording
(BRK = delay counter elapsed,
recording is stopped)

6. Display the result.



The screenshot shows the Bi:Trace.List software interface. The window title is "B:Trace.List". The menu bar includes "Setup...", "Goto...", "Find...", "Chart", "Profile", "MIPS", "More", and "Less". The main area displays a trace list with the following columns: record, run, address, cycle, data, and symbol. The "symbol" column shows assembly code. A specific row is highlighted with a blue background, indicating a "watchpoint TraceTrigger". The assembly code in the highlighted row is as follows:

record	run	address	cycle	data	symbol
1					add d15,d0,#0xA0 ; d15,core,#160
1					st16.b [a15]0x0,d15
1					addi16 d1,#0x1 ; i,#1
1					movi16 d15,#0x12
1					jge d15,d1,0x70100D2A ; d15,i,0x70100D2A
-00000047	0	D:70000C3B	wr-data 00		\\triboard-tc275_multisieve_intmem\Global\f1ags+0x0F
-00000043	1	D:D000000C	wr-data A1		\\triboard-tc275_multisieve_intmem\Global\f1agsC+0x8
● watchpoint TraceTrigger					
-00000039		wpt-mcx 01			
-00000037	0	P:70100DC6	ptrace		\\triboard-tc275_multisieve_intmem\multisieve\sieve+0x2C
36	0				if (flags[i]) {
0	0	movh. a	a15,#0x7000		
0	0	lea	a15,[a15]0xC2C		
0	0	addsc. a	a15,a15,d0,#0x0		; a15,a15,i,#0
0	0	ld.b	d15,[a15]0x0		
0	0	jz16	d15,0x70100DFE		
37	0				primz = i + i + 3;
0	0	addi16	d15,d0,d0		; d15,i,i

A TraceTrigger watchpoint indicates the occurrence of the TraceTrigger event.

Activate the TRACE32 OS Awareness (Supported OS)

AMP Systems: Since each core is controlled by a separate operating system, the OS Awareness has to be activated separately for each TRACE32 instance.

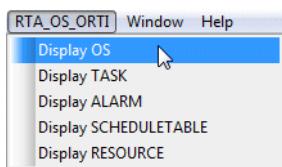
Since most users use an OSEK operating system this is taken as an example here. Setup command:

TASK.ORTI <orti_file>

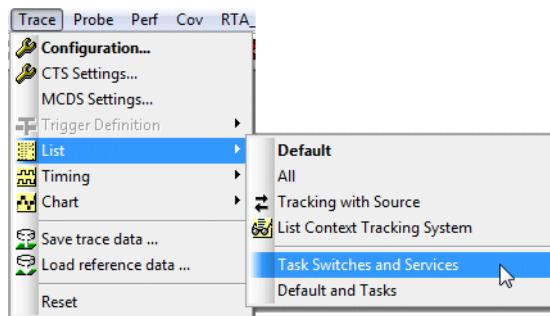
Load the ORTI file

Loading the ORTI file results in the following:

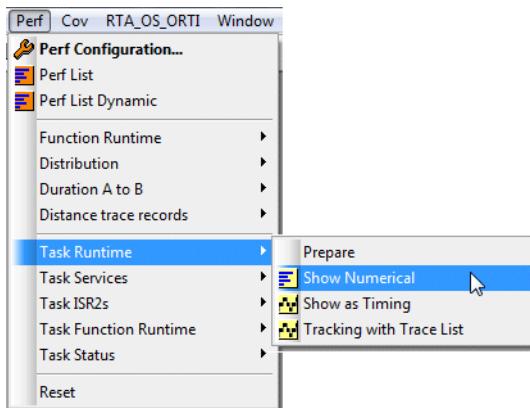
- Symbolic debugging of the OSEK OS is possible. Debug commands are provided via an ORTI menu.



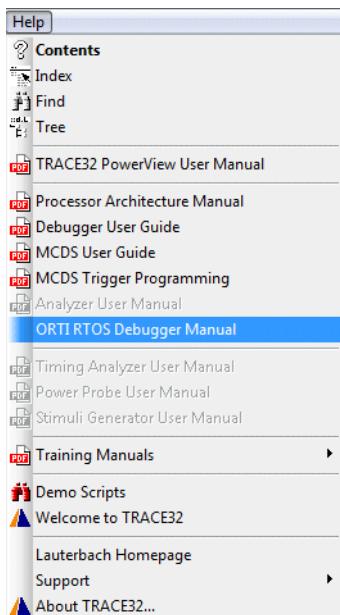
- The **Trace** menu is extended for OS-aware trace display.



- The **Perf** menu is extended for OS-aware profiling.



- The manual of the OS Awareness for OSEK/ORTI is added to the **Help** menu.



- The name of the current task is displayed in the **Task** field of the TRACE32 state line.



Exporting the Task Switches

Each operating system has a variable that contains the information which task is currently running. This variable can hold a task ID, a pointer to the task control block or something else that is unique for each task.

MCDS can be configured to generate a Write Data Trace Message when a write access to this variable occurs.

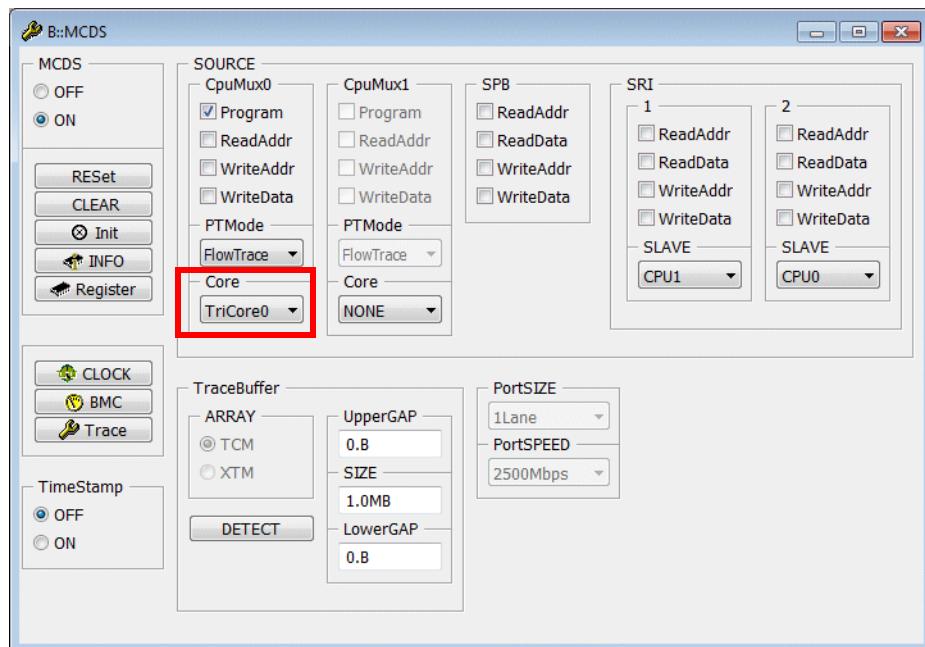
The address of this variable is provided by the TRACE32 function **TASK.CONFIG(magic)**.

```
PRINT TASK.CONFIG(magic) ; print the address that holds  
; the task identifier
```

Example: Advise the Processor Observation Block to generate trace messages only on task switches.

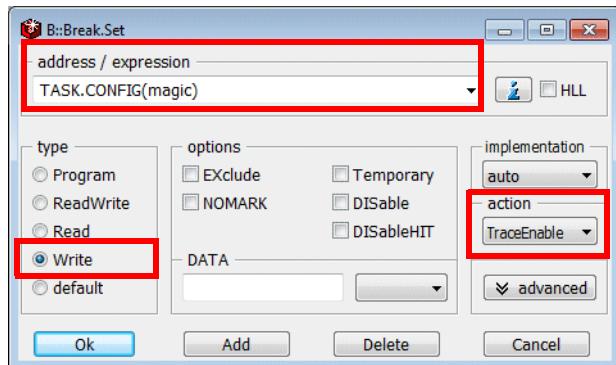
- **Core under debug:** TC 1.6.1 CPU0.
- **Event of interest:** Write access to TASK.CONFIG(magic)
- **Requested Messages:** Write Data Trace Messages, Timestamp Messages.

1. Configure the trace multiplexer.



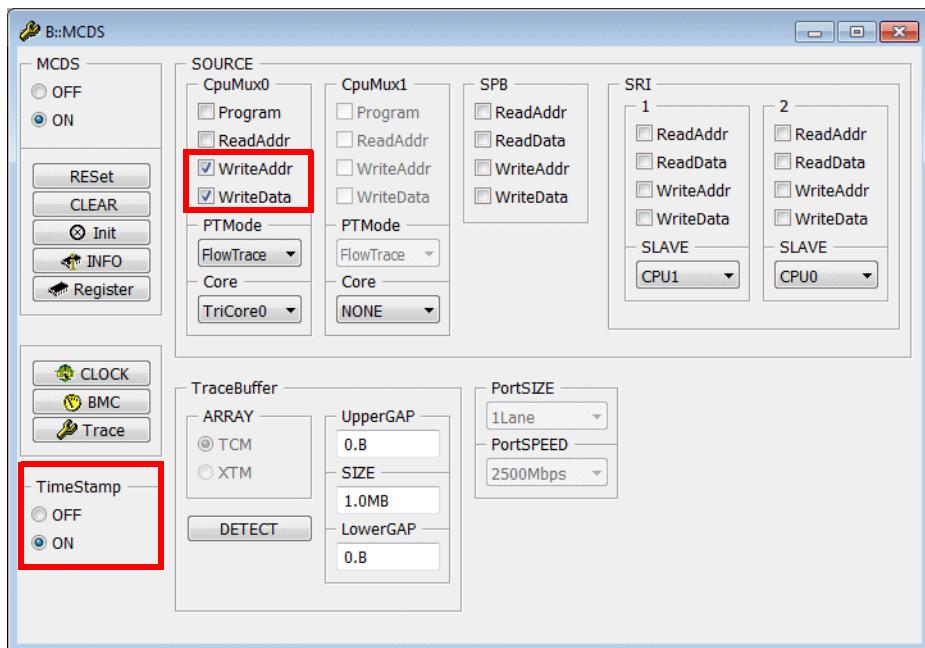
```
MCDS.SOURCE.Set CpuMux0.Core TriCore0 ; enable TC 1.6.1 CPU0 as  
; trace source
```

2. Specify the event.



```
Break.Set TASK.CONFIG(magic) /Write /TraceEnable
```

3. Configure which trace messages are generated.



```
MCDS.TimeStamp ON ; enable Timestamp Messages

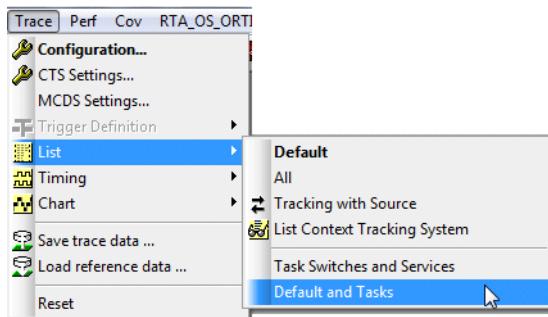
CLOCK.ON

MCDS.SOURCE.Set CpuMux0.Program OFF ; disable Instruction
; Pointer Call Messages for
; TC 1.6.1 CPU0

MCDS.SOURCE.Set CpuMux0.WriteAddr ON ; enable Write Data Trace
MCDS.SOURCE.Set CpuMux0.WriteData ON ; Messages for TC 1.6.1 CPU0
```

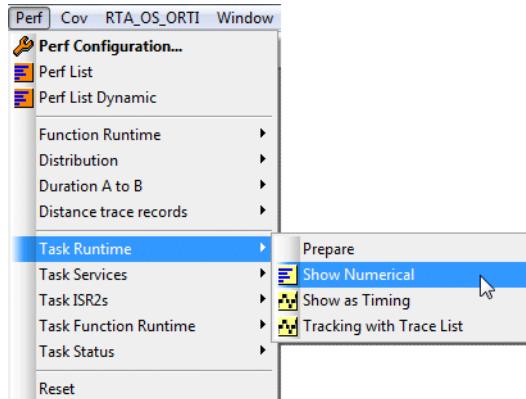
4. Start and stop the program execution to fill the trace buffer.

5. Display the result.



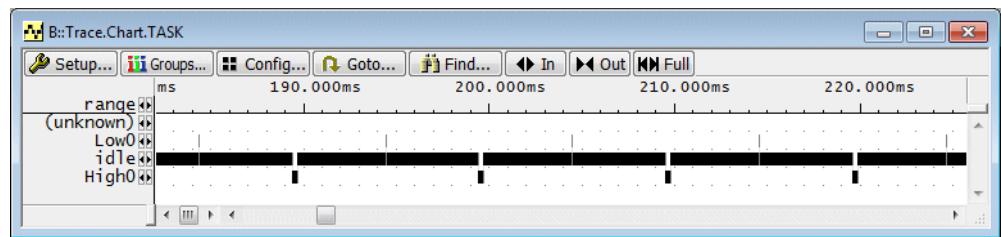
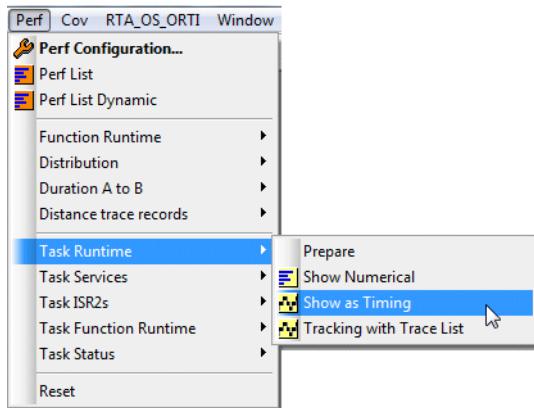
record	run address	cycle	data	symbol	busmaster	ti.back
-00006649	---					
	TASK = High0					
	D:900000BC wr-data	80000E70		\\APP\\Global\\Os_ControlledCoreInfo+0x4		
-00006478	---					
	TASK = idle					
	D:900000BC wr-data	00000000		\\APP\\Global\\Os_ControlledCoreInfo+0x4	279.240us	
-00003672	---					
	TASK = Low0					
	D:900000BC wr-data	80000E28		\\APP\\Global\\Os_ControlledCoreInfo+0x4	4.754ms	
-00003656	---					
	TASK = idle					
	D:900000BC wr-data	00000000		\\APP\\Global\\Os_ControlledCoreInfo+0x4	8.410us	
-0000692	---					
	TASK = High0					
	D:900000BC wr-data	80000E70		\\APP\\Global\\Os_ControlledCoreInfo+0x4	5.023ms	
-00000517	---					
	TASK = idle					
	D:900000BC wr-data	00000000		\\APP\\Global\\Os_ControlledCoreInfo+0x4	279.240us	
+*****						

The following two commands perform a statistical analysis of the task switches:



Trace information recorded before the first task switch is assigned to (unknown).

B:Trace.STATistic.TASK									
	range	total	min	max	avr	count	ratio%	1%	2%
	(Unknown)	3.155ms	3.155ms	3.155ms	3.155ms	0.	0.178%		
	Low0	1.467ms	8.380us	8.380us	8.380us	175.	0.082%		
	idle	1.715s	4.748ms	13.803ms	4.915ms	349.	96.987%		
	High0	48.654ms	278.960us	286.240us	279.623us	174.	2.751%		



Trace.Chart.TASK

Time-chart of tasks.

Exporting Task Services

The ORTI file may also provide means to analyze the time in task service routines.

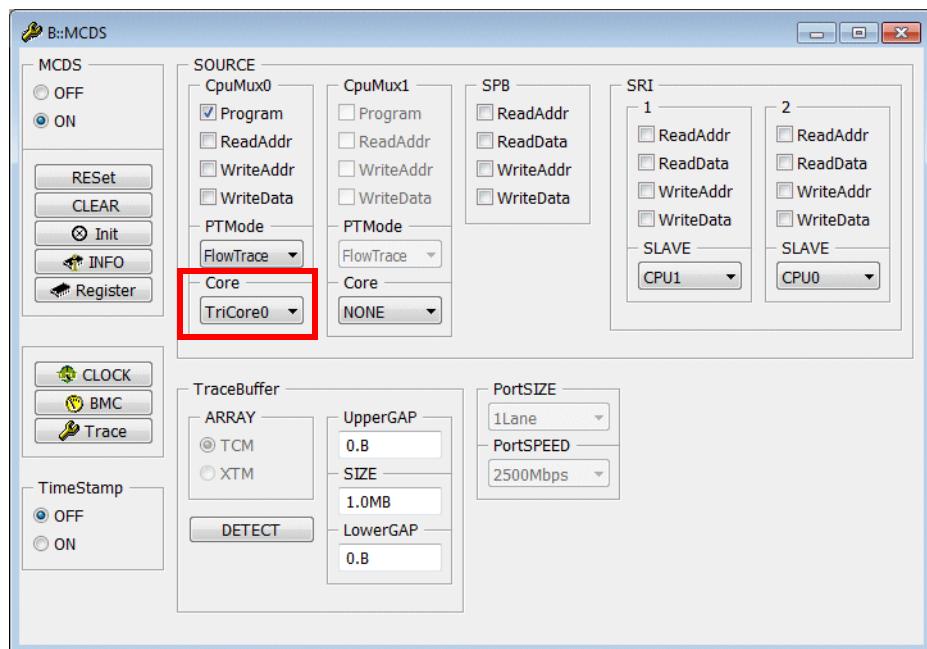
TASK.CONFIG(magic_service) is the name of the TRACE32 function that is used for this purpose.

```
PRINT TASK.CONFIG(magic_service) ; print the address that holds  
; the service table
```

Example: Advise the Processor Observation Block to generate trace messages only on write accesses to the service table.

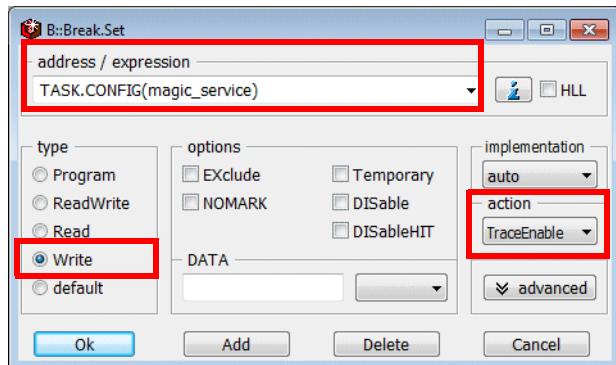
- **Core under debug:** TC 1.6.1 CPU0.
- **Event of interest:** Write access to TASK.CONFIG(magic_service)
- **Requested Messages:** Write Data Trace Messages, Timestamp Messages.

1. Configure the trace multiplexer.



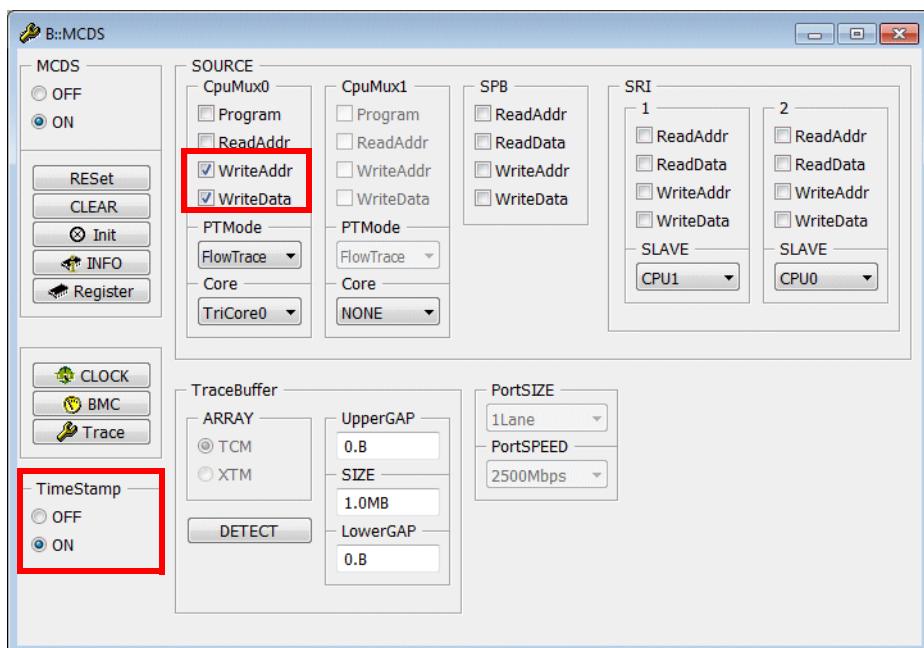
```
MCDS.SOURCE.Set CpuMux0.Core TriCore0 ; enable TC 1.6.1 CPU0 as  
; trace source
```

2. Specify the event.



```
Break.Set TASK.CONFIG(magic_service) /Write /TraceEnable
```

3. Configure which trace messages are generated.



```
MCDS.TimeStamp ON ; enable Timestamp Messages

CLOCK.ON

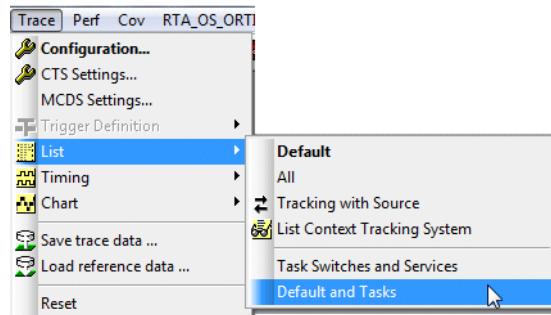
MCDS.SOURCE.Set CpuMux0.Program OFF ; disable Instruction
; Pointer Call Messages for
; TC 1.6.1 CPU0

MCDS.SOURCE.Set CpuMux0.WriteAddr ON ; enable Write Data Trace
; Messages for TC 1.6.1 CPU0

MCDS.SOURCE.Set CpuMux0.WriteData ON
```

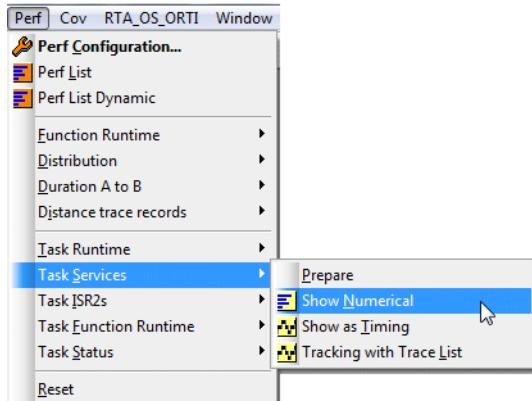
4. Start and stop the program execution to fill the trace buffer.

5. Display the result.



record	run	address	cycle	data	busmaster	ti.back
-00001155	---	SERVICE = ActivateTask entry		D:9000008B wr-data 03	\\APP\\Global\\Os_AnyCoreInfo+0x0F	2.790us
-00001151	---	SERVICE = ActivateTask exit		D:9000008B wr-data 02	\\APP\\Global\\Os_AnyCoreInfo+0x0F	2.030us
-00001145	---	SERVICE = IncrementCounter exit		D:9000008B wr-data 34	\\APP\\Global\\Os_AnyCoreInfo+0x0F	2.740us

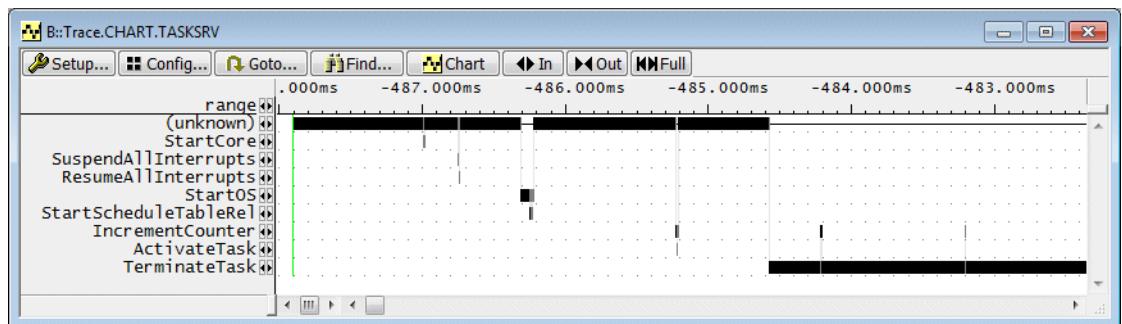
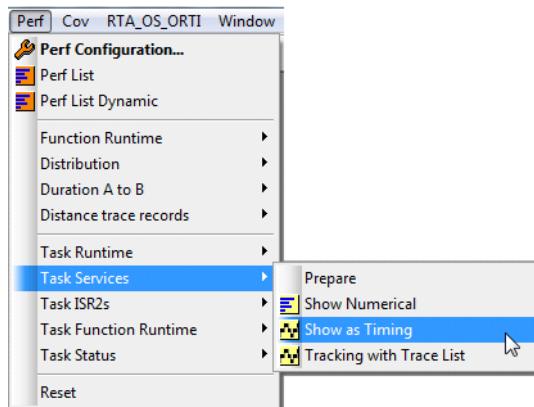
The following two commands perform a statistical analysis of the time in task service routines:



(unknown) represents the time in which the core is not in an OSEK service routine

B::Trace.STATistic.TASKSRV								
	range	total	min	max	avr	count	ratio%	1%
(unknown)	2.318ms	-	2.318ms	2.318ms	2.318ms	0.	-	4
SuspendAllInterrupts	2.690us	0.630us	1.100us	0.897us	3.	<0.001%	4	
ResumeAllInterrupts	0.600us	0.600us	0.600us	0.600us	0.600us	1.	<0.001%	4
StartOS	0.320us	0.320us	0.320us	0.320us	0.320us	1.	<0.001%	4
StartScheduleTableRel	87.300us	87.300us	87.300us	87.300us	87.300us	1.	0.015%	4
IncrementCounter	13.240us	4.360us	4.440us	4.413us	3.	0.002%	4	
ActivateTask	2.004ms	0.850us	14.320us	3.176us	966.	0.371%	4	
TerminateTask	194.030us	1.730us	3.760us	2.000us	97.	0.039%	4	
	482.582ms	-	5.289ms	482.582ms	0.	99.094%		

Trace.STATistic.TASKSRV Numeric analysis of task services.



Trace.Chart.TASKSRV

Time-chart of task services.

Exporting ISR2 (OSEK Interrupt Service Routines)

The ORTI file may also provide means to analyze the time in interrupt service routines.

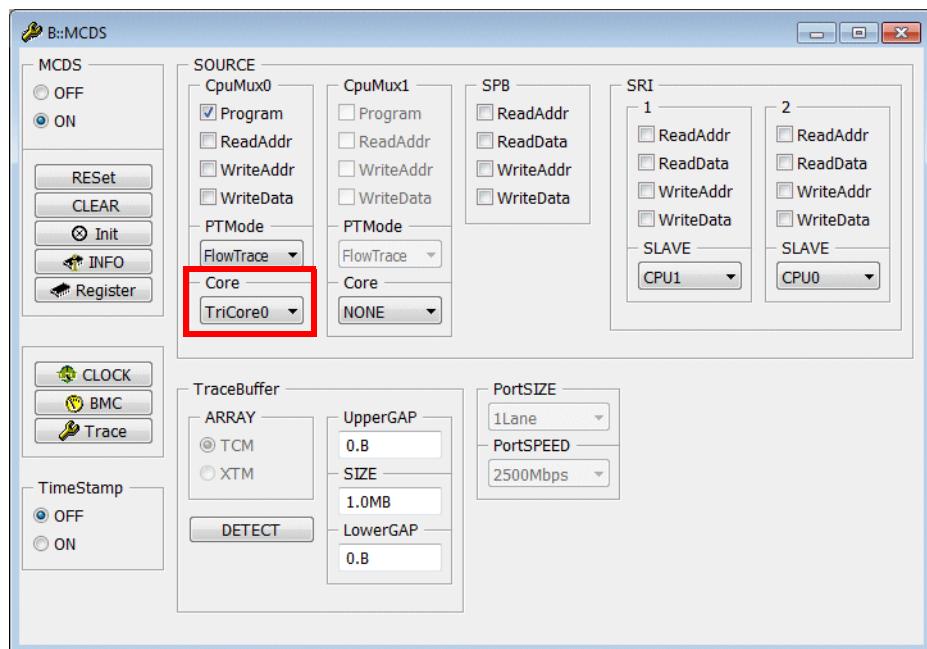
TASK.CONFIG(magic_isr2) is the name of the TRACE32 function that is used for this purpose.

```
PRINT TASK.CONFIG(magic_isr2) ; print the address that holds  
; the interrupt service table
```

Example: Advise the Processor Observation Block to generate trace messages only on write accesses to the interrupt service table.

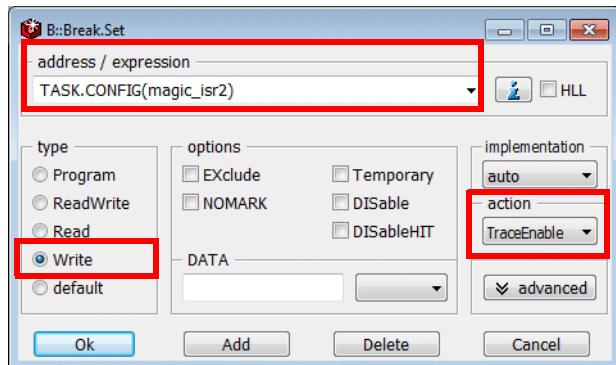
- **Core under debug:** TC 1.6.1 CPU0.
- **Event of interest:** Write access to TASK.CONFIG(magic_isr2)
- **Requested Messages:** Write Data Trace Messages, Timestamp Messages.

1. Configure the trace multiplexer.



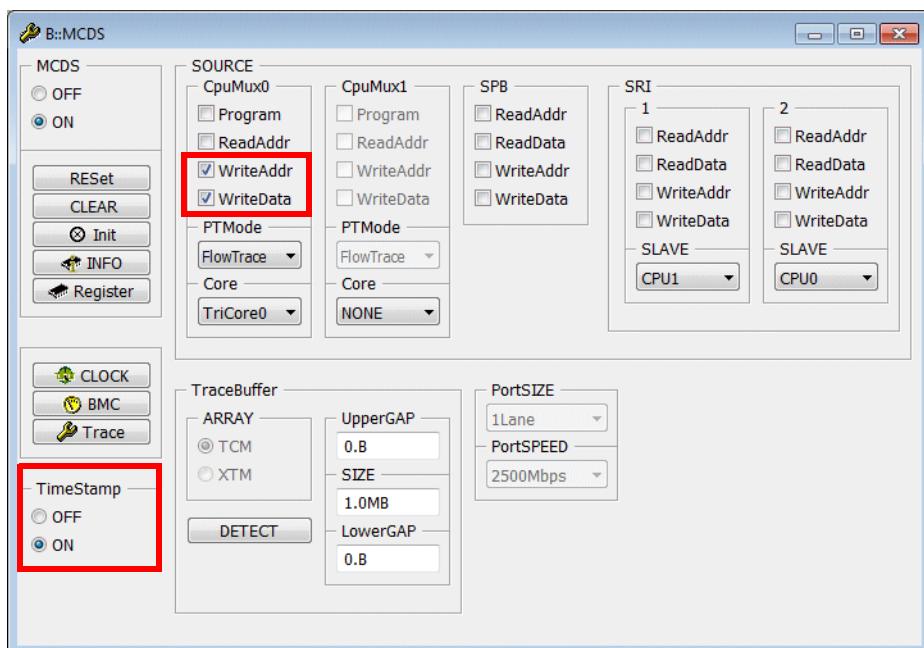
```
MCDS.SOURCE.Set CpuMux0.Core TriCore0 ; enable TC 1.6.1 CPU0 as  
; trace source
```

2. Specify the event.



```
Break.Set TASK.CONFIG(magic_isr2) /Write /TraceEnable
```

3. Configure which trace messages are generated.



```
MCDS.TimeStamp ON ; enable Timestamp Messages

CLOCK.ON

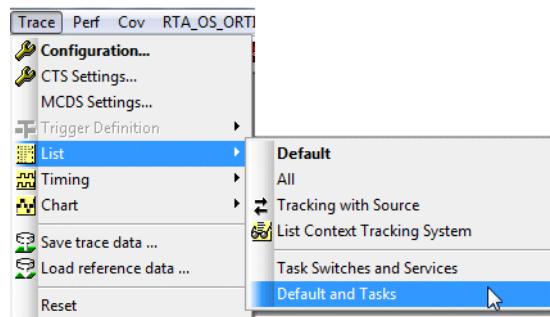
MCDS.SOURCE.Set CpuMux0.Program OFF ; disable Instruction
; Pointer Call Messages for
; TC 1.6.1 CPU0

MCDS.SOURCE.Set CpuMux0.WriteAddr ON ; enable Write Data Trace
; Messages for TC 1.6.1 CPU0

MCDS.SOURCE.Set CpuMux0.WriteData ON
```

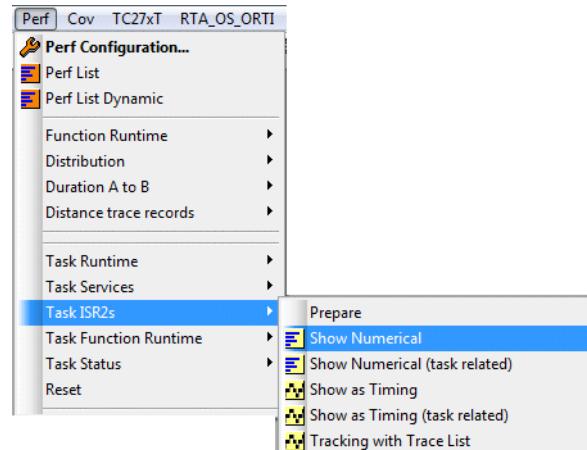
4. Start and stop the program execution to fill the trace buffer.

5. Display the result.



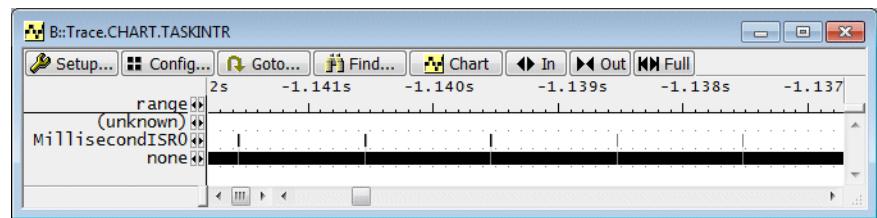
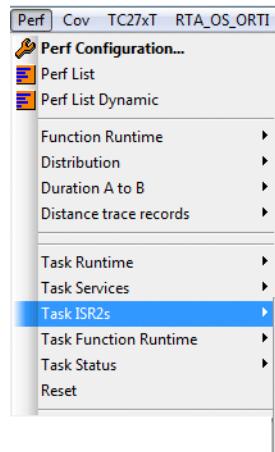
	record	run	address	cycle	data	symbol	busmaster	ti.back
-00005465	---		ISR2 = MillisecondISR0	---		80000D2C \\APP\\Global\\0s_ControlledCoreInfo		1.001ms
			D:900000B8	wr-data				
-00005456	---		ISR2 = none	---		00000000 \\APP\\Global\\0s_ControlledCoreInfo		4.890us
-00004858	---		ISR2 = MillisecondISR0	---		80000D2C \\APP\\Global\\0s_ControlledCoreInfo		1.001ms
			D:900000B8	wr-data				
-00004841	---		ISR2 = none	---		00000000 \\APP\\Global\\0s_ControlledCoreInfo		11.670us
-00004245	---		ISR2 = MillisecondISR0	---		80000D2C \\APP\\Global\\0s_ControlledCoreInfo		1.001ms
			D:900000B8	wr-data				

The following two commands perform a statistical analysis of the time in interrupt service routines:



B:Trace.STATistic.TASKINTR									
		intrs: 3.		total: 1.279s					
range	total	min	max	avr	count	ratio%	1%	2%	
(unknown)	0.000us	0.000us	-	0.000us	1.	0.000%			
MillisecondISR0	7.797ms	4.440us	11.670us	6.134us	1271.	0.609%	←		
none	1.272s	1.000ms	1.001ms	1.001ms	1270.	99.391%			

Trace.STATistic.TASKINTR Numeric analysis of ISR2s.

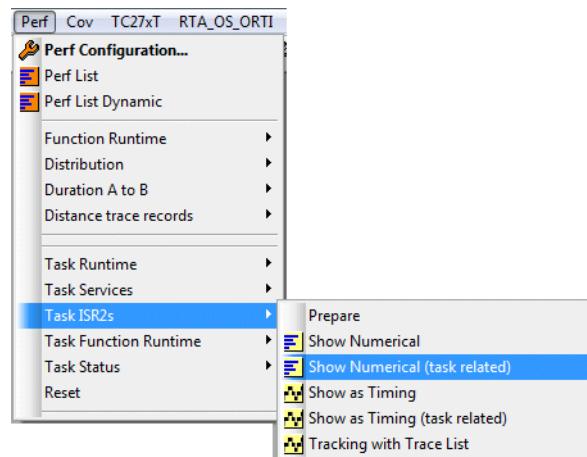


Trace.Chart.TASKINTR

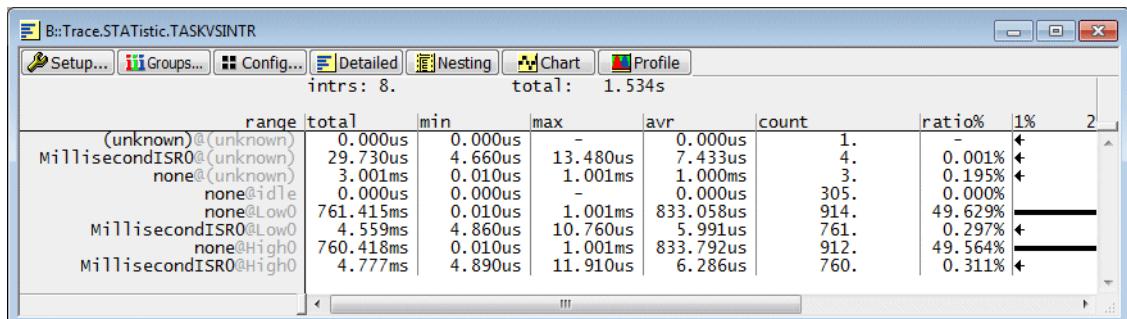
Time-chart of ISR2s.

Exporting Task Switches and ISR2

The following commands allow to perform a statistical analysis of the OSEK interrupt service routines related to the active tasks, if you export task switch and ISR2 information.

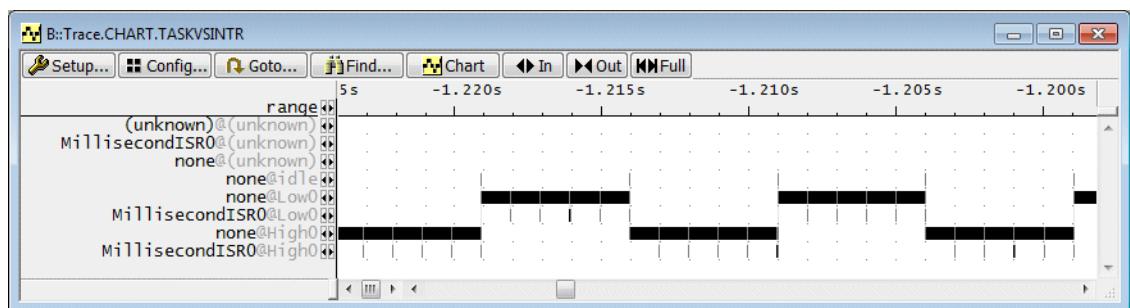
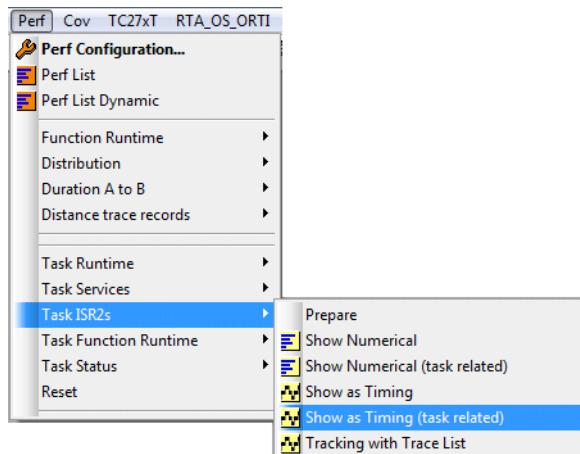


ISR2 information that was generated before the first task information is assigned to the @ (unknown) task



Trace.STATistic.TASKVSINTR

Task-related statistic on interrupt service routines



Trace.Chart.TASKVSINTR

Time-chart on task related interrupt service routines

Exporting Task Switches and all Instructions

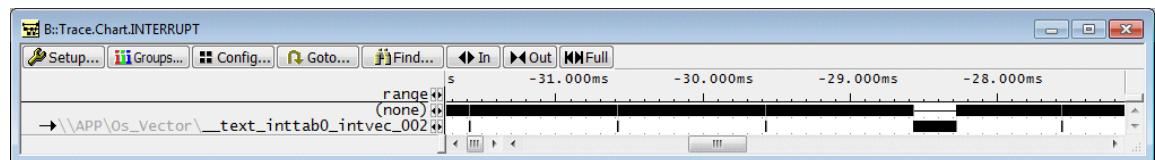
General setup:

```
Break.Set TASK.CONFIG(magic) /Write /TraceData
; advise TRACE32 to regard the time between interrupt entry
; and exit as function
Trace.STATistic.InterruptIsFunction ON
```

Statistic Analysis of Interrupts

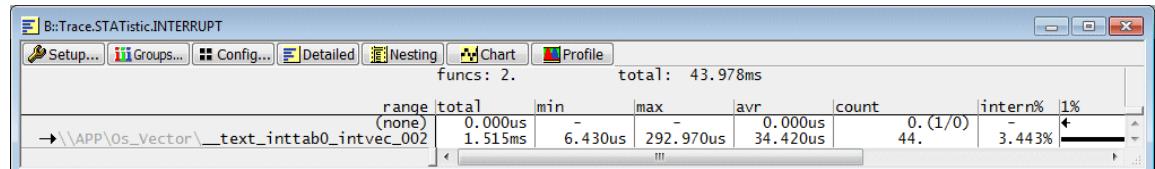
Trace.Chart.INTERRUPT

Interrupt time chart



Trace.STATistic.INTERRUPT

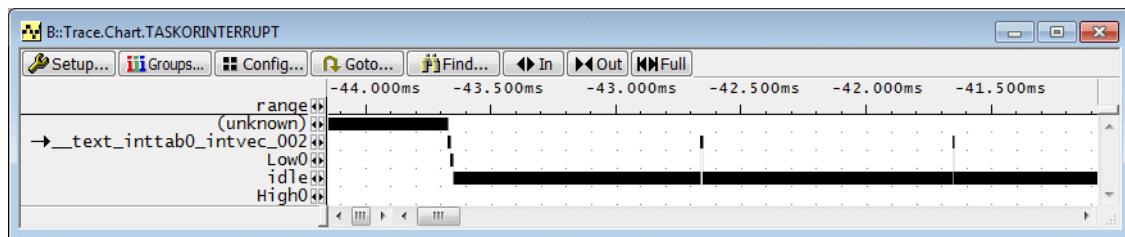
Interrupt statistic



Statistic Analysis of Interrupts and Tasks

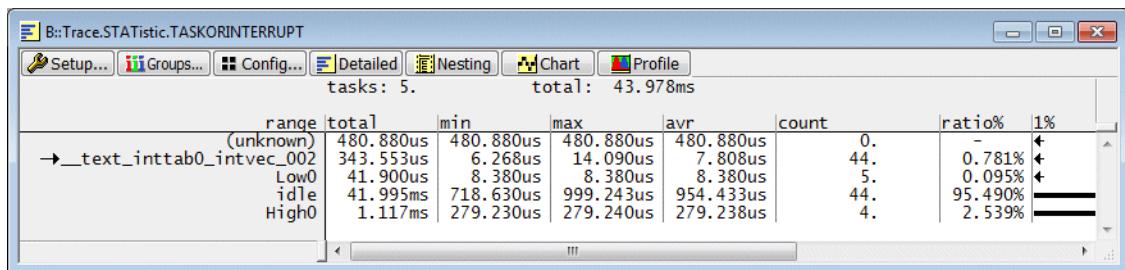
Trace.Chart.TASKORINTERRUPT

Time chart of interrupts and tasks



Trace.STATistic.TASKORINTERRUPT

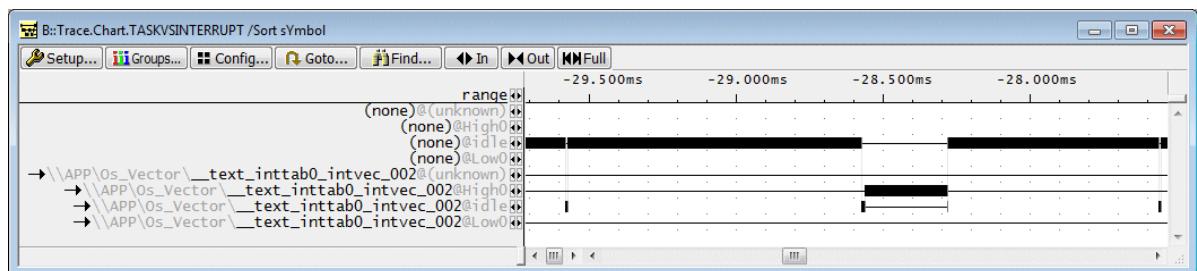
Statistic of interrupts and tasks



Statistic Analysis of Interrupts in Tasks

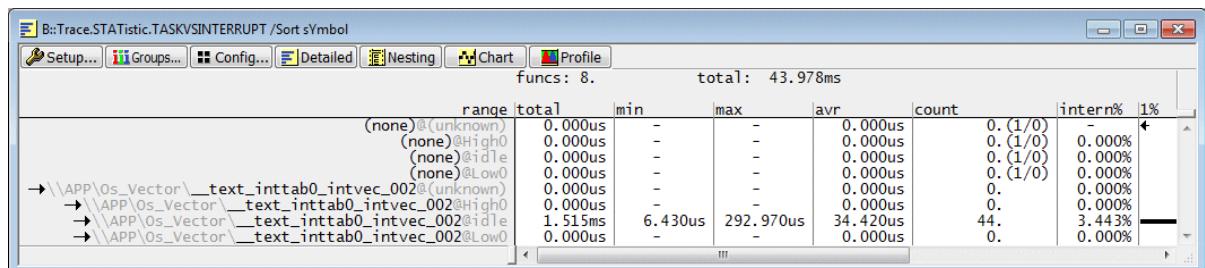
Trace.Chart.TASKVSINTERRUPT

Time chart interrupts, task-related



Trace.STATistic.TASKVSINTERRUPT

Statistic of interrupts, task-related



Belated Trace Analysis (OS)

The TRACE32 Instruction Set Simulator can be used for a belated OS-aware trace evaluation. To set up the TRACE32 Instruction Set Simulator for belated OS-aware trace evaluation proceed as follows:

1. Save the trace information for the belated evaluation to a file.

```
Trace.SAVE belated_orti.ad
```

2. Set up the TRACE32 Instruction Set Simulator for a belated OS-aware trace evaluation (here OSEK on a TC277TE):

```
SYStem.CPU TC277TE ; select the target CPU
SYStem.Up ; establish the
            ; communication between
            ; TRACE32 and the TRACE32
            ; Instruction Set
            ; Simulator
Trace.LOAD belated_orti.ad ; load the trace file
Data.Load.Elf my_app.out ; load the symbol and
                        ; debug information
TASK.ORTI my_orti.ort ; load the ORTI file
Trace.List List.TASK DEFault ; display the trace
                            ; listing
```

Enable an OS-aware Tracing (Not-Supported OS)

If you use an OS that is not supported by Lauterbach you can use the “simple” awareness to configure your debugger for OS-aware tracing.

Current information on the “simple” awareness can be found in `~/demo/kernel/simple/readme.txt`.

Each operating system has a variable that contains the information which task is currently running. This variable can hold a task ID, a pointer to the task control block or something else that is unique for each task.

Use the following command to inform TRACE32 about this variable:

```
TASK.CONFIG ~~/demo/kernel/simple/simple.t32 <var> Var.SIZEOF(<var>)
```

If `current_thread` is the name of your variable the command would be as follows:

```
TASK.CONFIG ~~/demo/kernel/simple/simple current_thread \
Var.SIZEOF(current_thread)
```

The OS-aware debugging is easier to perform, if you assign names to your tasks.

TASK.NAME.Set `<task_id> <name>` Specify a name for your task

TASK.NAME.view Display all specified names

```
TASK.NAME.Set 0x58D68 "My_Task 1"
```

OS-Aware Tracing - SMP Systems

All cores are controlled by an SMP operating system.

Activate the TRACE32 OS Awareness (Supported OS)

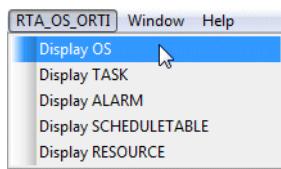
Since most users use an OSEK operating system this is taken as an example here. Setup command:

| TASK.ORTI <orti_file>

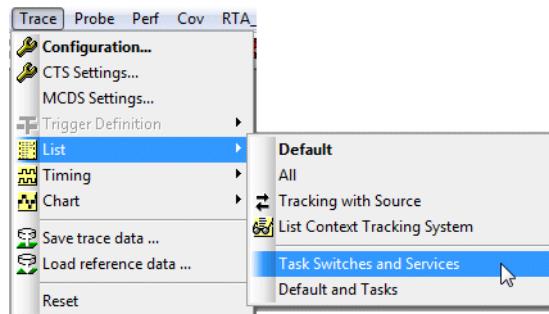
Load the ORTI file

Loading the ORTI file results in the following:

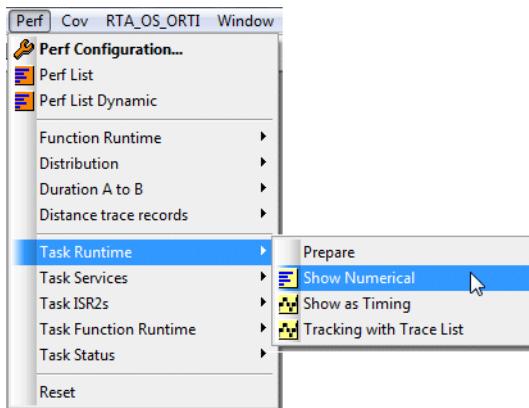
- Symbolic debugging of the OSEK OS is possible. Debug commands are provided via an ORTI menu.



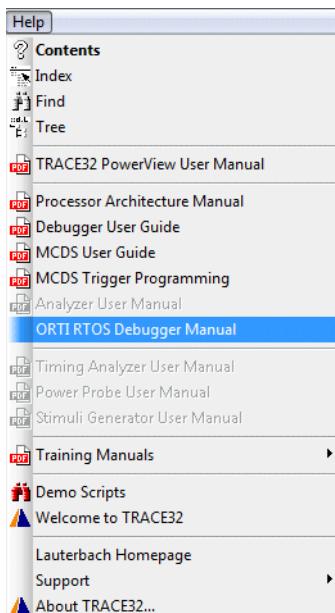
- The **Trace** menu is extended for OS-aware trace display.



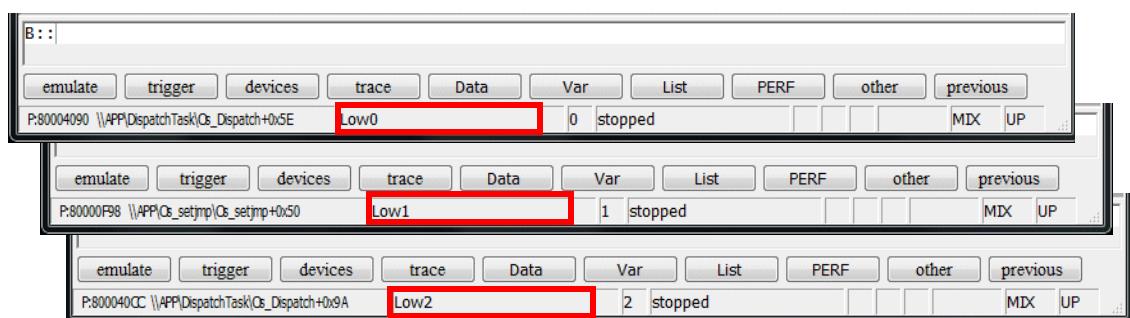
- The **Perf** menu is extended for OS-aware profiling.



- The manual of the OS Awareness for OSEK/ORTI is added to the **Help** menu.



- The name of the current task is displayed in the **Task** field of the TRACE32 state line.



Exporting the Task Switches

An SMP operating system has one variable per core that contains the information which task is currently running. This variable can hold a task ID, a pointer to the task control block or something else that is unique for each task.

MCDS can be configured to generate Write Data Trace Messages when a write accesses to these variables occur.

The addresses of these variables are provided by the TRACE32 functions **TASK.CONFIG(magic[<core>])**.

```
PRINT TASK.CONFIG(magic[0])          ; print the address that holds
                                         ; the task identifier for
                                         ; TC 1.6.1 CPU0

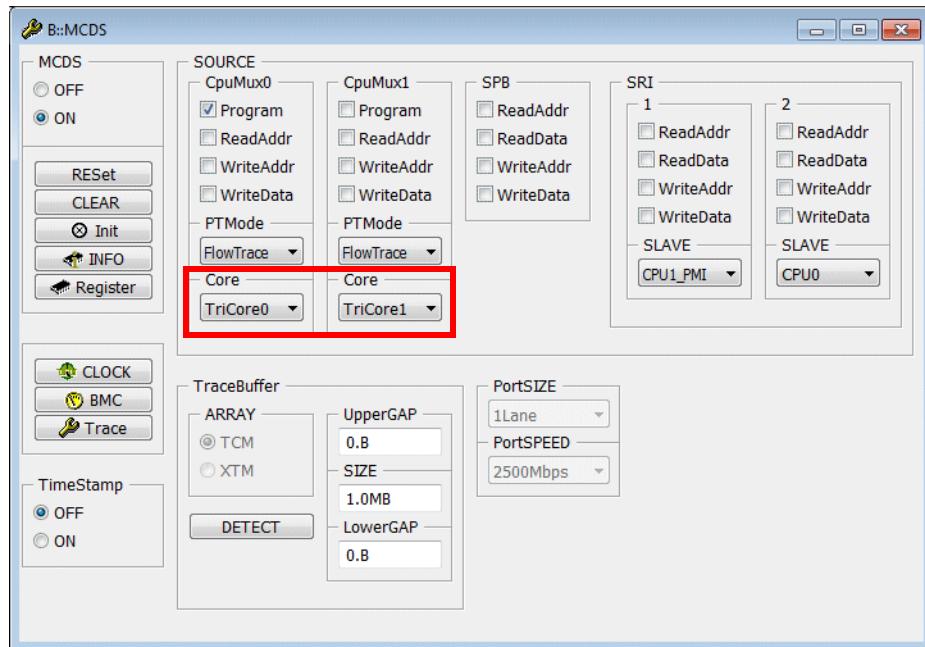
PRINT TASK.CONFIG(magic[1])          ; print the address that holds
                                         ; the task identifier for
                                         ; TC 1.6.1 CPU1

PRINT TASK.CONFIG(magic[2])          ; print the address that holds
                                         ; the task identifier for
                                         ; TC 1.6.1 CPU2
```

Example: Advise the Processor Observation Blocks to generate trace messages only on task switches.

- **System under debug:** SMP system with 3 TriCore cores.
- **Cores under debug:** TC 1.6.1 CPU0 and TC 1.6.1 CPU1.
- **Event of interest:** Write accesses to TASK.CONFIG(magic[0]) and TASK.CONFIG(magic[1]).
- **Requested Messages:** Write Data Trace Messages, Timestamp Messages.

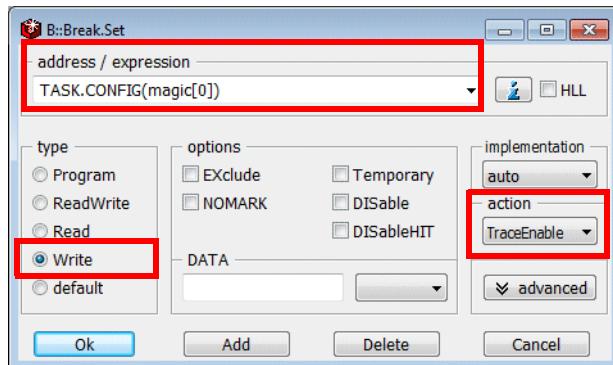
1. Configure the trace multiplexer.



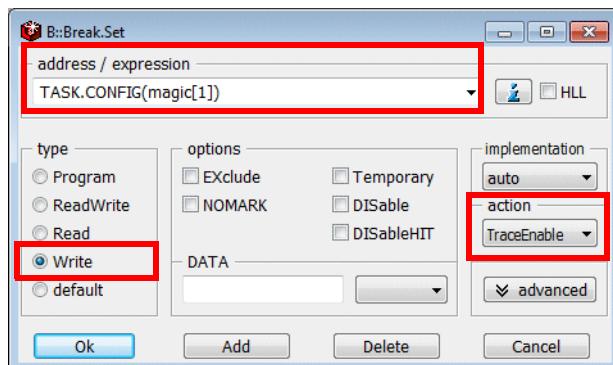
```
MCDS.SOURCE.Set CpuMux0.Core TriCore0 ; enable TC 1.6.1 CPU0 as
; trace source

MCDS.SOURCE.Set CpuMux1.Core TriCore1 ; enable TC 1.6.1 CPU1 as
; trace source
```

2. Specify the events.

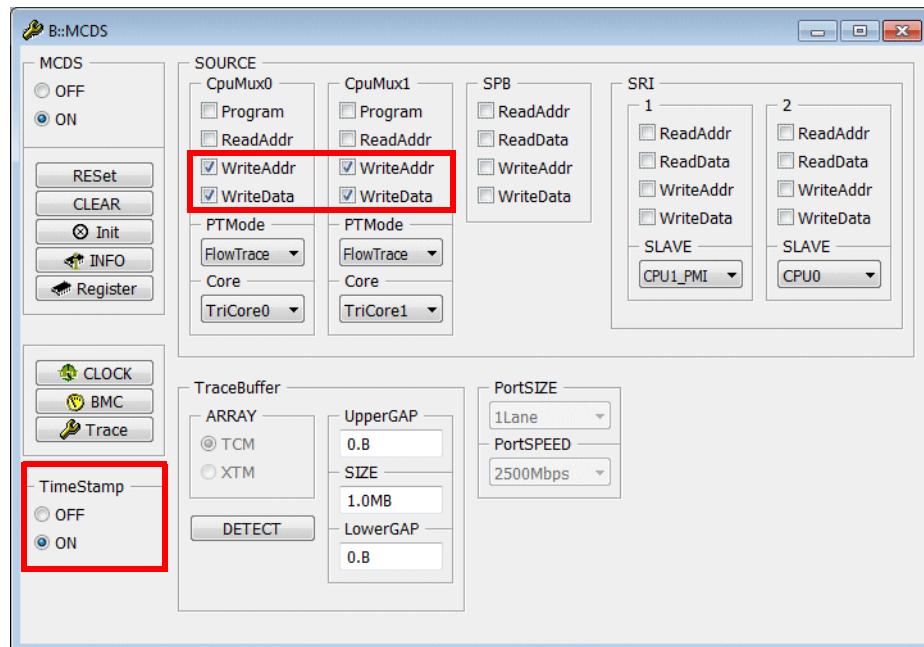


```
Break.Set TASK.CONFIG(magic[0]) /Write /TraceEnable
```



```
Break.Set TASK.CONFIG(magic[1]) /Write /TraceEnable
```

3. Configure which trace messages are generated.



```
MCDS.TimeStamp ON ; enable Timestamp Messages

CLOCK.ON

MCDS.SOURCE.Set CpuMux0.Program OFF ; disable Instruction
; Pointer Call Messages for
; TC 1.6.1 CPU0

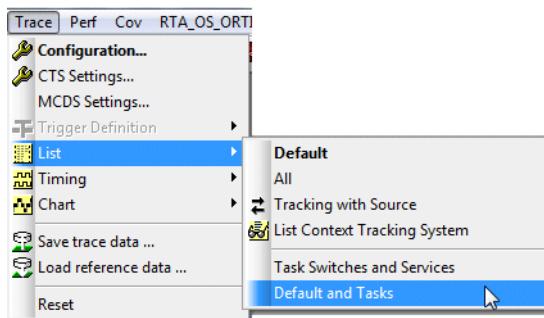
MCDS.SOURCE.Set CpuMux1.Program OFF ; disable Instruction
; Pointer Call Messages for
; TC 1.6.1 CPU1

MCDS.SOURCE.Set CpuMux0.WriteAddr ON ; enable Write Data Trace
MCDS.SOURCE.Set CpuMux0.WriteData ON ; Messages for TC 1.6.1 CPU0

MCDS.SOURCE.Set CpuMux1.WriteAddr ON ; enable Write Data Trace
MCDS.SOURCE.Set CpuMux1.WriteData ON ; Messages for TC 1.6.1 CPU1
```

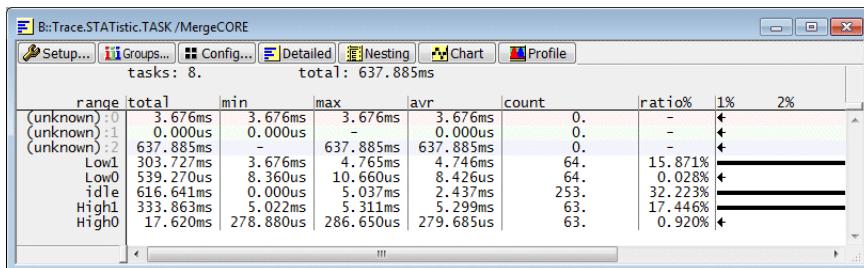
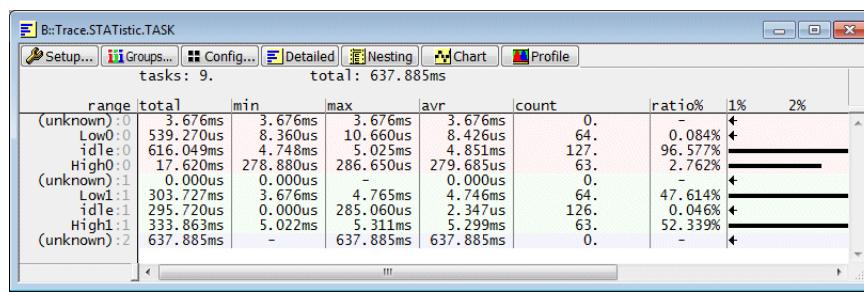
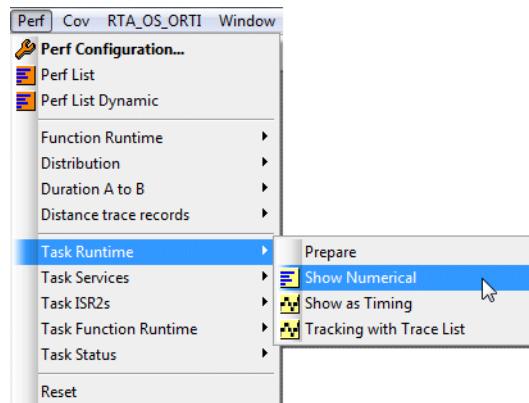
4. Start the program execution and stop it.

5. Display the result.



record	run	address	cycle	data	symbol	busmaster	ti.back
-00004065	0	D:900000BC	wr-data	80000E28	\\APP\\Global\\Os_ControlledCoreInfo+0x4		4.755ms
-00004049	0	D:900000BC	wr-data	00000000	\\APP\\Global\\Os_ControlledCoreInfo+0x4		8.380us
-00002405	1	D:900000DC	wr-data	80000E88	\\APP\\Global\\Os_ControlledCoreInfo+0x24		5.020ms
-00002393	1	D:900000DC	wr-data	00000000	\\APP\\Global\\Os_ControlledCoreInfo+0x24		7.780us
-00001069	0	D:900000BC	wr-data	80000E70	\\APP\\Global\\Os_ControlledCoreInfo+0x4		5.024ms
-00000894	0	D:900000BC	wr-data	00000000	\\APP\\Global\\Os_ControlledCoreInfo+0x4		279.290us

The following two commands perform a statistical analysis of the task switches:



Trace information recorded before the first task switch is assigned to (unknown).

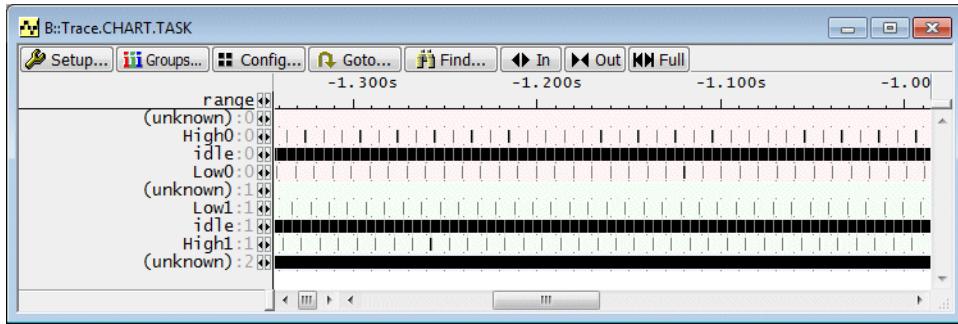
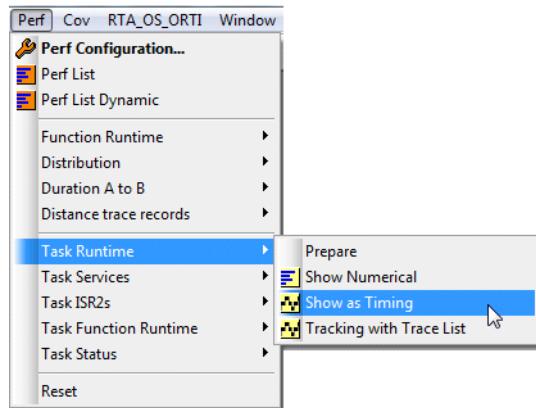
Since no trace information is recorded for TC 1.6.1 CPU2, it stays (unknown) for the total recording time.

Trace.STATistic.TASK [/SplitCORE]

Numeric task run-time analysis - split the result per core

Trace.STATistic.TASK [/MergeCORE]

Numeric task run-time analysis - merge the results of all cores



Trace.Chart.TASK [/SplitCORE]

Time-chart of tasks - split the result per core

Exporting Task Services

The ORTI file may also provide means to analyze the time in task service routines.

TASK.CONFIG(magic_service[<core>]) is the name of the TRACE32 function that is used for this purpose.

```
PRINT TASK.CONFIG(magic_service[0])          ; print the address that holds
                                              ; the task service table for
                                              ; TC 1.6.1 CPU0

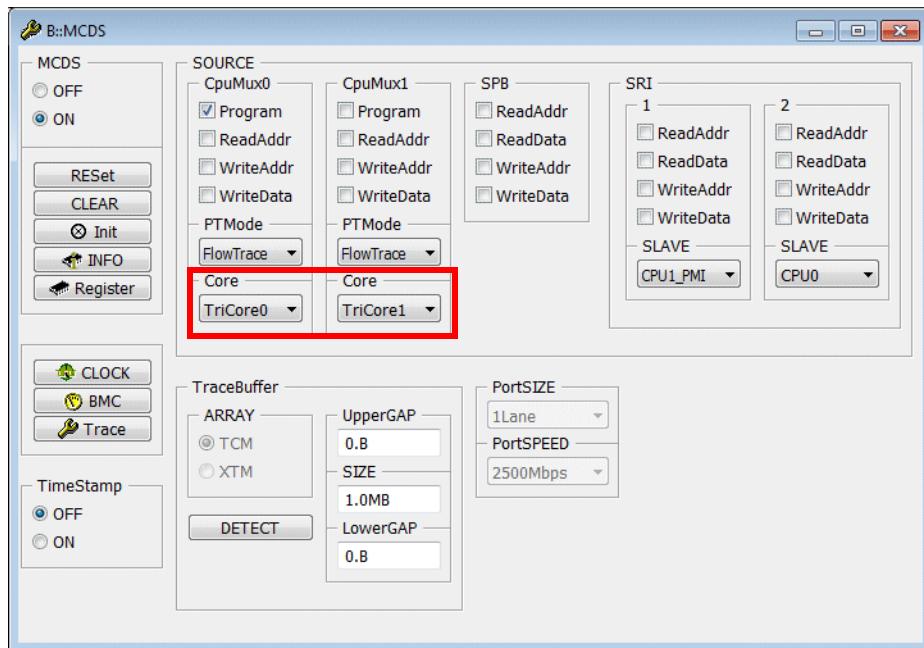
PRINT TASK.CONFIG(magic_service[1])          ; print the address that holds
                                              ; the task service table for
                                              ; TC 1.6.1 CPU1

PRINT TASK.CONFIG(magic_service[2])          ; print the address that holds
                                              ; the task service table for
                                              ; TC 1.6.1 CPU2
```

Example: Advise the Processor Observation Blocks to generate trace messages only on write accesses to the service tables.

- **System under debug:** SMP system with 3 TriCore cores.
- **Cores under debug:** TC 1.6.1 CPU0 and TC 1.6.1 CPU1.
- **Event of interest:** Write accesses to TASK.CONFIG(magic_service[0]) and TASK.CONFIG(magic_service[1]).
- **Requested Messages:** Write Data Trace Messages, Timestamp Messages.

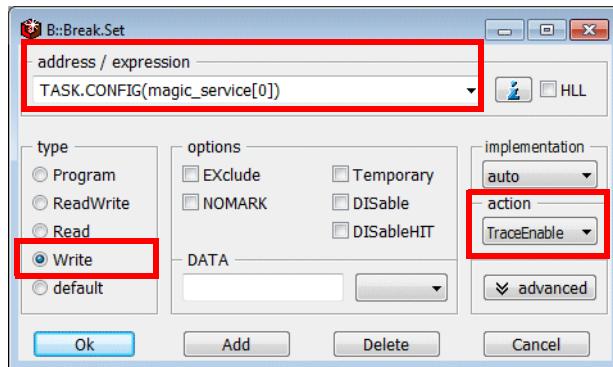
1. Configure the trace multiplexer.



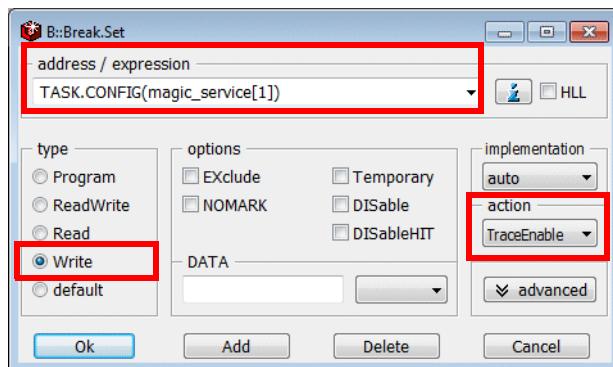
```
MCDS.SOURCE.Set CpuMux0.Core TriCore0 ; enable TC 1.6.1 CPU0 as
; trace source

MCDS.SOURCE.Set CpuMux1.Core TriCore1 ; enable TC 1.6.1 CPU1 as
; trace source
```

2. Specify the events.

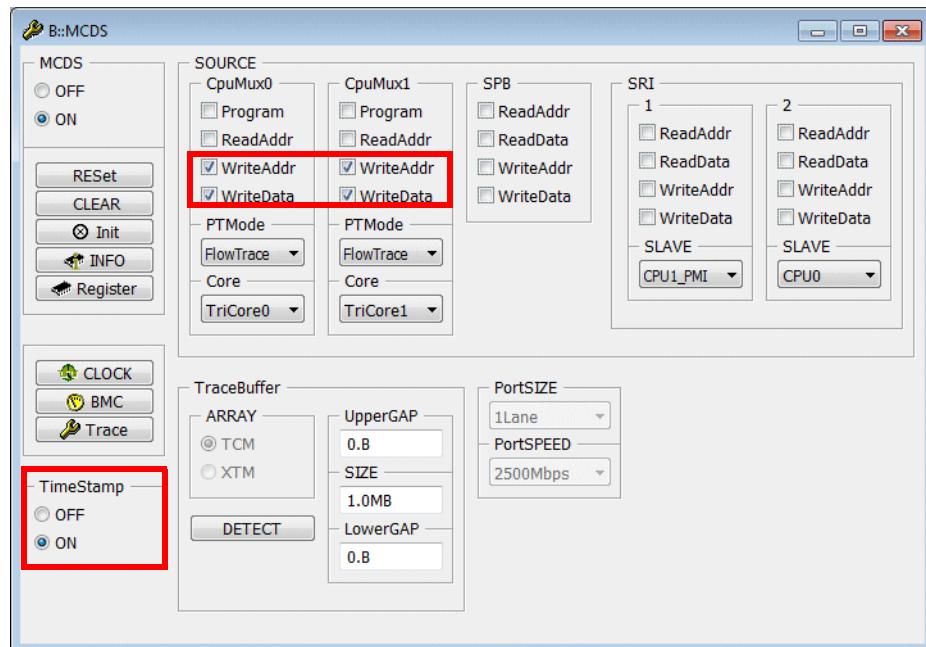


```
Break.Set TASK.CONFIG(magic_service[0]) /Write /TraceEnable
```



```
Break.Set TASK.CONFIG(magic_service[1]) /Write /TraceEnable
```

3. Configure which trace messages are generated.



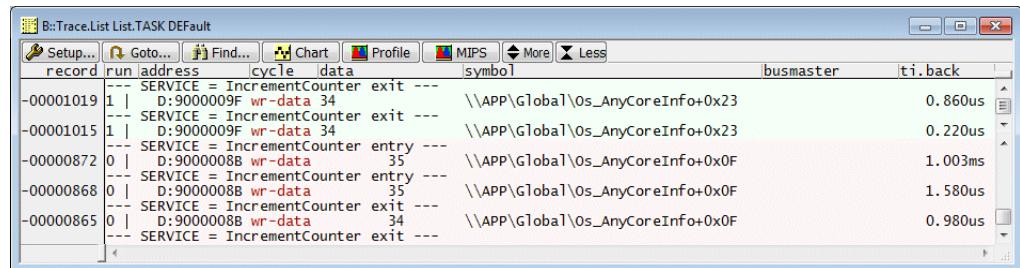
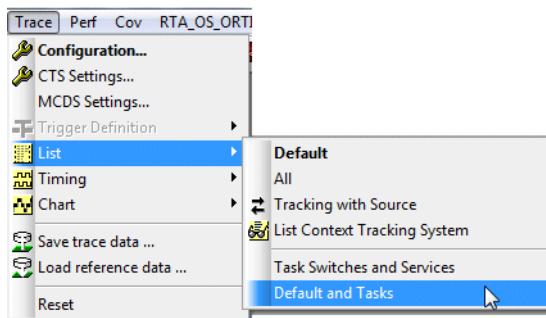
```
MCDS.TimeStamp ON ; enable Timestamp Messages

CLOCK.ON

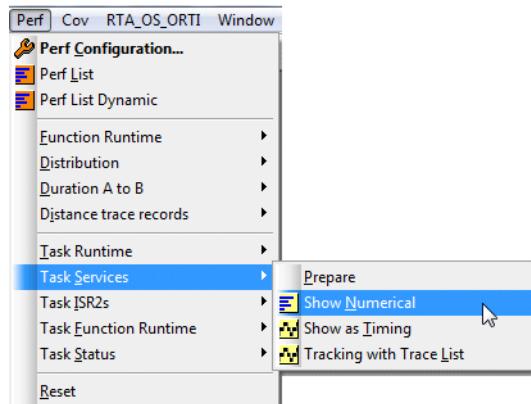
MCDS.SOURCE.Set CpuMux0.Program OFF ; disable Instruction Pointer
MCDS.SOURCE.Set CpuMux1.Program OFF ; disable Instruction Pointer
MCDS.SOURCE.Set CpuMux0.WriteAddr ON ; enable Write Data Trace
MCDS.SOURCE.Set CpuMux0.WriteData ON ; Messages for TC 1.6.1 CPU0
MCDS.SOURCE.Set CpuMux1.WriteAddr ON ; enable Write Data Trace
MCDS.SOURCE.Set CpuMux1.WriteData ON ; Messages for TC 1.6.1 CPU1
```

4. Start the program execution and stop it.

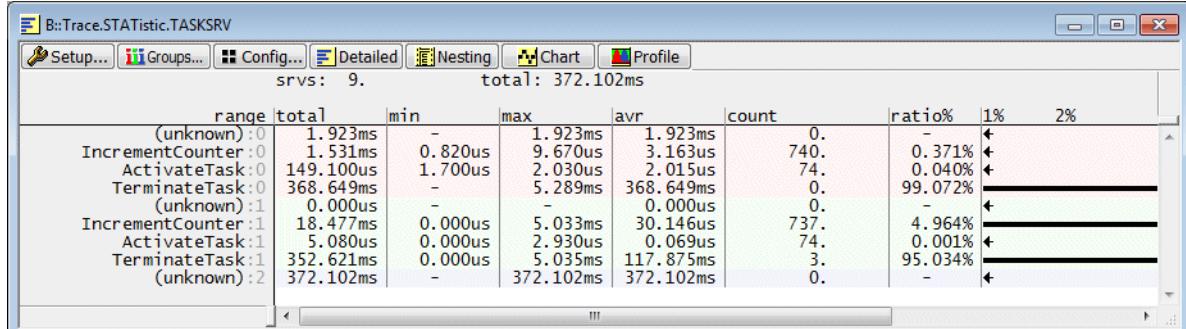
5. Display the result.



The following two commands perform a statistical analysis of the time in task service routines:

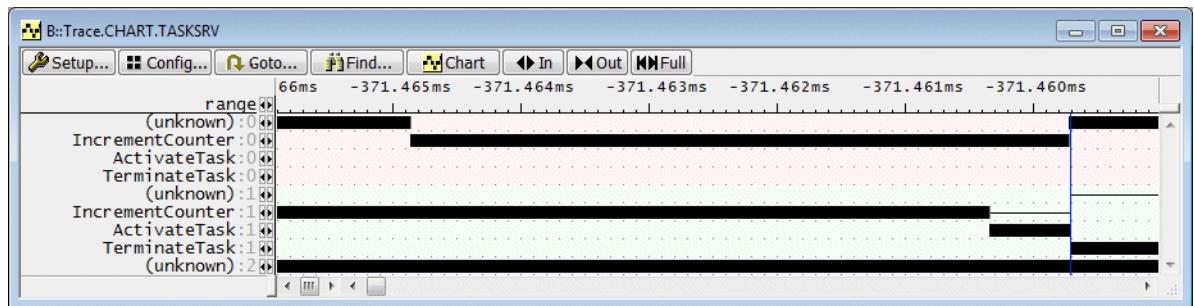
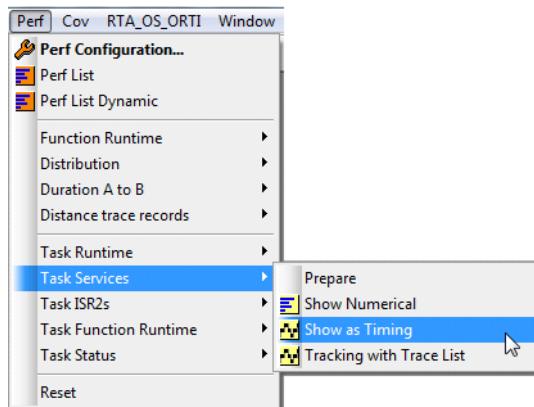


(unknown) represents the time in which the core is not in an OSEK service routine



Trace.STATistic.TASKSRV [/SplitCORE]

Numeric analysis of task services - split the result per core.



Trace.Chart.TASKSRV [/SplitCORE]

Time-chart of task services - split the result per core.

Exporting ISR2 (OSEK Interrupt Service Routines)

The ORTI file may also provide means to analyze the time in interrupt service routines.

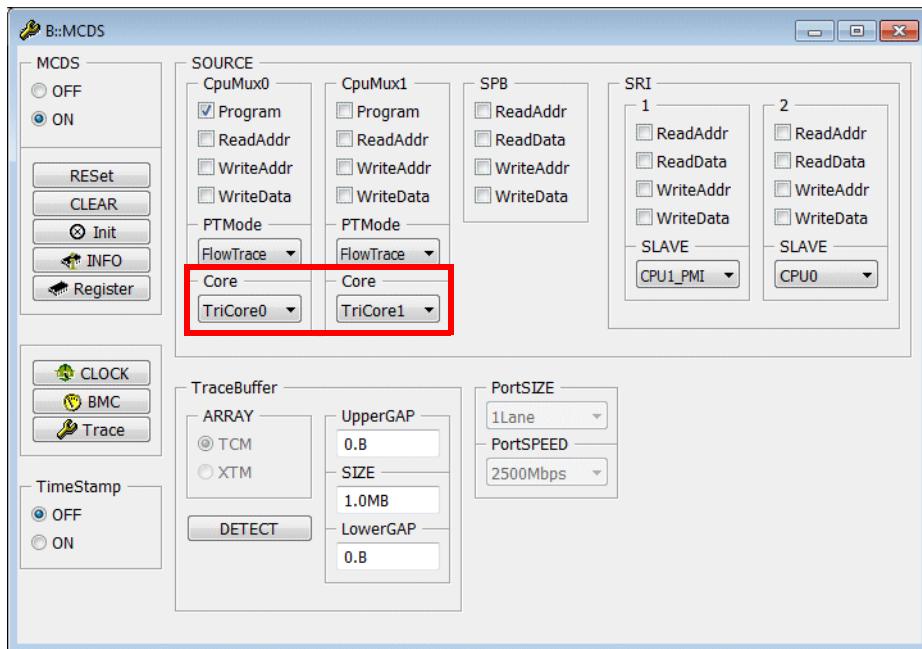
TASK.CONFIG(magic_isr2[<core>]) is the name of the TRACE32 function that is used for this purpose.

```
PRINT TASK.CONFIG(magic_isr2[0]) ; print the address that holds  
; the interrupt service table  
; for TC 1.6.1 CPU0  
  
PRINT TASK.CONFIG(magic_isr2[1]) ; print the address that holds  
; the interrupt service table  
; for TC 1.6.1 CPU1  
  
PRINT TASK.CONFIG(magic_isr2[2]) ; print the address that holds  
; the interrupt service table  
; for TC 1.6.1 CPU2
```

Example: Advise the Processor Observation Blocks to generate trace messages only on write accesses to the interrupt service tables.

- **System under debug:** SMP system with 3 TriCore cores.
- **Cores under debug:** TC 1.6.1 CPU0 and TC 1.6.1 CPU1.
- **Event of interest:** Write accesses to TASK.CONFIG(magic_isr2[0]) and TASK.CONFIG(magic_isr2[1]).
- **Requested Messages:** Write Data Trace Messages, Timestamp Messages.

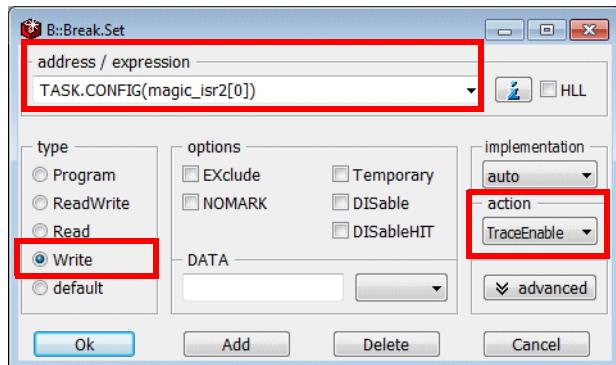
1. Configure the trace multiplexer.



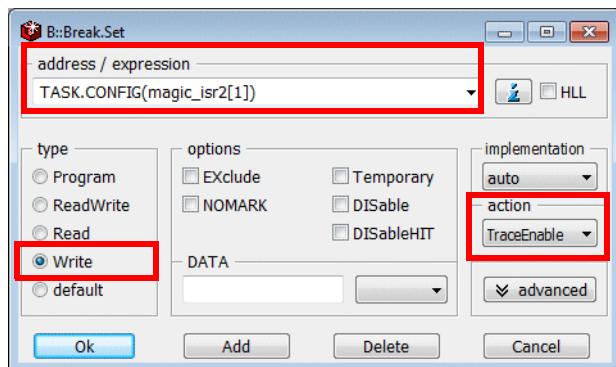
```
MCDS.SOURCE.Set CpuMux0 Core TriCore0 ; enable TC 1.6.1 CPU0 as
; trace source
```

```
MCDS.SOURCE.Set CpuMux1 Core TriCore1 ; enable TC 1.6.1 CPU1 as
; trace source
```

2. Specify the events.

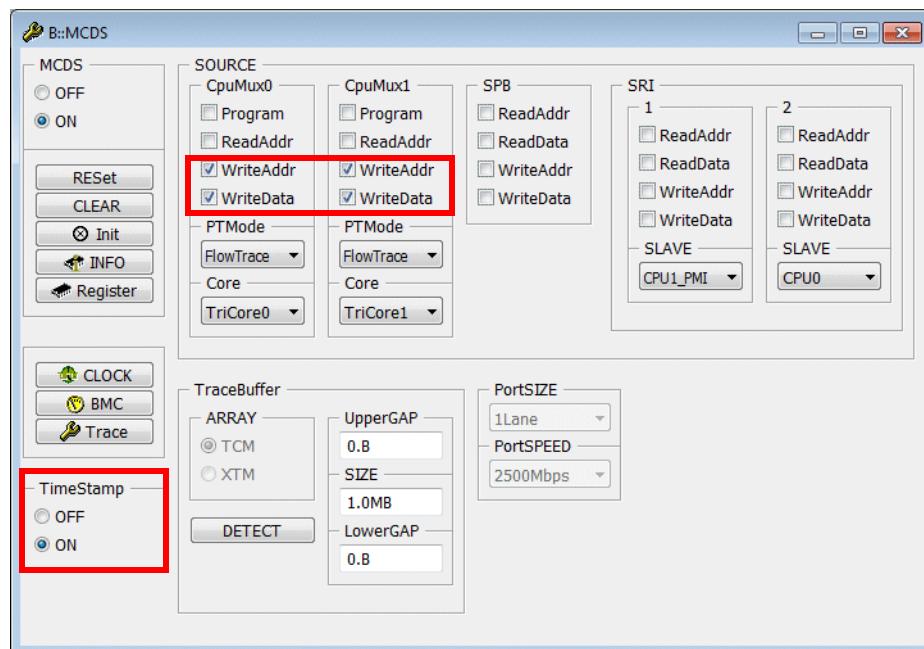


```
Break.Set TASK.CONFIG(magic_service[0]) /Write /TraceEnable
```



```
Break.Set TASK.CONFIG(magic_service[1]) /Write /TraceEnable
```

3. Configure which trace messages are generated.



```
MCDS.TimeStamp ON ; enable Timestamp Messages

CLOCK.ON

MCDS.SOURCE.Set CpuMux0.Program OFF ; disable Instruction
; Pointer Call Messages for
; TC 1.6.1 CPU0

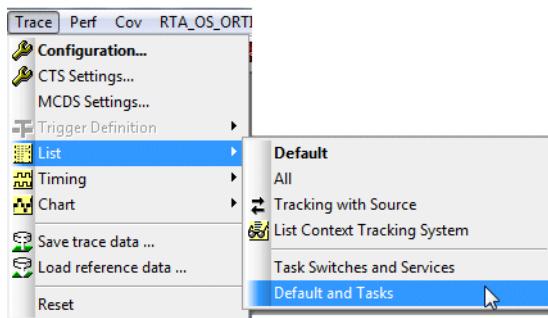
MCDS.SOURCE.Set CpuMux1.Program OFF ; disable Instruction
; Pointer Call Messages for
; TC 1.6.1 CPU1

MCDS.SOURCE.Set CpuMux0.WriteAddr ON ; enable Write Data Trace
MCDS.SOURCE.Set CpuMux0.WriteData ON ; Messages for TC 1.6.1 CPU0

MCDS.SOURCE.Set CpuMux1.WriteAddr ON ; enable Write Data Trace
MCDS.SOURCE.Set CpuMux1.WriteData ON ; Messages for TC 1.6.1 CPU1
```

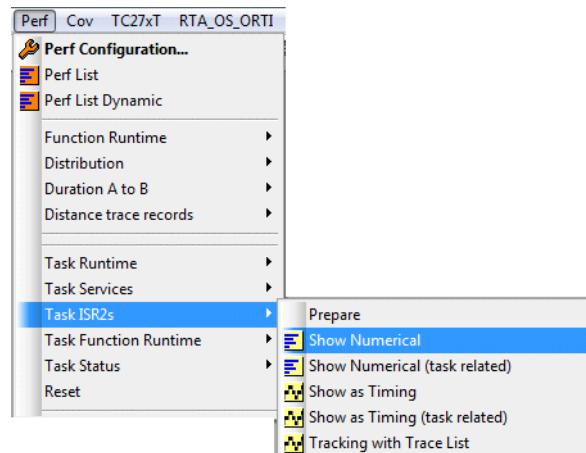
4. Start the program execution and stop it.

5. Display the result.



B:Trace.List List.TASK DEFault								
record	run	address	cycle	data	symbol	busmaster	ti.back	
-00001031	1	D:900000D8 wr-data	80000D38	\\APP\\Global\\Os_ControlledCoreInfo+0x20			1.000ms	
-00001022	1	D:900000D8 wr-data	00000000	\\APP\\Global\\Os_ControlledCoreInfo+0x20			4.540us	
-00000673	0	D:900000B8 wr-data	80000D2C	\\APP\\Global\\Os_ControlledCoreInfo			1.001ms	
-00000664	0	D:900000B8 wr-data	00000000	\\APP\\Global\\Os_ControlledCoreInfo			4.890us	
-00000413	1	D:900000D8 wr-data	80000D38	\\APP\\Global\\Os_ControlledCoreInfo+0x20			1.000ms	
-00000404	1	D:900000D8 wr-data	00000000	\\APP\\Global\\Os_ControlledCoreInfo+0x20			4.880us	

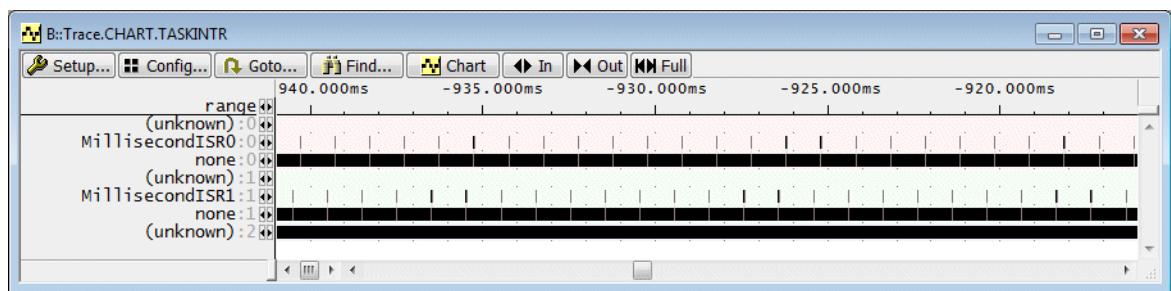
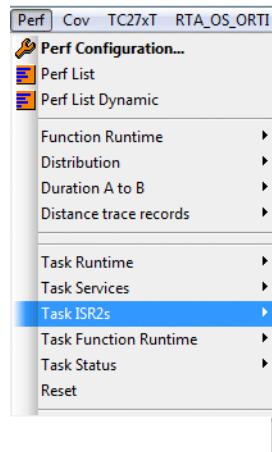
The following two commands perform a statistical analysis of the time in interrupt service routines:



B:Trace.STATistic.TASKINTR								
	range	total	min	max	avr	count	ratio%	1%
(unknown):0	0.150us	0.150us	0.150us	0.150us	0.150us	1.	<0.001%	↔
MillisecondISR0:0	8.970ms	4.520us	11.750us	6.131us	1463.		0.609%	↔
none:0	1.464s	1.000ms	1.077ms	1.001ms	1462.		99.391%	██████████
(unknown):1	0.000us	0.000us	-	0.000us	1.		0.000%	↔
MillisecondISR1:1	8.633ms	4.180us	11.390us	5.897us	1464.		0.586%	↔
none:1	1.464s	1.000ms	1.001ms	1.001ms	1463.		99.414%	██████████
(unknown):2	1.473s	-	1.473s	1.473s	1.473s	0.	100.000%	██████████

Trace.STATistic.TASKINTR [/SplitCORE]

Numeric analysis of ISR2s - split the result per core.

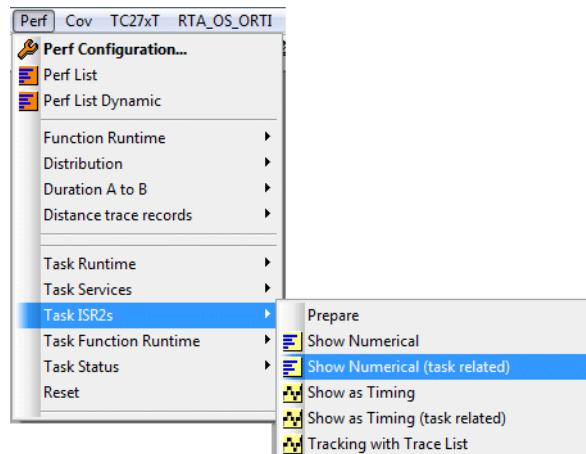


Trace.Chart.TASKINTR [/SplitCORE]

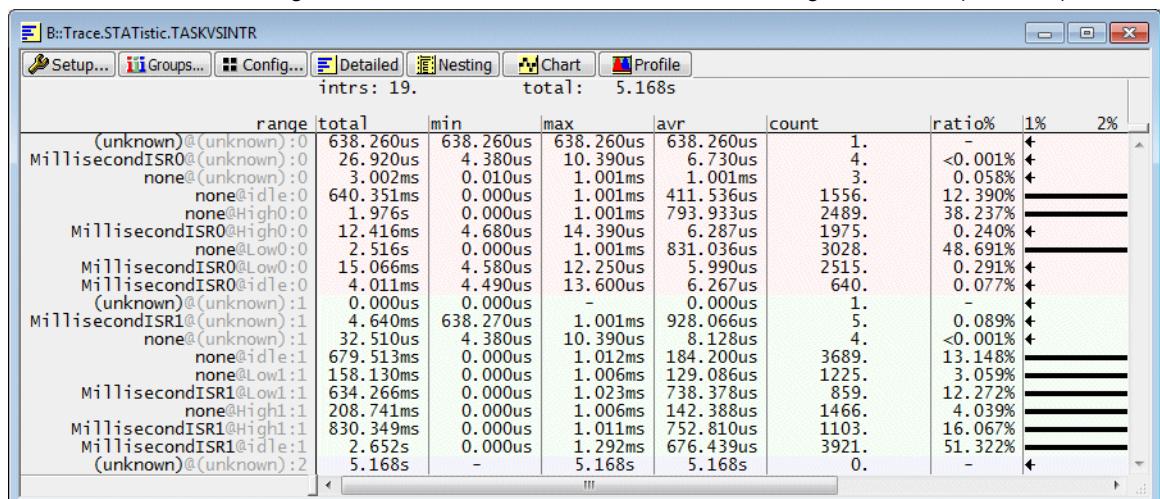
Time-chart of ISR2s - split the result per core.

Exporting Task Switches and ISR2

The following commands allow to perform a statistical analysis of the OSEK interrupt service routines related to the active tasks, if you export task switch and ISR2 information.

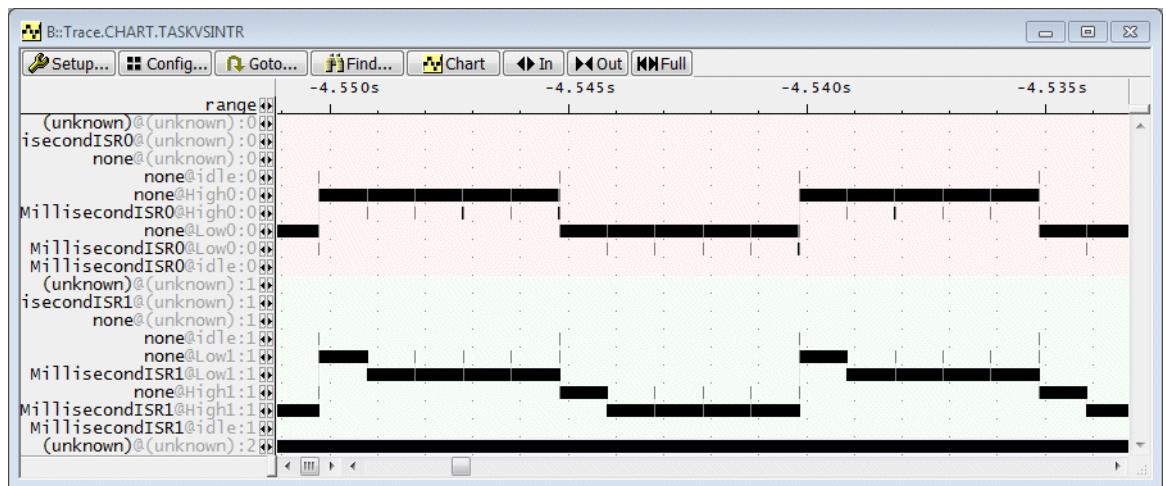
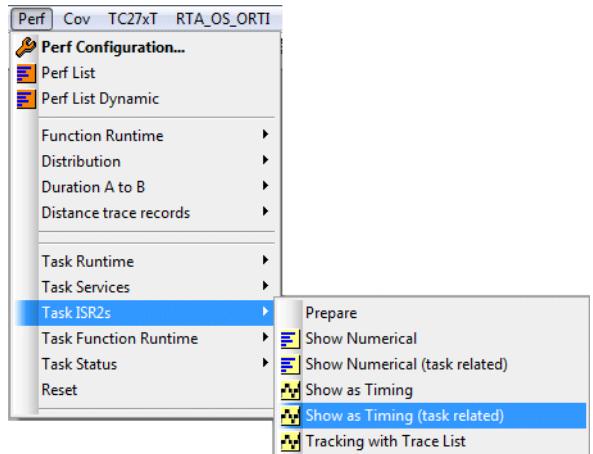


ISR2 information that was generated before the first task information is assigned to the @ (unknown) task



Trace.STATistic.TASKVSINTR [/SplitCORE]

Task-related statistic on interrupt service routines
- split the result per core



Trace.Chart.TASKVSINTR [/SplitCORE]

Time-chart on task related interrupt service routines - split the result per core

Exporting Task Switches and all Instructions

General setup:

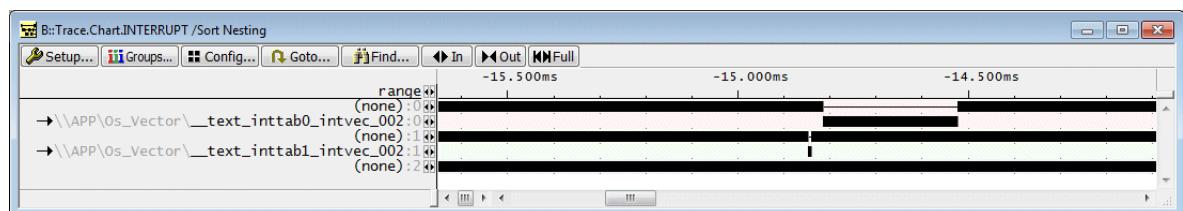
```
Break.Set TASK.CONFIG(magic[0]) /Write /TraceData
Break.Set TASK.CONFIG(magic[1]) /Write /TraceData
; Break.Set TASK.CONFIG(magic[2]) /Write /TraceData

; advise TRACE32 to regard the time between interrupt entry
; and exit as function
Trace.STATistic.InterruptIsFunction ON
```

Statistic Analysis of Interrupts

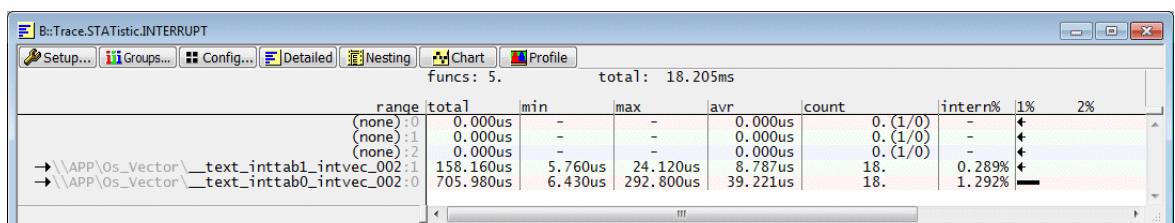
Trace.Chart.INTERRUPT [/SplitCORE]

Interrupt time chart - split the result per core



Trace.STATistic.INTERRUPT [/SplitCORE]

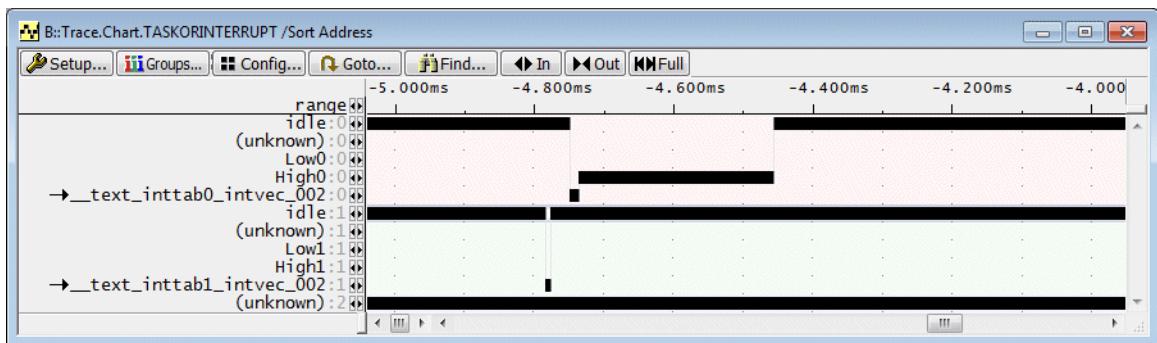
Interrupt statistic - split the result per core



Statistic Analysis of Interrupts and Tasks

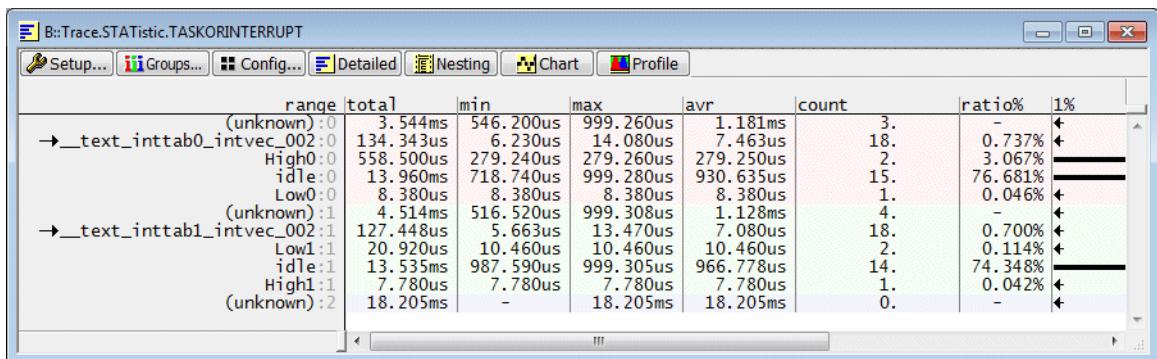
Trace.Chart.TASKORINTERRUPT [/SplitCORE]

Time chart of interrupts and tasks - split the result per core



Trace.STATistic.TASKORINTERRUPT [/SplitCORE]

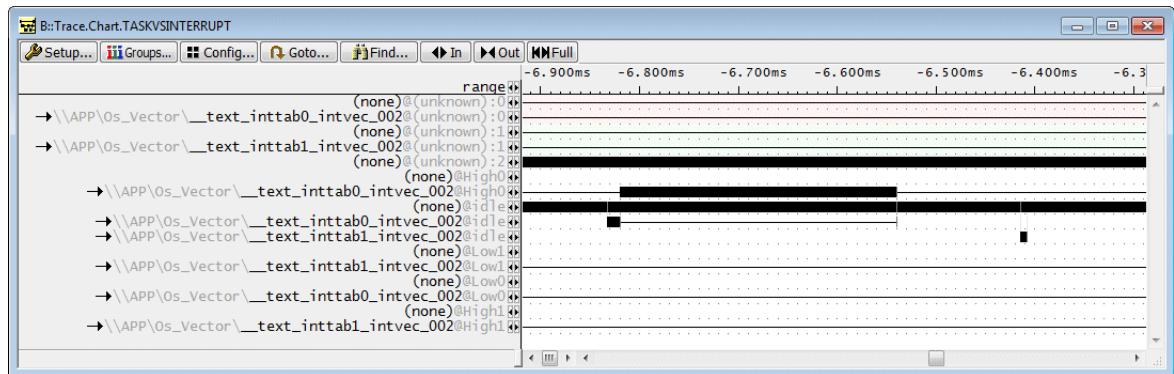
Statistic of interrupts and tasks - split the result per core



Statistic Analysis of Interrupts in Tasks

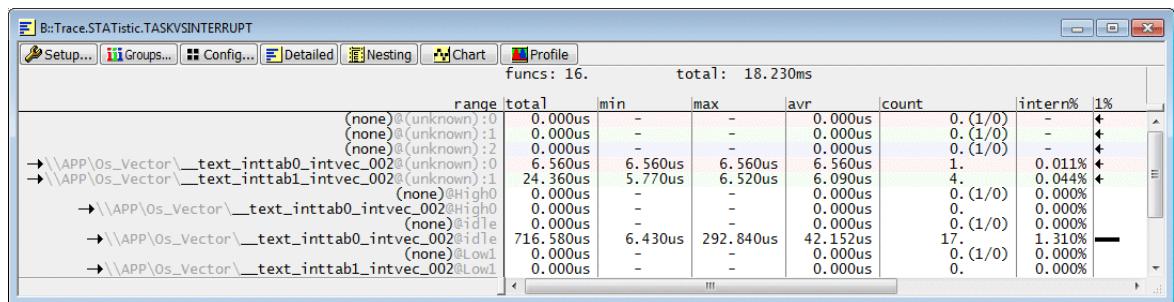
Trace.Chart.TASKVSINTERRUPT

Time chart interrupts, task-related



Trace.STATistic.TASKVSINTERRUPT

Statistic of interrupts, task-related



The TRACE32 Instruction Set Simulator can be used for a belated OS-aware trace evaluation. To set up the TRACE32 Instruction Set Simulator for belated OS-aware trace evaluation proceed as follows:

1. Save the trace information for the belated evaluation to a file.

```
Trace.SAVE belated_orti.ad
```

2. Set up the TRACE32 Instruction Set Simulator for a belated OS-aware trace evaluation (here OSEK on a TC277TE):

```
SYStem.CPU TC277TE ; select the target CPU

CORE.ASSIGN 1. 2. 3. ; configure the SMP
; system

SYStem.Up ; establish the
; communication between
; TRACE32 and the TRACE32
; Instruction Set
; Simulator

Trace.LOAD belated_orti.ad ; load the trace file

Data.Load.Elf my_app.out ; load the symbol and
; debug information

TASK.ORTI my_orti.ort ; load the ORTI file

Trace.List List.TASK DEFault ; display the trace
; listing
```

Software under Analysis (no OS or OS)

For the use of the function run-time analysis it is helpful to differentiate between two types of application software:

1. Software without operating system (abbreviation: **no OS**)
2. Software with an operating system (abbreviation: **OS**)

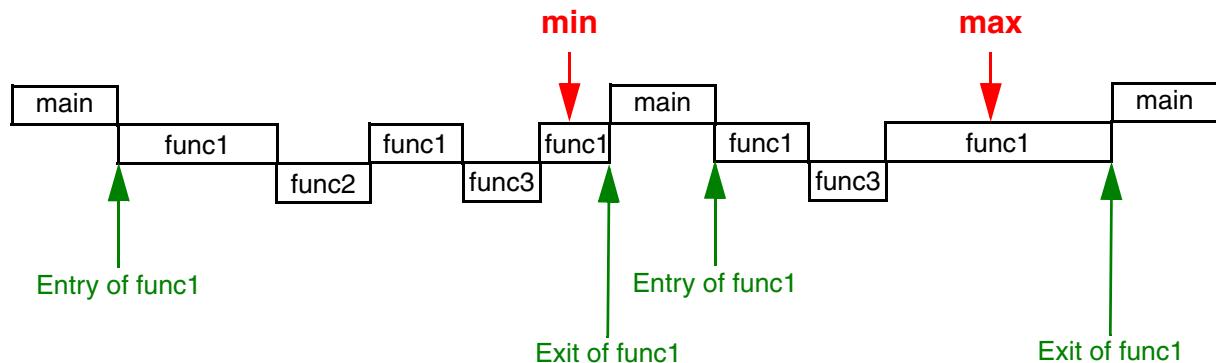
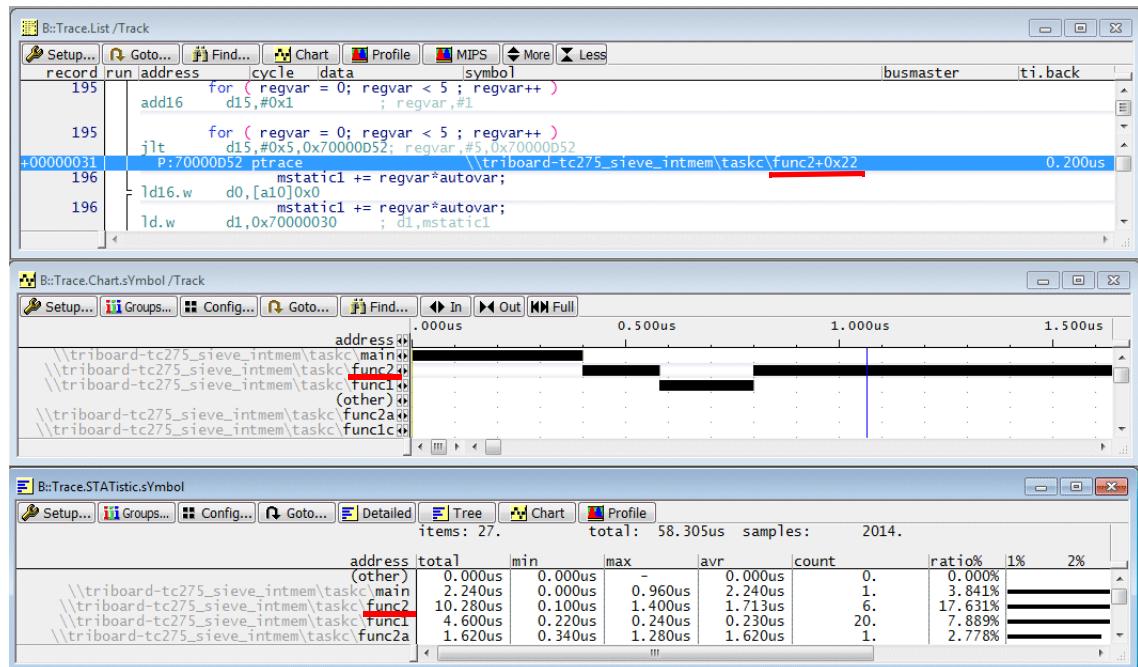
Flat vs. Nesting Analysis

TRACE32 provides two methods to analyze function run-times:

- Flat analysis
- Nesting analysis

Basic Knowledge about Flat Analysis

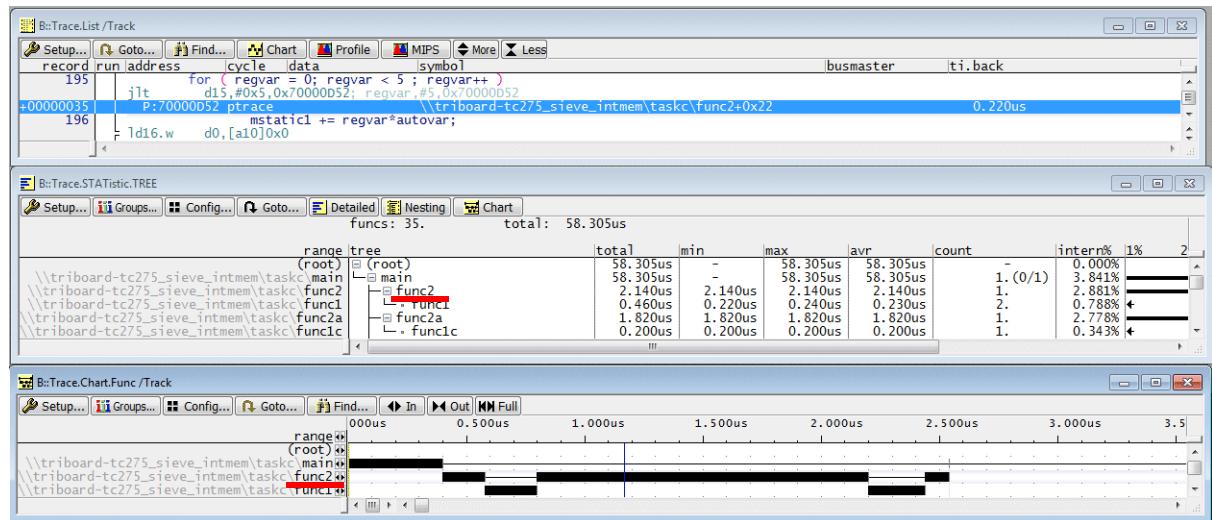
The flat function run-time analysis bases on the symbolic instruction addresses of the trace entries. The time spent by an instruction is assigned to the corresponding function/symbol region.

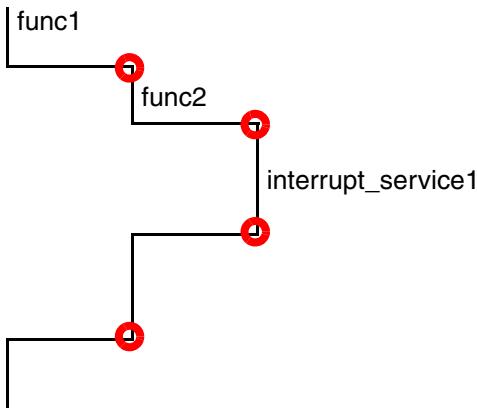


min	shortest time continuously in the address range of the function/symbol region
max	longest time continuously in the address range of the function/symbol region

Basic Knowledge about Nesting Analysis

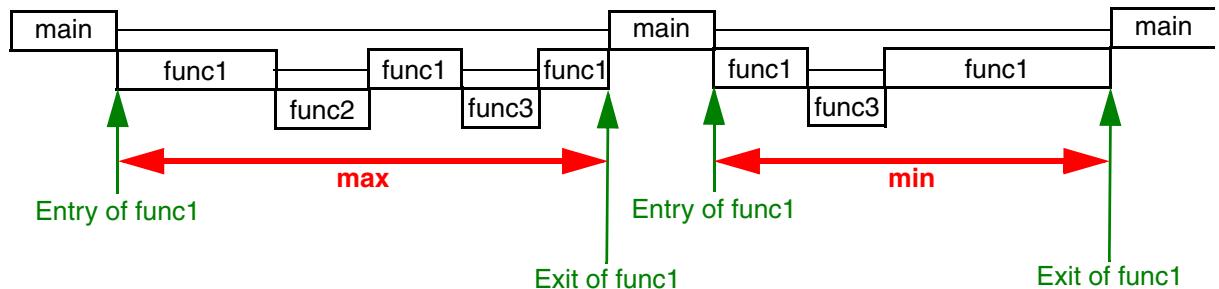
The function nesting analysis analyses only high-level language functions.





In order to display a nesting function run-time analysis TRACE32 analyzes the structure of the program execution by processing the trace information. The focus is put on the transition between functions (see picture above). The following events are of interest:

1. **Function entries**
2. **Function exits**
3. **Entries to interrupt service routines**
4. **Exits of interrupt service routines**
5. **Entries to TRAP handlers**
6. **Exits of TRAP handlers**



min	shortest time within the function including all subfunctions and traps
max	longest time within the function including all subfunctions and traps

The nesting analysis provides more details on the structure and the timing of the program run, but it is much more sensitive than the flat analysis.

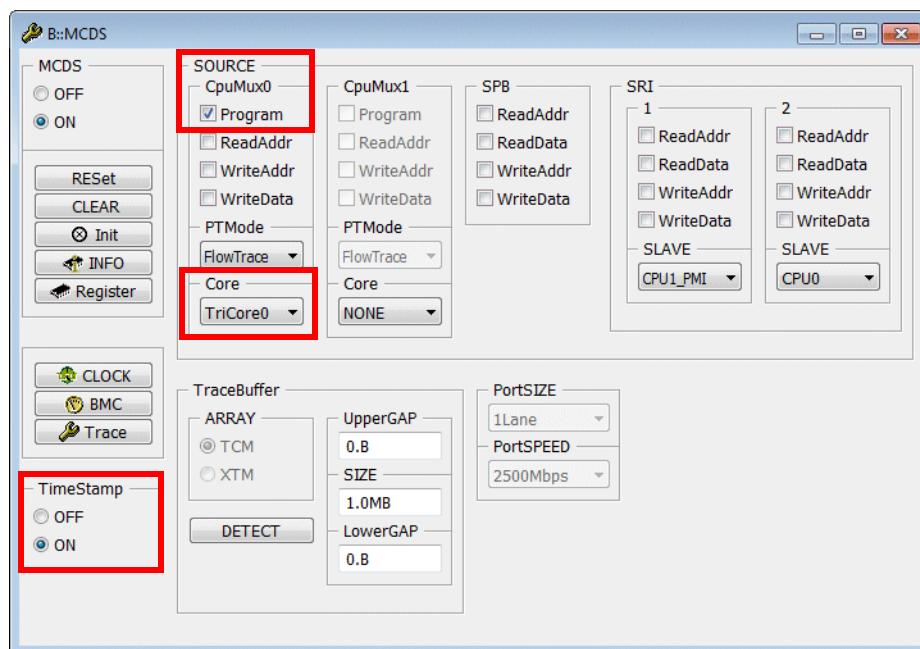
It is recommended to reduce the trace information generated by MCDS to the required minimum.

- To make best use of the available trace memory.

Optimum MCDS Configuration (No OS)

Flat function run-time analysis does **not** require any **data information** if no OS is used. That's why it is recommended to disable the generation of Write and Read Data Trace Messages.

Since the serial off-chip trace provides imprecise timestamps Timestamp Messages have to be enabled for any run-time measurement.



```
MCDS.SOURCE CpuMux0 Core TriCore0 ; enable TC 1.6.1 CPU0 as
; trace source

MCDS.TimeStamp ON ; enable Timestamp Messages

CLOCK.ON

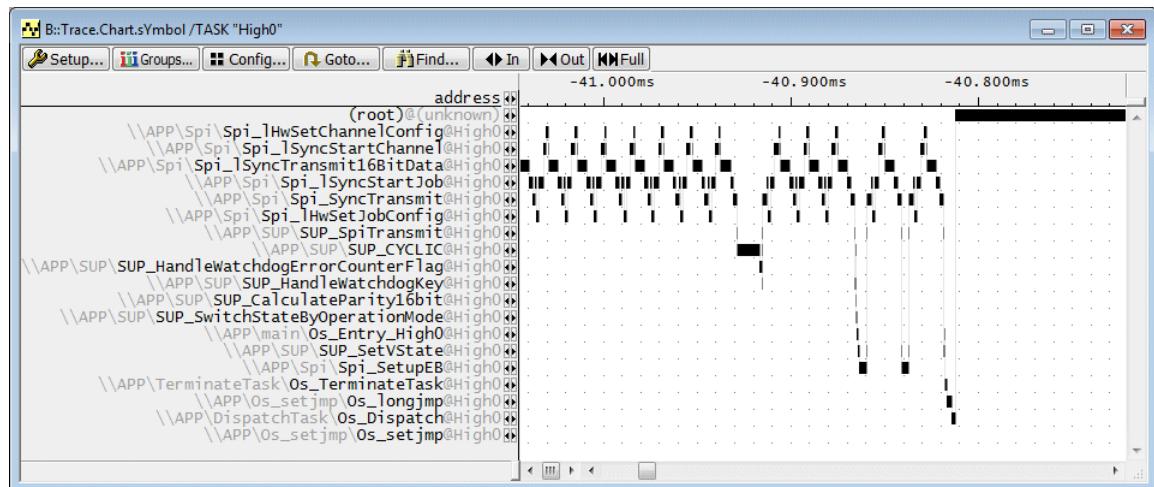
MCDS.SOURCE CpuMux0 Program ON ; enable Instruction Pointer
; Call Messages for
; TC 1.6.1 CPU0
```

Optimum MCDS Configuration (OS)

Your function time chart **can** include task information if you advise MCDS to export the instruction flow and task switches. For details refer to the chapter “[OS-Aware Tracing - Single-Core and AMP](#)”, page 157 of this training.

Function time chart with task information:

```
Trace.Chart.sYmbol /TASK "High0"
```



Command to advise MCDS to export the instruction flow and task switches.

```
Break.Set TASK.CONFIG(magic) /Write /TraceData
```

Since the serial off-chip trace provides imprecise timestamps Timestamp Messages have to be enabled for any run-time measurement.

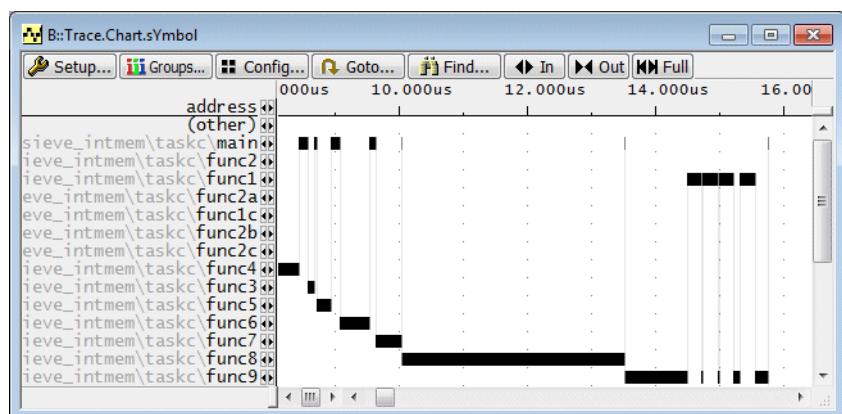
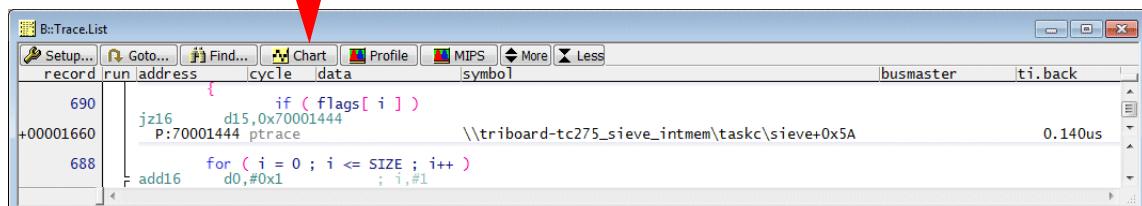
```
CLOCK.ON
```

```
MCDS.TimeStamp ON
```

Function Timing Diagram (no TASK Information)

TRACE32 PowerView provides a timing diagram which shows when the program counter was in which function/symbol range.

Pushing the **Chart** button in the **Trace.List** window opens a **Trace.Chart.sYmbol** window



Trace.Chart.sYmbol

Display function time chart (no OS)

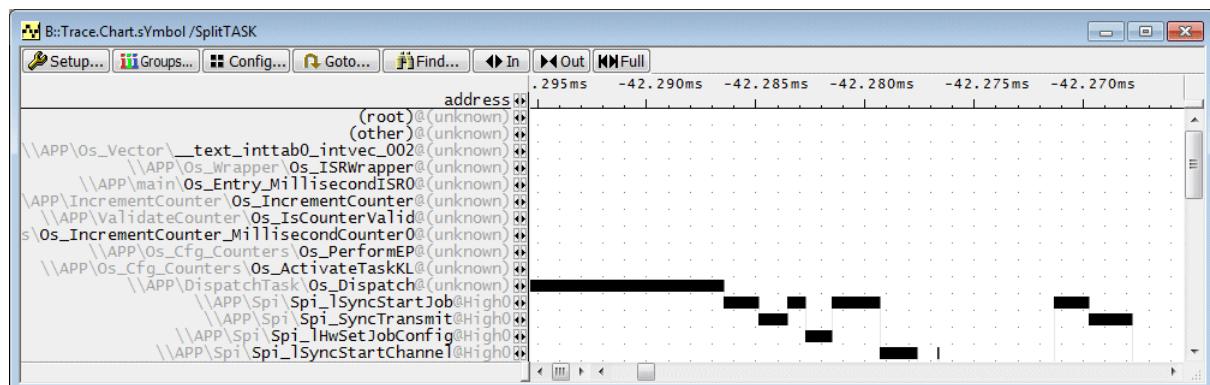
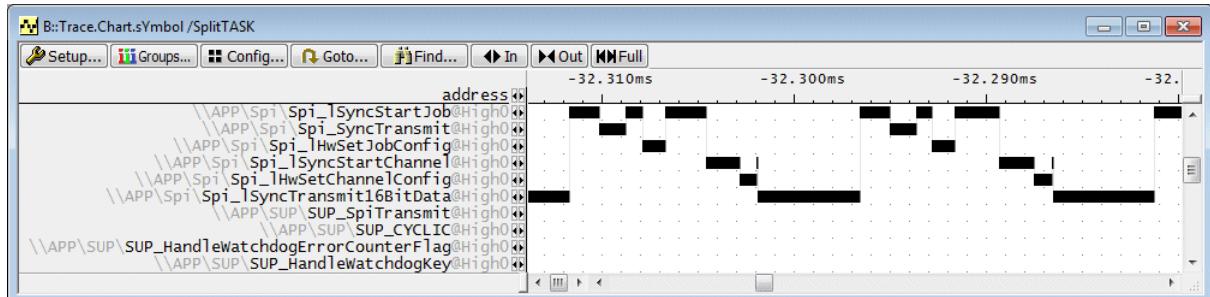
Trace.Chart.sYmbol [/MergeTASK]

Display function time chart (OS but task information is not of interest)

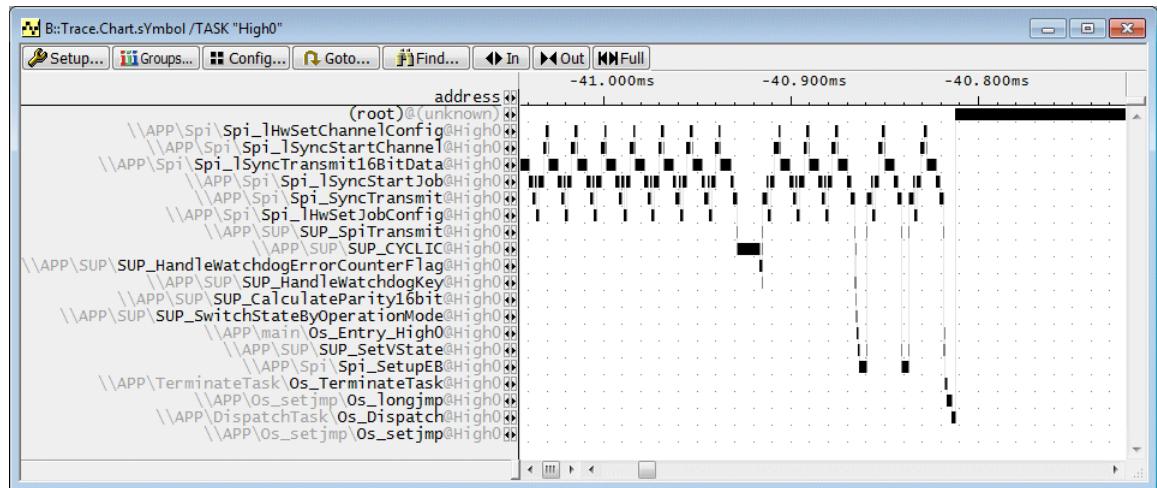
Function Timing Diagram (TASK information)

Trace.Chart.sYmbol /SplitTASK

Display function time chart including task information (OS only)

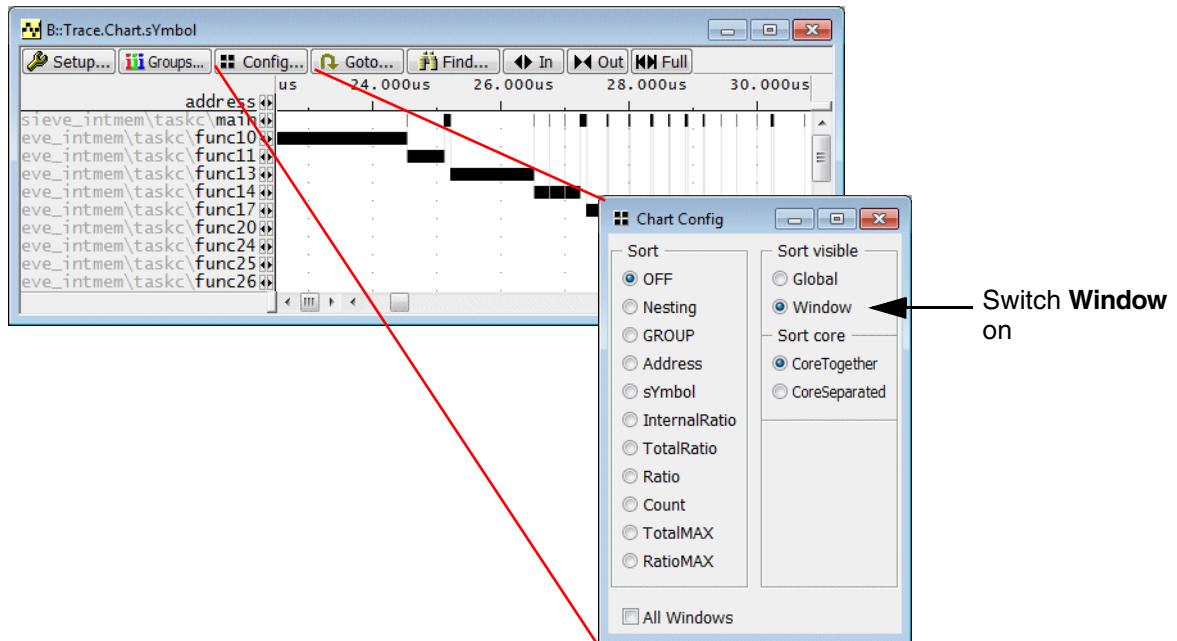


@ <task_name>	Task name information
@(unknown)	<ul style="list-style-type: none">Function was running before the OS was startedFunction was recorded before first task switch information was recorded
(root)@(unknown)	No trace information available.



Trace.Chart.sYmbol /TASK <name>

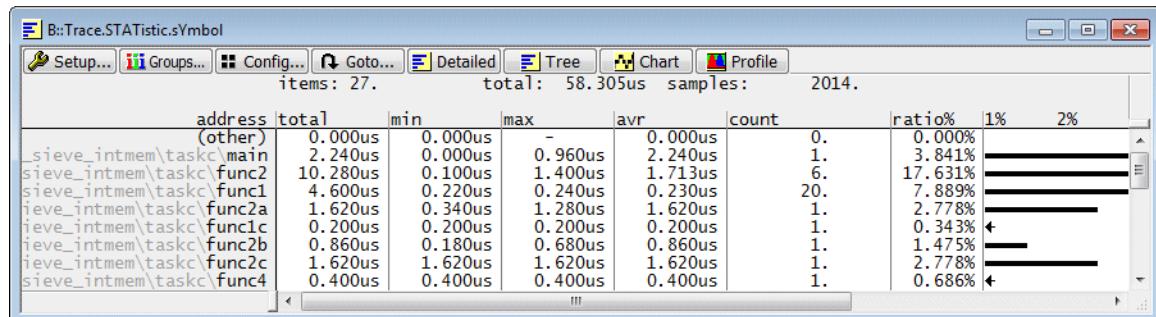
Display function time chart for specified task
(OS only)



If **Window** in the **Sort visible** field is switched ON in the **Chart Config** dialog, the functions that are active at the selected point of time are visualized in the scope of the **Trace.Chart.sYmbol** window. This is helpful especially if you scroll horizontally.

Numeric Analysis

Analog to the timing diagram there is also a numerical analysis.

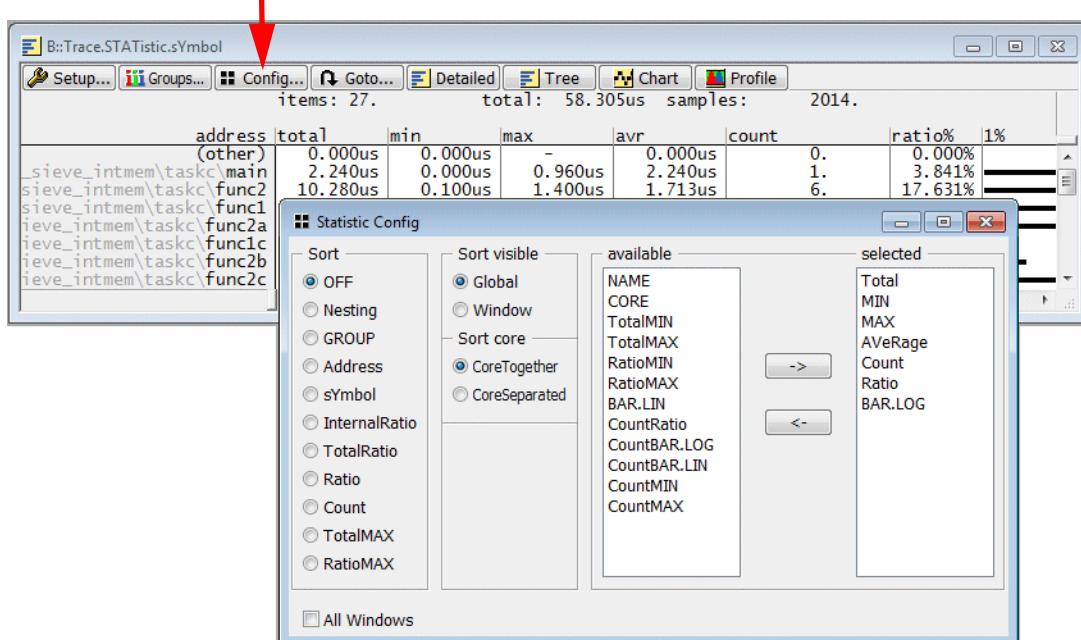


survey	
item	number of recorded functions/symbol regions
total	time period recorded by the trace
samples	total number of recorded changes of functions/symbol regions (instruction flow continuously in the address range of a function/symbol region)

function details	
address	function/symbol region name (other) program sections that can not be assigned to a function/symbol region
total	time period in the function/symbol region during the recorded time period
min	shortest time continuously in the address range of the function/symbol region
max	longest time continuously in the address range of the function/symbol region
avr	average time continuously in the address range of the function/symbol region (calculated by total/count)

count	number of new entries (start address executed) into the address range of the function/symbol region
ratio	ratio of time in the function/symbol region with regards to the total time period recorded

Pushing the **Config** button provides the possibility to specify a different column layout and a different sorting criterion for the address column. By default the functions/symbol regions are sorted by their recording order.



Trace.STATistic.sYmbol

Flat function run-time analysis
- numerical display

Trace.STATistic.sYmbol [/MergeTASK]

Flat function run-time analysis (OS but task information is not of interest)
- numerical display

Trace.STATistic.sYmbol /SplitTASK

Flat function run-time analysis including task information (OS only)
- numerical display

Trace.STATistic.sYmbol /TASK <name>

Flat function run-time analysis for specified task (OS only)
- numerical display

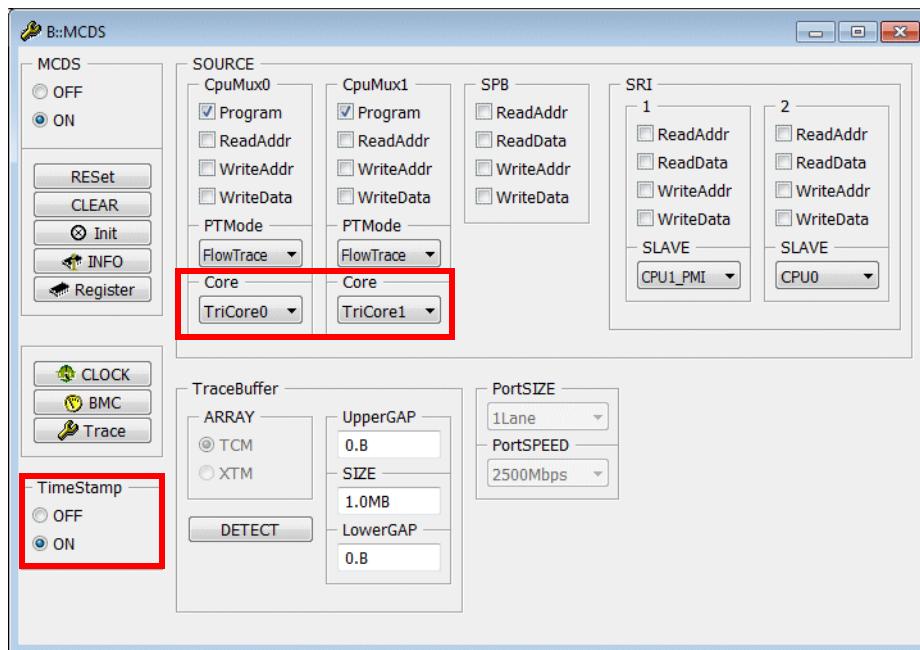
It is recommended to reduce the trace information generated by MCDS to the required minimum.

- To make best use of the available trace memory.

Optimum MCDS Configuration (OS)

Your function time chart **can** include task information if you advise MCDS to export the instruction flow and task switches. For details refer to the chapter “**OS-Aware Tracing - SMP Systems**”, page 178 of this training.

Since the serial off-chip trace provides imprecise timestamps Timestamp Messages have to be enabled for any run-time measurement.



```
MCDS.SOURCE.Set CpuMux0.Core TriCore0 ; enable TC 1.6.1 CPU0 as
                                         ; trace source

MCDS.SOURCE.Set CpuMux1.Core TriCore1 ; enable TC 1.6.1 CPU1 as
                                         ; trace source

MCDS.TimeStamp ON ; enable Timestamp Messages

CLOCK.ON
```

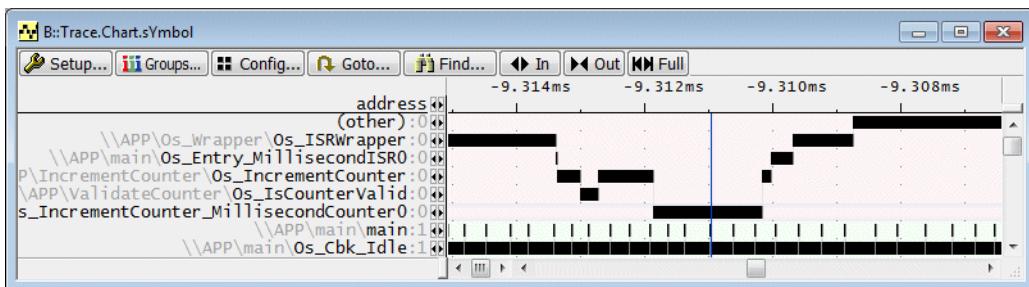
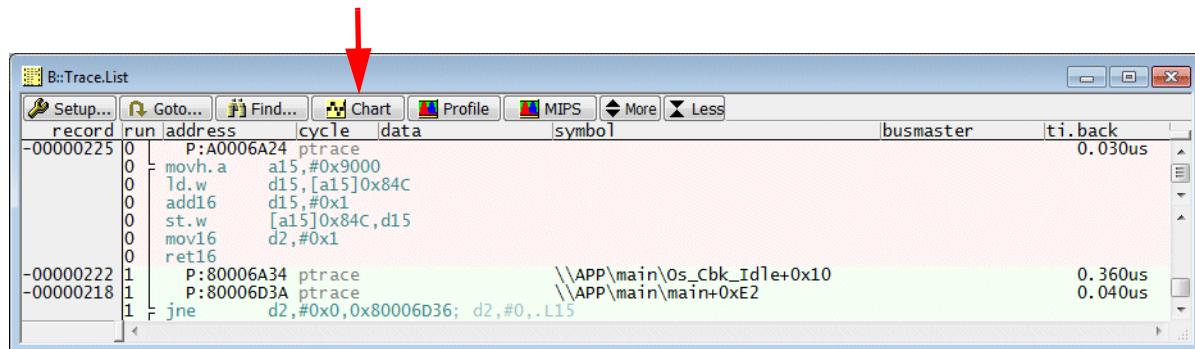
Commands to advise MCDS to export the instruction flow and task switches:

```
Break.Set TASK.CONFIG(magic[0]) /Write /TraceData
Break.Set TASK.CONFIG(magic[1]) /Write /TraceData
; Break.Set TASK.CONFIG(magic[2]) /Write /TraceData
```

Function Timing Diagram (no TASK Information)

TRACE32 PowerView provides a timing diagram which shows when the program counter was in which function/symbol range.

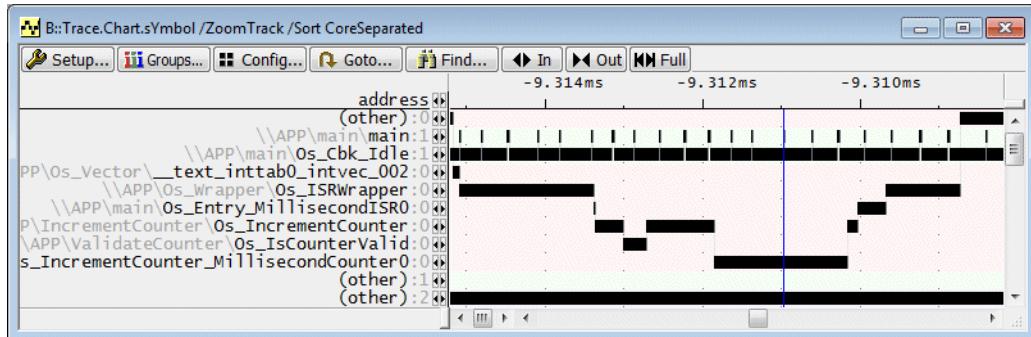
Pushing the **Chart** button in the **Trace.List** window
opens a **Trace.Chart.sYmbol** window



Flat function run-time analysis

- graphical display
- split the result per core
- sort results per core and the per recording order
- no task information

Trace.Chart.sYmbol [/SplitCore /MergeTASK /Sort CoreTogether]

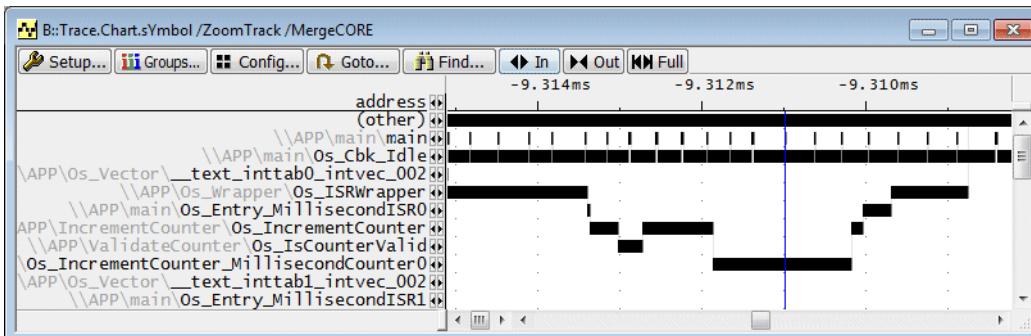


Since no trace information is recorded for TC 1.6.1 CPU2 (other):2 is active for the complete recording time.

Flat function run-time analysis

- graphical display
- split the result per core
- sort results by recording order
- no task information

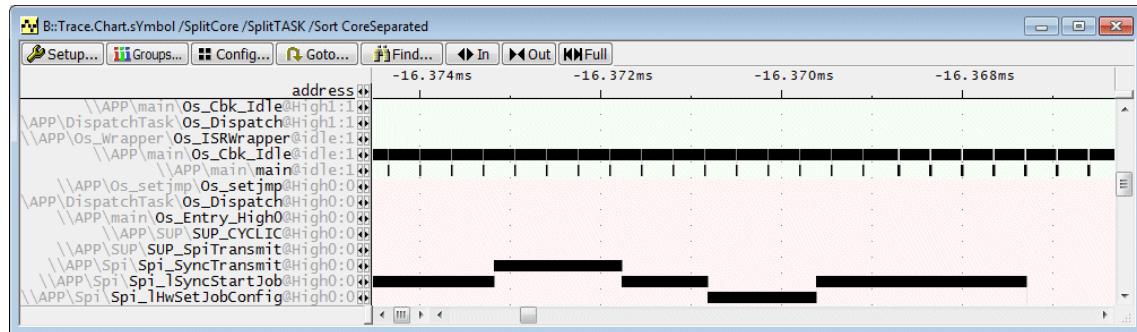
Trace.Chart.sYmbol [/SplitCore /MergeTASK] /Sort CoreSeparated



Trace.Chart.sYmbol [/MergeTASK] /MergeCore

Flat function run-time analysis
- graphical display
- merge the results of all cores
- no task information

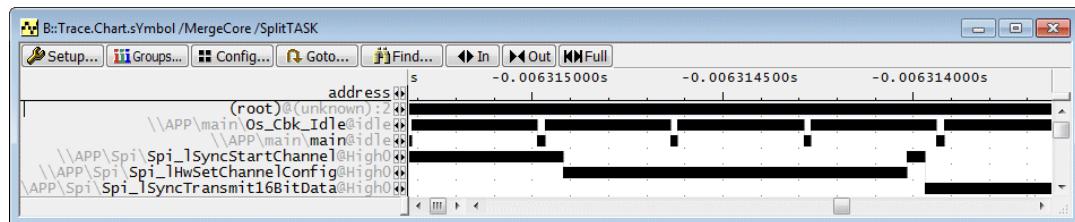
Function Timing Diagram (TASK Information)



Flat function run-time analysis

- graphical display
- split the result per core
- task information
- sort results by recording order

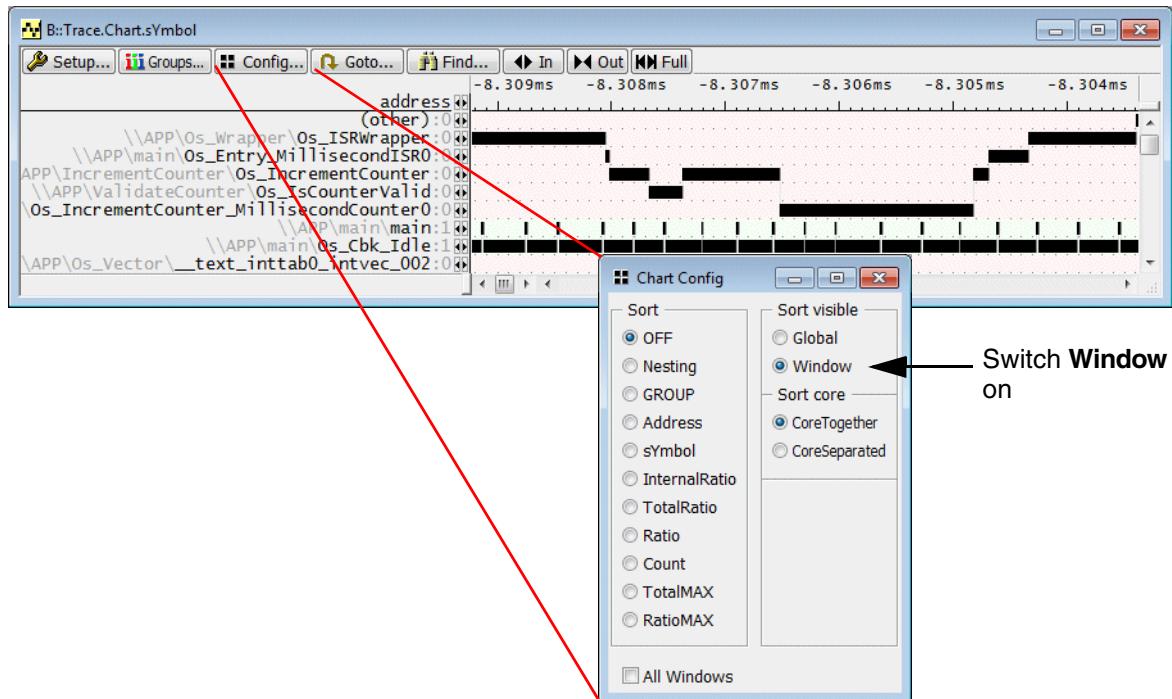
Trace.Chart.sYmbol [/SplitCore] /SplitTASK /Sort CoreSeparated



Flat function run-time analysis

- graphical display
- merge the results of all cores
- task information

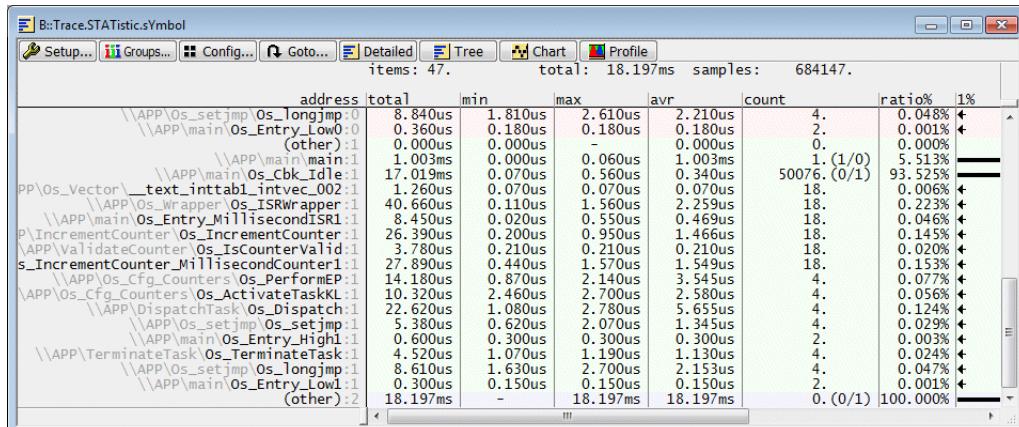
Trace.Chart.sYmbol /MergeCore /SplitTASK



If **Window** in the **Sort visible** field is switched ON in the **Chart Config** window, the functions that are active at the selected point of time are visualized in the scope of the **Trace.Chart.sYmbol** window. This is helpful especially if you scroll horizontally.

Numeric Analysis

Analog to the timing diagram there is also a numerical analysis.



survey

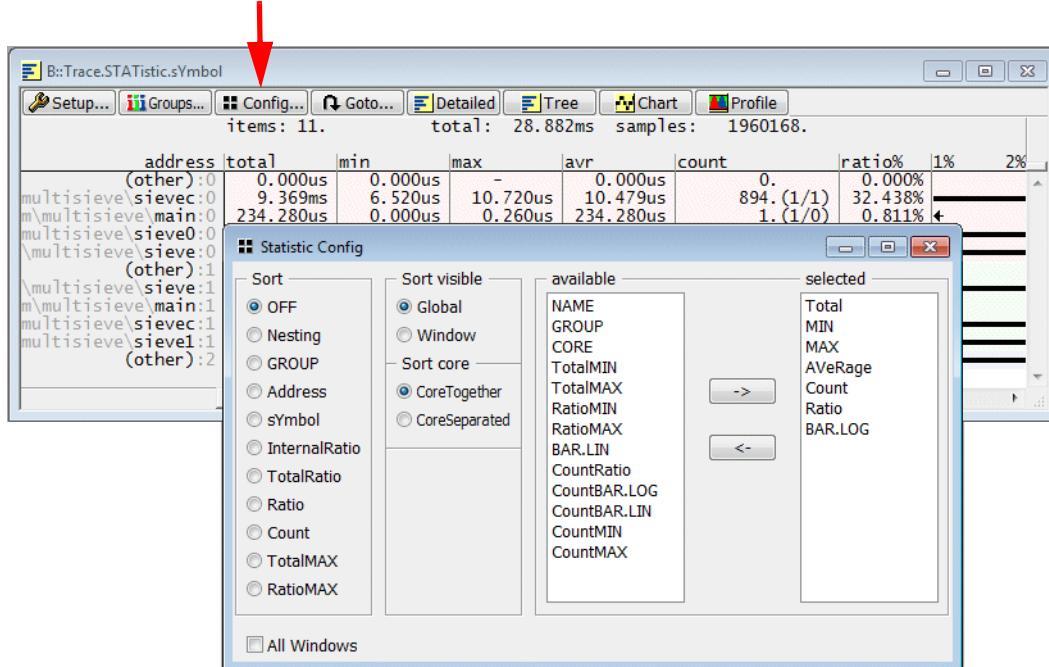
item	number of recorded functions/symbol regions
total	time period recorded by the trace
samples	total number of recorded changes of functions/symbol regions (instruction flow continuously in the address range of a function/symbol region)

function details

address	function/symbol region name (here per core) (other) program sections that can not be assigned to a function/symbol region
total	time period in the function/symbol region during the recorded time period
min	shortest time continuously in the address range of the function/symbol region
max	longest time continuously in the address range of the function/symbol region
avr	average time continuously in the address range of the function/symbol region (calculated by total/count)

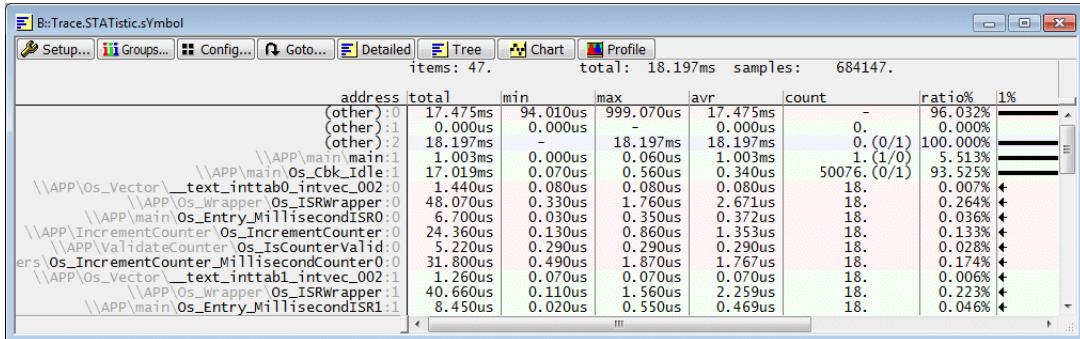
count	number of new entries (start address executed) into the address range of the function/symbol region
ratio	ratio of time in the function/symbol region with regards to the total time period recorded

Pushing the **Config** button provides the possibility to specify a different column layout and a different sorting criterion for the address column.
By default the functions/symbol regions are sorted by their recording order.



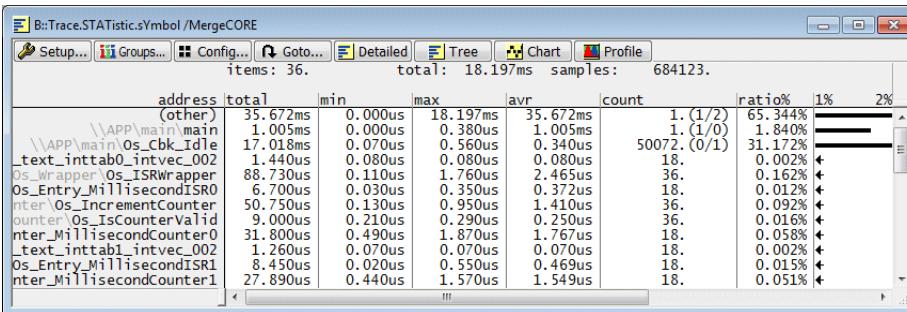
Trace.STATistic.sYmbol [/SplitCORE /Sort CoreTogether]

- Flat function run-time analysis
- numeric display
- split the result per core
- sort results per core and then per recording order
- no task information



Trace.STATistic.sYmbol [/SplitCORE] /Sort CoreSeparated

- Flat function run-time analysis
- numeric display
- split the result per core
- sort results per recording order
- no task information



Trace.STATistic.sYmbol [/MergeCORE]

- Flat function run-time analysis
- numeric display
- merge the results of all cores
- no task information

Nesting Function Run-Time Analysis - Single

The following applies to single core and AMP applications.

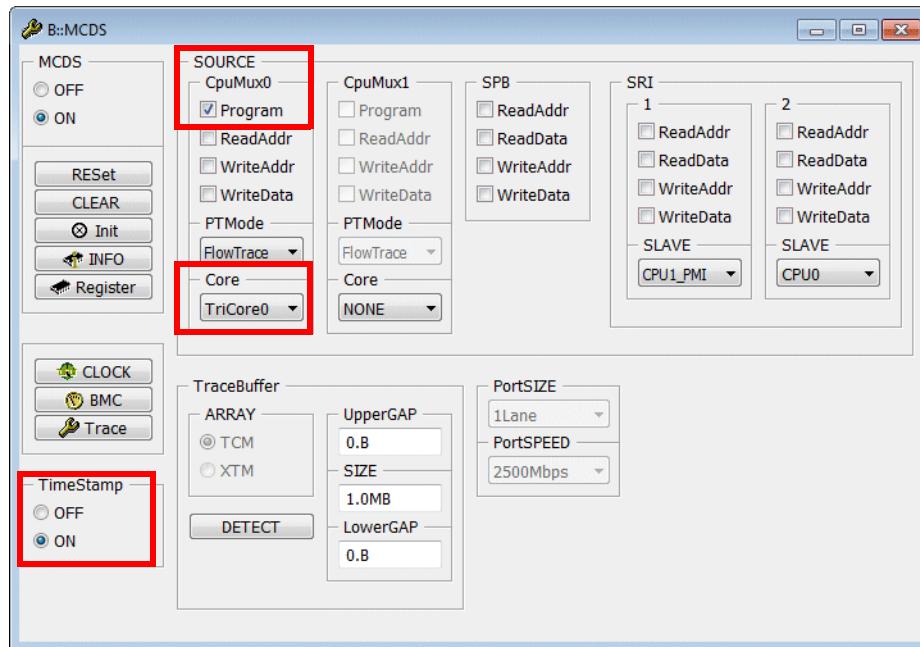
Restrictions

1. The nesting analysis analyses only high-level language functions.
2. The nesting function run-time analysis expects common ways to enter/exit functions.
3. The nesting analysis is sensitive with regards to FIFOFULLs.

Optimum MCDS Configuration (No OS)

Nesting function run-time analysis does **not** require any **data information** if no OS is used. That's why it is recommended to disable the generation of Write Data and Read Data Trace Messages.

Since the serial off-chip trace provides imprecise timestamps Timestamp Messages have to be enabled for any run-time measurement.



```
MCDS.SOURCE.Set CpuMux0.Core TriCore0 ; enable TC 1.6.1 CPU0 as
                                         ; trace source

MCDS.TimeStamp ON ; enable Timestamp Messages

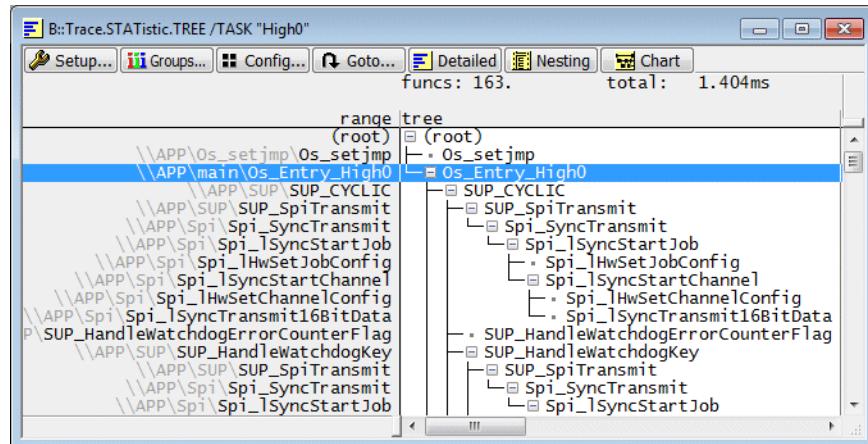
CLOCK.ON

MCDS.SOURCE.Set CpuMux0.Program ON ; enable Instruction Pointer
                                         ; Call Messages for
                                         ; TC 1.6.1 CPU0
```

```
; default setting since 2015-01
Trace.STATistic.INTERRUPTISFUNCTION
```

TRACE32 PowerView builds up a separate call tree for each task/process.

```
Trace.STATistic.TREE /TASK "High0"
```

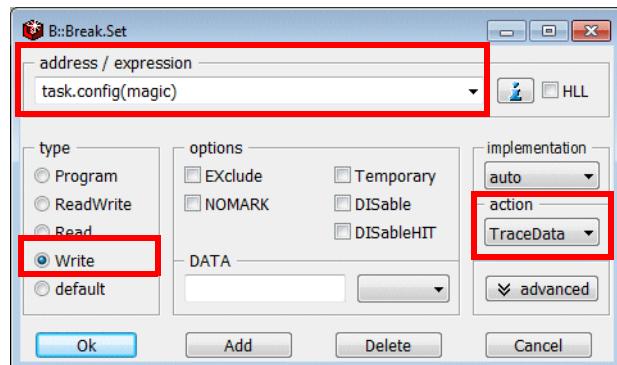


In order to hook a function entry/exit into the correct call tree, TRACE32 PowerView needs to know which task was running when the entry/exit occurred.

The standard way to get information on the current task is to advise the MCDS to generate trace messages for the instruction flow and the task switches. For details refer to the "["OS-Aware Tracing - Single-Core and AMP"](#)" in Training AURIX Tracing, page 157 (training_aurix_trace.pdf).

Advise the Processor Observation Block to generate trace messages for the complete instruction flow and for the task switches.

Filter settings



Since the serial off-chip trace provides imprecise timestamps Timestamp Messages have to be enabled for any run-time measurement.

```
MCDS.SOURCE.Set CpuMux0.Core TriCore0 ; enable TC 1.6.1 CPU0 as
                                         ; trace source

MCDS.TimeStamp ON ; enable Timestamp Messages

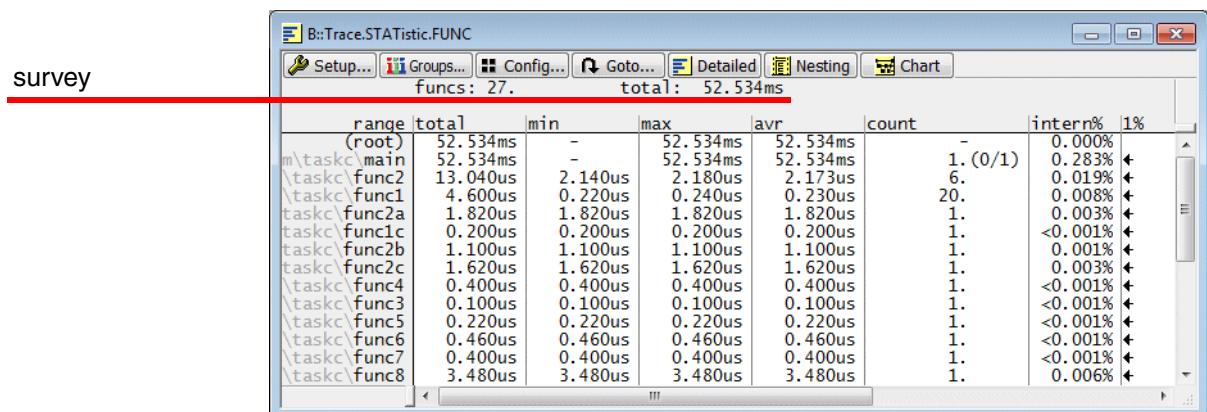
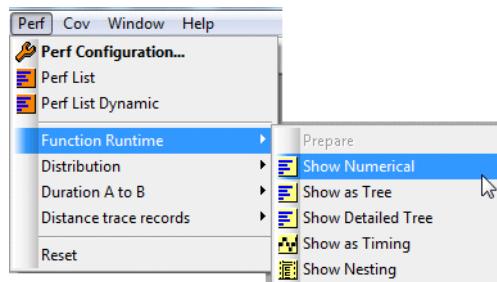
CLOCK.ON
```

```
; default setting since 2015-01
Trace.STATistic.InterruptIsFunction
```

Statistics Items

Trace.STATistic.Func

Nesting function run-time analysis
- numeric display

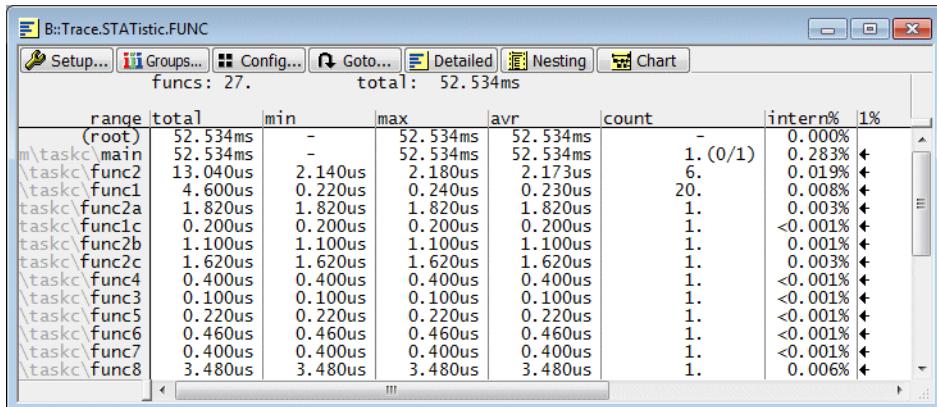


funcs: 92. total: 4.203ms intr: 20.665ms

funcs: 45. total: 44.918ms intr: 651.830us 31 problems 18 workarounds

survey	
funcs: <number>	number of functions in the trace
total: <time>	total measurement time
intr: <time>	total time in interrupt service routines

survey (issue indication)	
stopped: <time>	The analyzed trace recording contains program stops. <time> indicates the total time the program execution was stopped.
<number> problems	The nested analysis contains problems. Please contact support@lauterbach.com .
<number> workarounds	The nested analysis contains issues, but TRACE32 found solutions for them. It is recommended to perform a sanity check on the proposed solutions.
stack overflow at <record>	The nested analysis exceeds the nesting level 200. It is highly likely that the function exit for an often called function is missing. The command Trace.STATistic.TREE can help you to identify the function. If you need further help please contact support@lauterbach.com .
stack underflow at <record>	The nested analysis exceeds the nesting level 200. It is highly likely that the function entry for an often executed function is missing. The command Trace.STATistic.TREE can help you to identify the function. If you need further help please contact support@lauterbach.com .



columns

range (NAME)	function name, sorted by their recording order as default
--------------	---

- **HLL function**

`\triboard-tc275_sieve_intmem\taskc\func6`

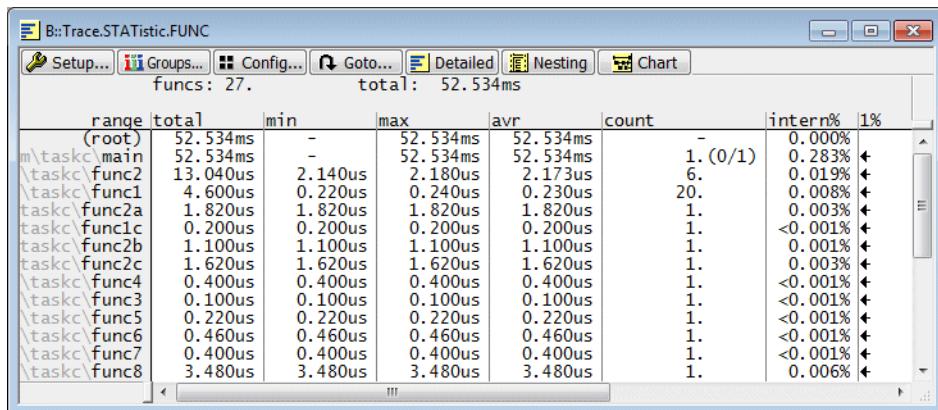
- **(root)**

`(root)`

The function nesting is regarded as tree, root is the root of the function nesting.

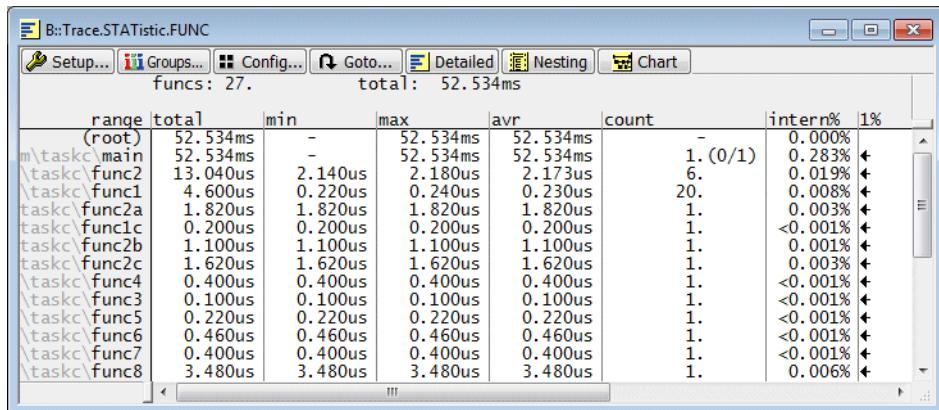
- **Interrupt** (branch to an address of an interrupt vector)

`→ \APP__text_inttab0_intvec_002`



columns (cont.)

total	total time within the function
min	shortest time between function entry and exit, time spent in interrupt service routines is excluded No min time is displayed if a function exit was never executed.
max	longest time between function entry and exit, time spent in interrupt service routines is excluded
avr	average time between function entry and exit, time spent in interrupt service routines is excluded



columns (cont.)

count	number of times within the function
-------	-------------------------------------

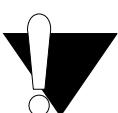
If function entries or exits are missing, this is displayed in the following format:

<times within the function>. (<number of missing function entries>|<number of missing function exits>).

3671. (0/1)

Interpretation examples:

1. 2. (2/0): 2 times within the function, 2 function entries missing.
2. 4. (0/3): 4 times within the function, 3 function exits missing.
3. 11. (1/1): 11 times within the function, 1 function entry and 1 function exit is missing.

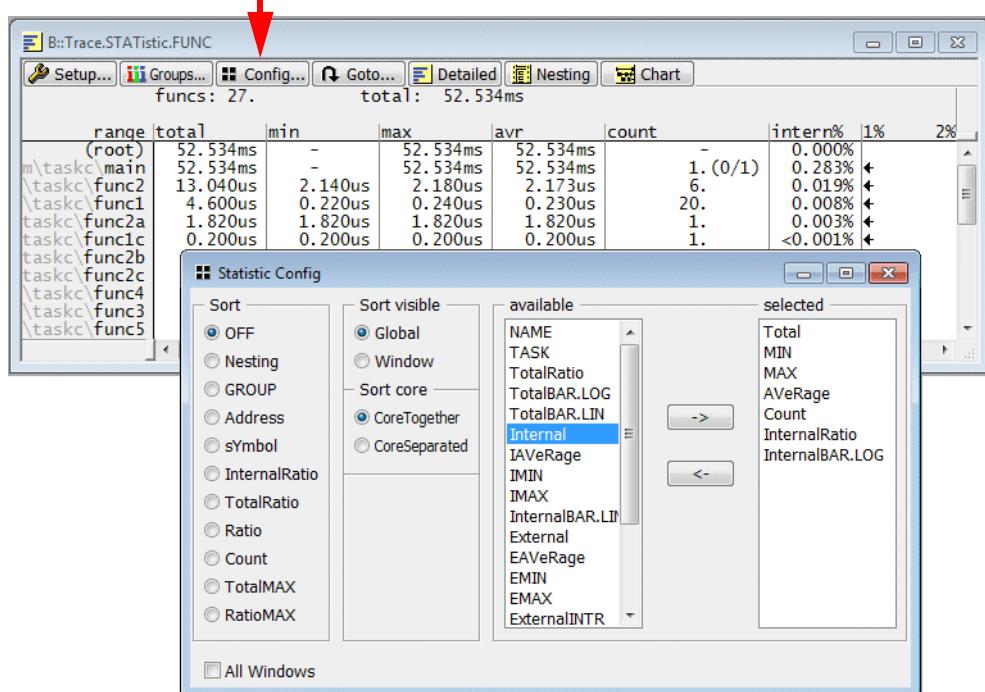


If the number of missing function entries or exits is higher than 1, the analysis performed by the command **Trace.STATistic.Func** might fail due to nesting problems. A detailed view of the trace contents is recommended.

columns (cont.)

intern% (InternalRatio, InternalBAR.LOG)	ratio of time within the function without subfunctions, TRAP handlers, interrupts
--	---

Pushing the **Config...** button allows to display additional columns



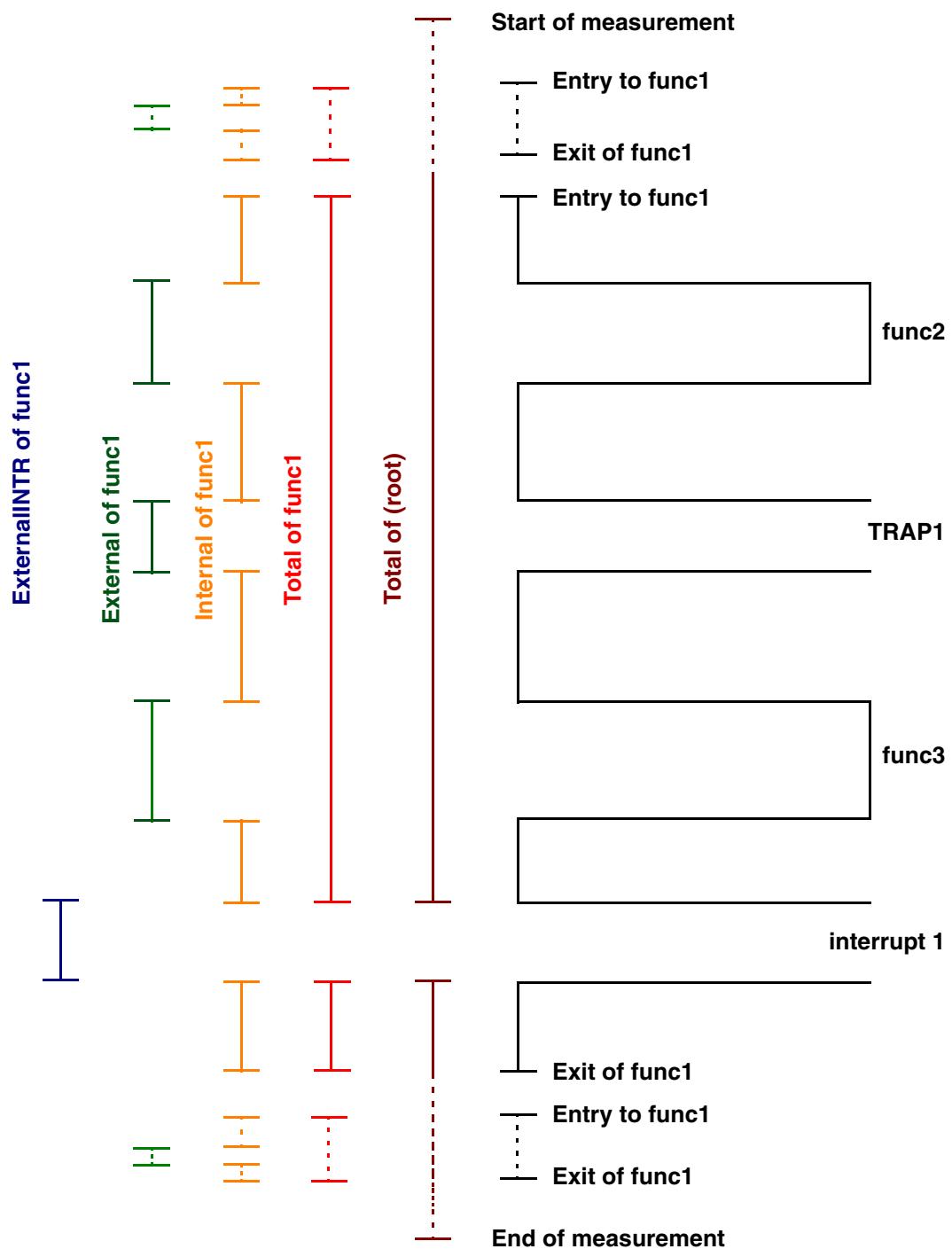
columns (cont.) - times only in function

Internal	total time between function entry and exit without called sub-functions, TRAP handlers, interrupt service routines
IAVeRage	average time between function entry and exit without called sub-functions, TRAP handlers, interrupt service routines
IMIN	shortest time between function entry and exit without called sub-functions, TRAP handlers, interrupt service routines
IMAX	longest time spent in the function between function entry and exit without called sub-functions, TRAP handlers, interrupt service routines
InternalRatio	$\frac{\text{Internal time of function}}{\text{Total measurement time}}$ as a numeric value.
InternalBAR	$\frac{\text{Internal time of function}}{\text{Total measurement time}}$ graphically.

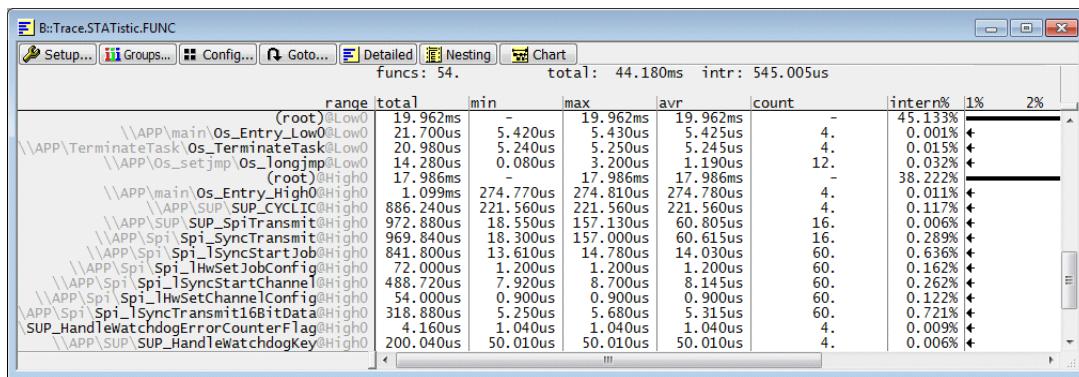
<i>columns (cont.) - times in sub-functions and TRAP handlers</i>	
External	total time spent within called sub-functions/TRAP handlers
EAVeRage	average time spent within called sub-functions/TRAP handlers
EMIN	shortest time spent within called sub-functions/TRAP handlers
EMAX	longest time spent within called sub-functions/TRAP handlers

<i>columns (cont.) - interrupt times</i>	
ExternalINTR	total time the function was interrupted
ExternalINTRMAX	max. time one function pass was interrupted
INTRCount	number of interrupts that occurred during the function run-time

The following graphic give an overview how times are calculated:



Function per Task



- HLL function**

`\\APP\\SUP\\SUP_SpiTransmit@High0`

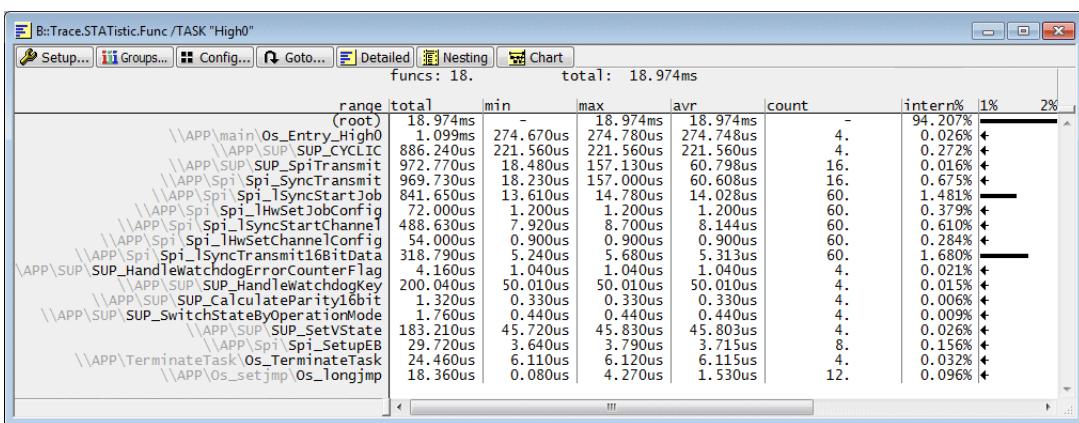
HLL function “SUP_SpiTransmit” running in task “High0”

- Root of call tree for task “High0”**

`(root)@High0`

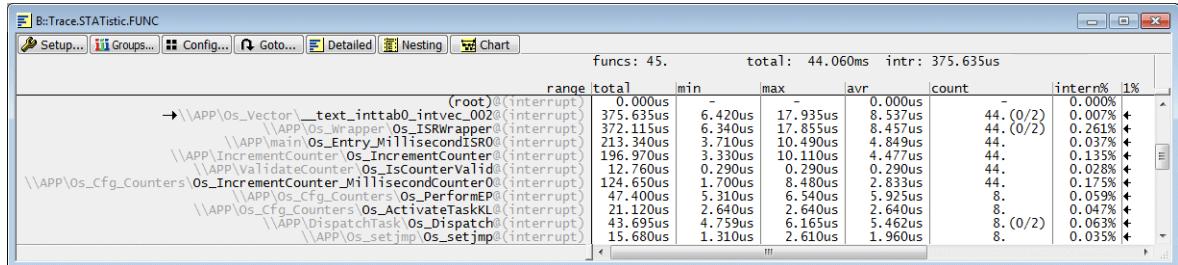
Trace.STATistic.Func /TASK <task_magic> | <task_id> | <task_name>

Trace.STATistic.Func /TASK "High0"

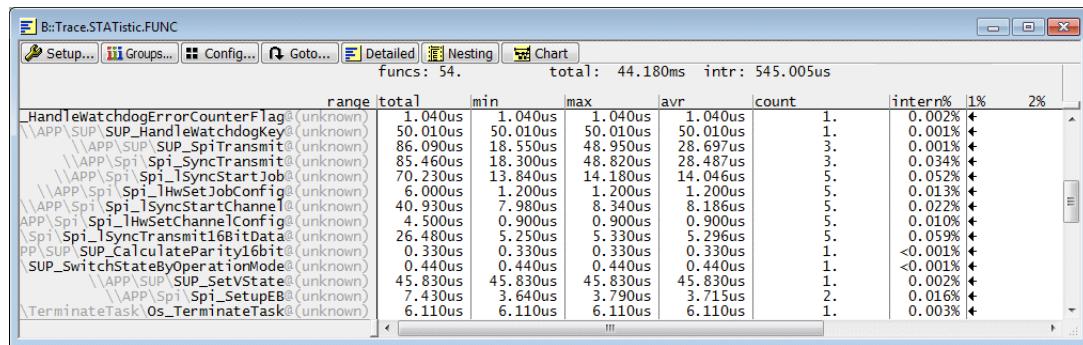


Interrupt Functions

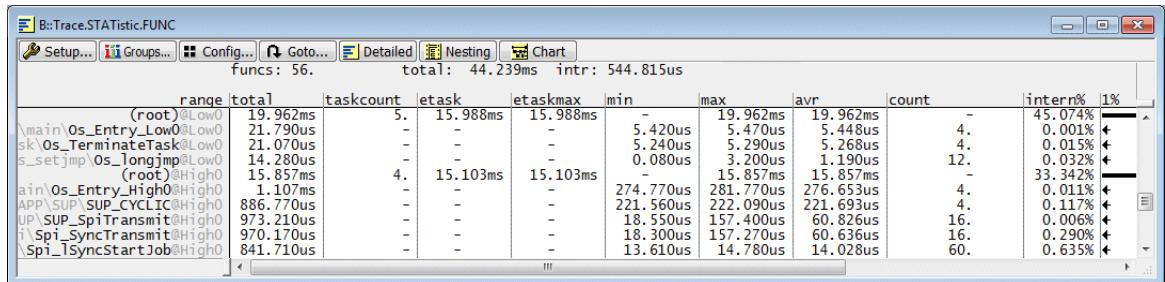
Interrupt are assigned to the @interrupt task.



The unknown Task

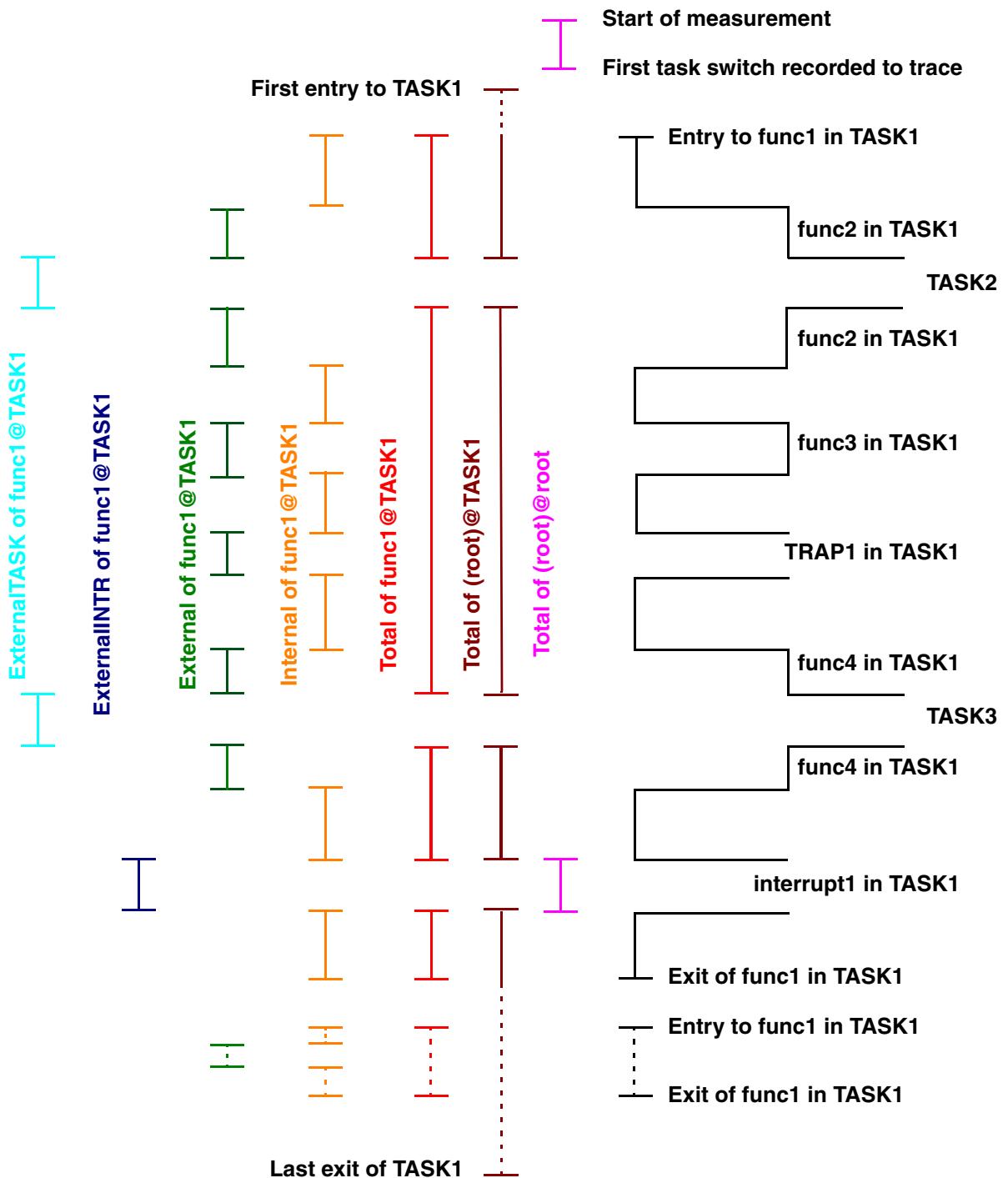


All function recorded before the first task switch record are assigned to the unknown task.



columns - task/thread related information

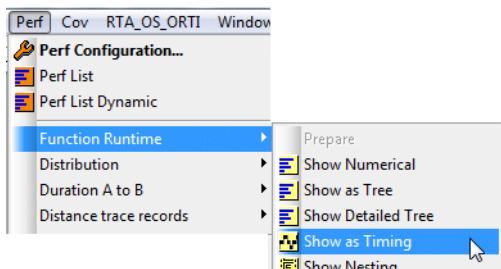
TASKCount	number of tasks that interrupt the function
ExternalTASK	total time in other tasks
ExternalTASKMAX	max. time 1 function pass was interrupted by a task



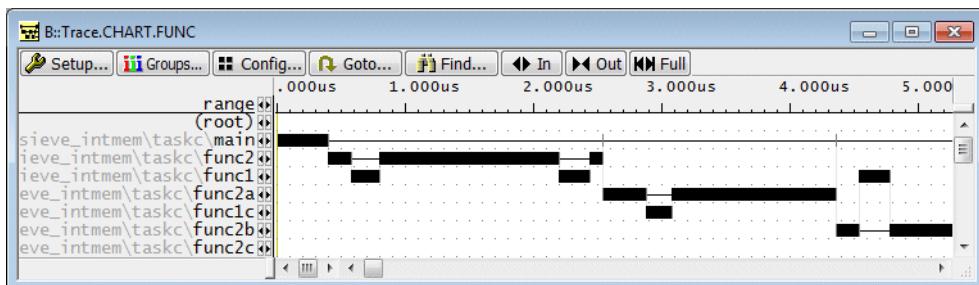
More Nesting Analysis Commands

Trace.Chart.Func

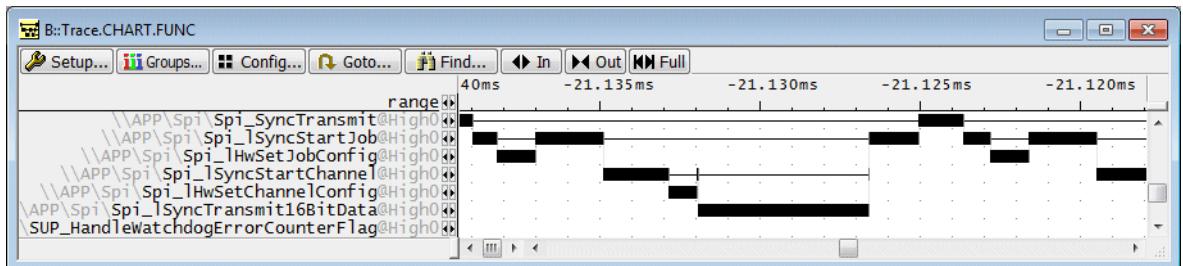
Nesting function run-time analysis
- graphical display

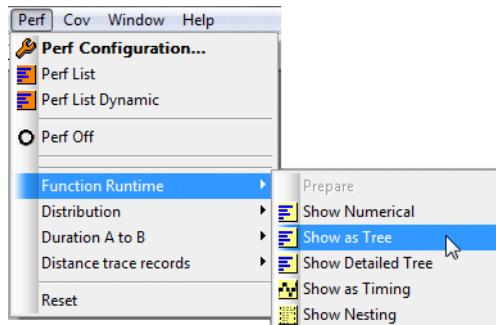


Look and Feel (No OS)



Look and Feel (OS)





Look and Feel (No OS)

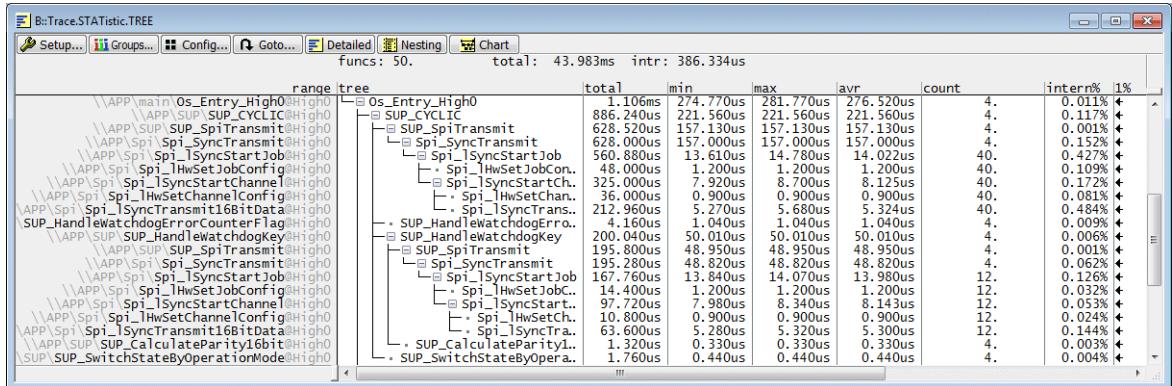
B:Trace.STATistic.TREE

Setup... Groups... Config... Goto... Detailed Nesting Chart

funcs: 35. total: 52.534ms

range	tree	total	min	max	avr	count	intern%	1%
(root)	(root)	52.534ms	-	52.534ms	52.534ms	-	0.000%	
sieve_intmem\taskc\main	└ main	52.534ms	-	52.534ms	52.534ms	1. (0/1)	0.283%	↳
sieve_intmem\taskc\func2	└ func2	2.140us	2.140us	2.140us	2.140us	1.	0.003%	↳
sieve_intmem\taskc\func1	└ func1	0.460us	0.220us	0.240us	0.230us	2.	<0.001%	↳
sieve_intmem\taskc\func2a	└ func2a	1.820us	1.820us	1.820us	1.820us	1.	0.003%	↳
sieve_intmem\taskc\func1c	└ func1c	0.200us	0.200us	0.200us	0.200us	1.	<0.001%	↳
sieve_intmem\taskc\func2b	└ func2b	1.100us	1.100us	1.100us	1.100us	1.	0.001%	↳
sieve_intmem\taskc\func1	└ func1	0.240us	0.240us	0.240us	0.240us	1.	<0.001%	↳
sieve_intmem\taskc\func2c	└ func2c	1.620us	1.620us	1.620us	1.620us	1.	0.003%	↳
sieve_intmem\taskc\func4	└ func4	0.400us	0.400us	0.400us	0.400us	1.	<0.001%	↳
sieve_intmem\taskc\func3	└ func3	0.100us	0.100us	0.100us	0.100us	1.	<0.001%	↳
sieve_intmem\taskc\func5	└ func5	0.220us	0.220us	0.220us	0.220us	1.	<0.001%	↳
sieve_intmem\taskc\func6	└ func6	0.460us	0.460us	0.460us	0.460us	1.	<0.001%	↳
sieve_intmem\taskc\func7	└ func7	0.400us	0.400us	0.400us	0.400us	1.	<0.001%	↳
sieve_intmem\taskc\func8	└ func8	3.480us	3.480us	3.480us	3.480us	1.	0.006%	↳
sieve_intmem\taskc\func9	└ func9	2.240us	2.240us	2.240us	2.240us	1.	0.002%	↳
sieve_intmem\taskc\func1	└ func1	0.920us	0.220us	0.240us	0.230us	4.	0.001%	↳
sieve_intmem\taskc\func10	└ func10	8.780us	8.780us	8.780us	8.780us	1.	0.016%	↳
sieve_intmem\taskc\func11	└ func11	0.580us	0.580us	0.580us	0.580us	1.	0.001%	↳
sieve_intmem\taskc\func13	└ func13	1.300us	1.300us	1.300us	1.300us	1.	<0.001%	↳
sieve_intmem\taskc\func13	└ func13	0.940us	0.940us	0.940us	0.940us	1.	<0.001%	↳
sieve_intmem\taskc\func13	└ func13	0.620us	0.620us	0.620us	0.620us	1.	<0.001%	↳

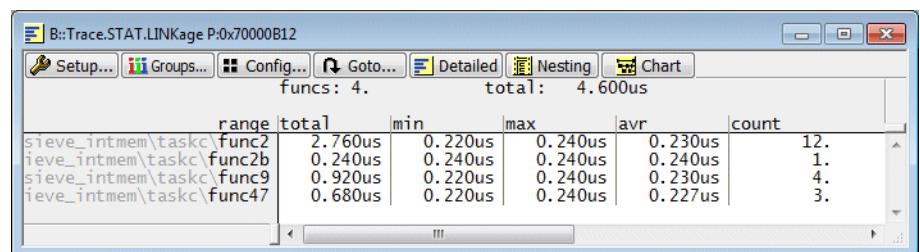
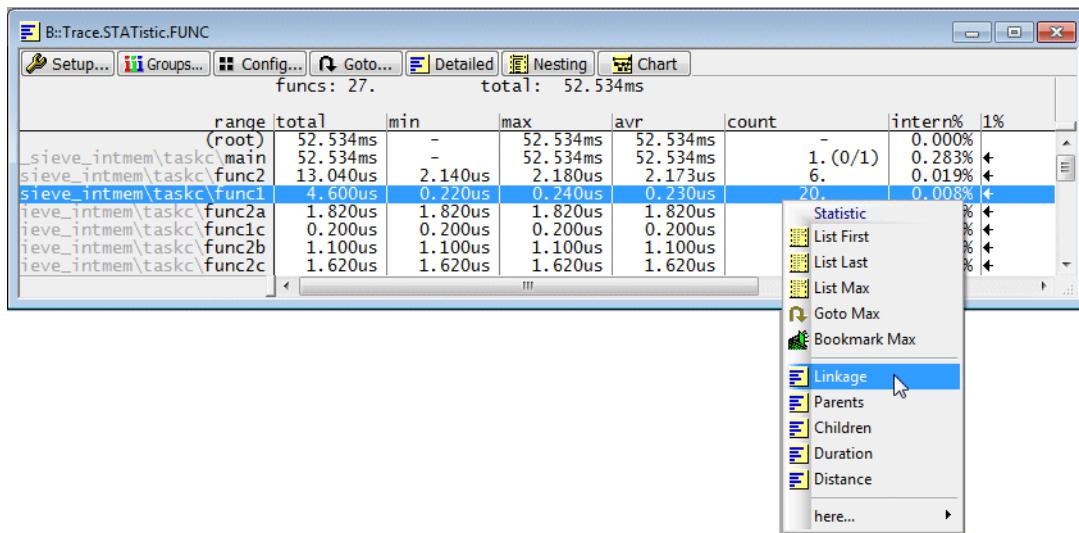
Look and Feel (OS)



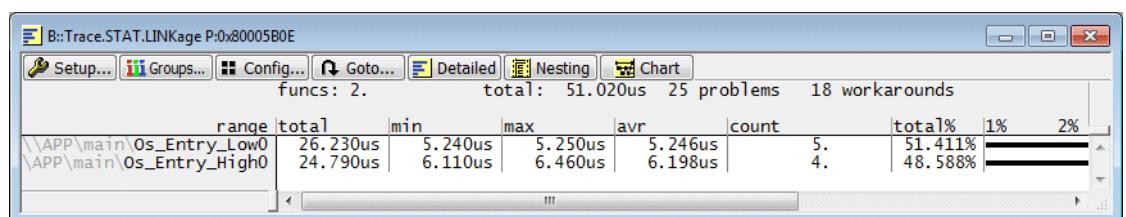
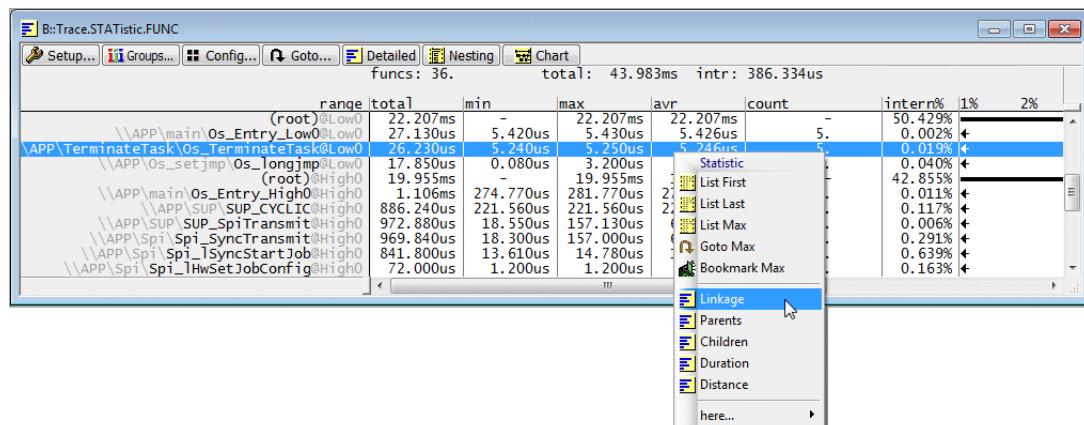
It is also possible to get a task/process-specific tree.

```
Trace.STATistic.TREE /TASK "High0"
```

Look and Feel (No OS)



Look and Feel (OS)



Nesting Function Run-Time Analysis for SMP

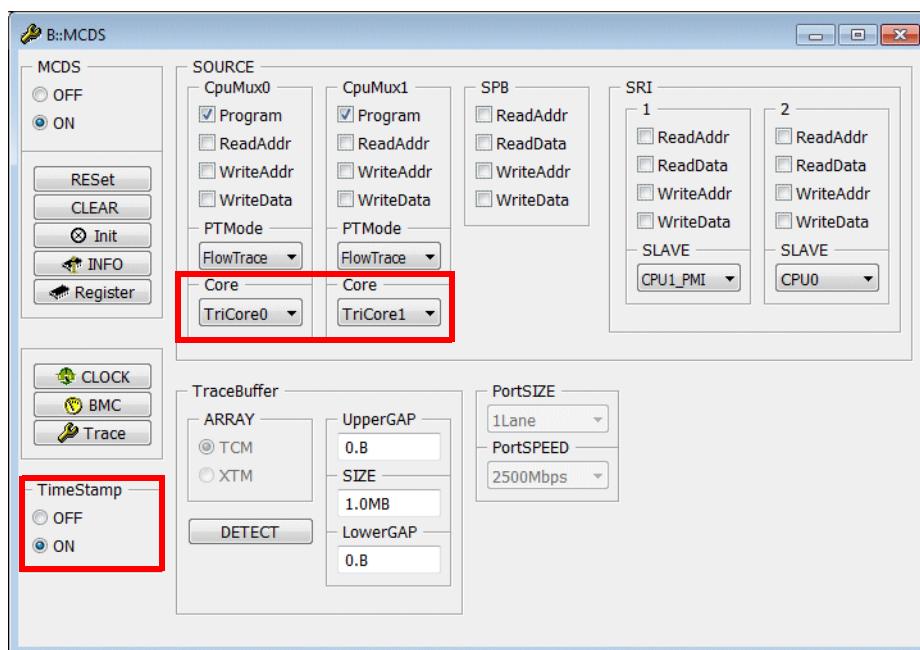
It is recommended to reduce the trace information generated by MCDS to the required minimum.

- To make best use of the available trace memory.

Optimum MCDS Configuration (OS)

Connect the cores of interest to the trace multiplexer

Since the serial off-chip trace provides imprecise timestamps Timestamp Messages have to be enabled for any run-time measurement.



```
MCDS.SOURCE.Set CpuMux0.Core TriCore0 ; enable TC 1.6.1 CPU0 as
                                         ; trace source

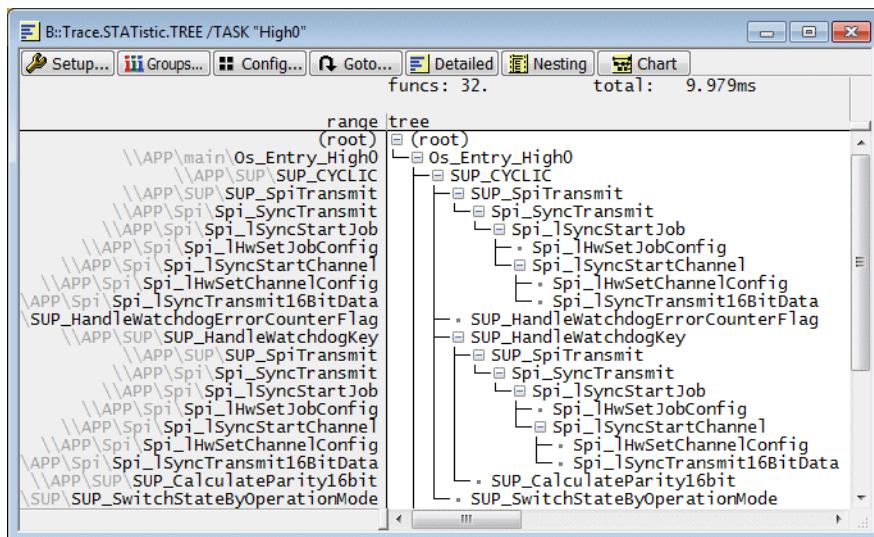
MCDS.SOURCE.Set CpuMux1.Core TriCore1 ; enable TC 1.6.1 CPU1 as
                                         ; trace source

MCDS.TimeStamp ON ; enable Timestamp Messages

CLOCK.ON
```

TRACE32 PowerView builds up a separate call tree for each task/process.

Trace.STATistic.TREE /TASK "High0"



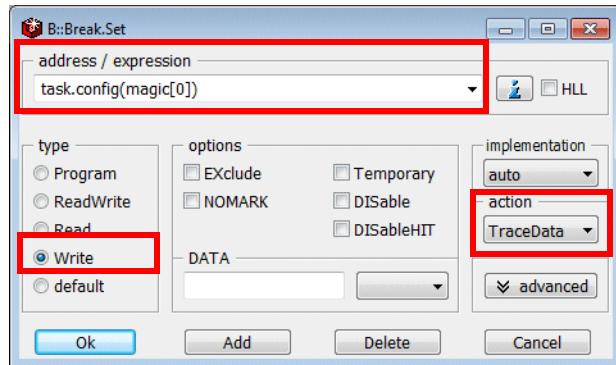
In order to hook a function entry/exit into the correct call tree, TRACE32 PowerView needs to know which task was running when the entry/exit occurred.

The standard way to get information on the current task is to advise the MCDS to generate trace messages for the instruction flow and the task switches. For details refer to the [“OS-Aware Tracing - SMP Systems”](#) in Training AURIX Tracing, page 178 (training_aurix_trace.pdf).

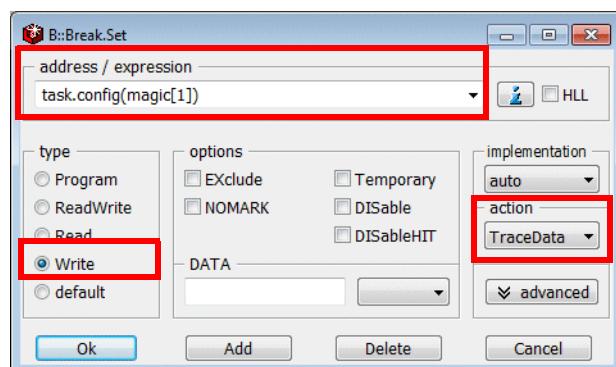
In order to do the optimum setting for the nesting analysis advise the Processor Observation Blocks to generate trace messages for the complete instruction flow and for the task switches.

Filter settings

Set filter for TC1.6.1 CPU0:



Set filter for TC1.6.1 CPU1:



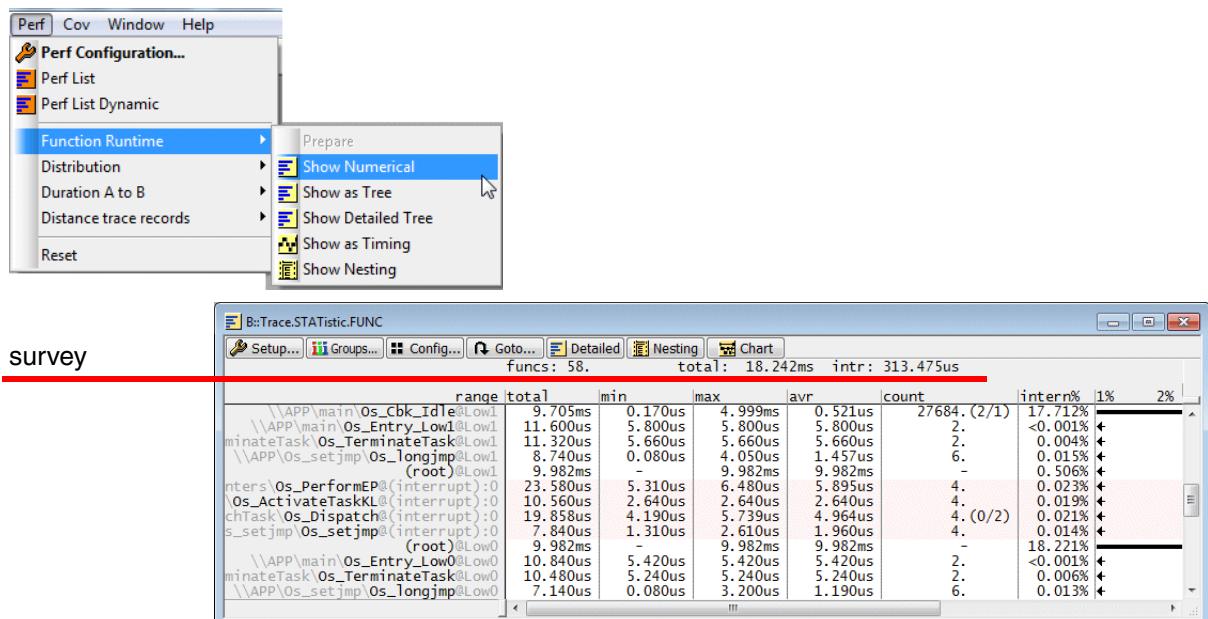
```
; default setting since 2015-01
Trace.STATistic.InterruptIsFunction
```

Statistics Items

Trace.STATistic.Func

Nesting function run-time analysis

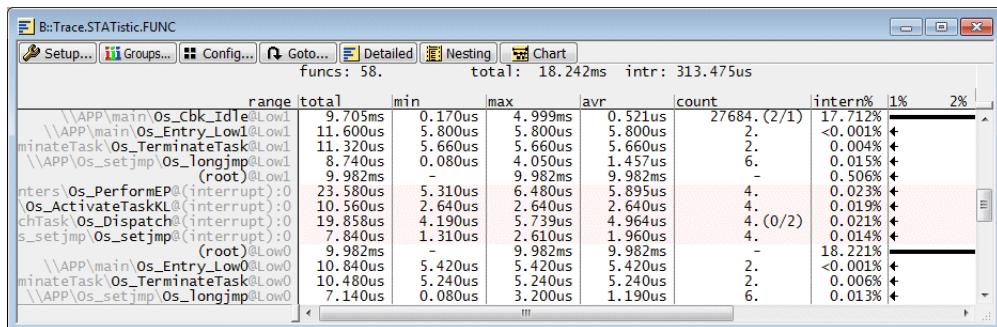
- numeric display
- core information is discarded except for @ (unknown) and @ (interrupt)



funcs: 90. total: 18.706ms intr: 11.929ms 21 problems 131 workarounds

survey	
funcs: <number>	number of functions in the trace
total: <time>	total measurement time
intr: <time>	total time in interrupt service routines

survey (issue indication)	
stopped: <time>	The analyzed trace recording contains program stops. <time> indicates the total time the program execution was stopped.
<number> problems	The nested analysis contains problems. Please contact support@lauterbach.com .
<number> workarounds	The nested analysis contains issues, but TRACE32 found solutions for them. It is recommended to perform a sanity check on the proposed solutions.
stack overflow at <record>	The nested analysis exceeds the nesting level 200. It is highly likely that the function exit for an often called function is missing. The command Trace.STATistic.TREE can help you to identify the function. If you need further help please contact support@lauterbach.com .
stack underflow at <record>	The nested analysis exceeds the nesting level 200. It is highly likely that the function entry for an often executed function is missing. The command Trace.STATistic.TREE can help you to identify the function. If you need further help please contact support@lauterbach.com .



columns

range (NAME)	function name, sorted by their recording order as default
--------------	---

- **HLL function**

`\APP\Os_Setjmp\Os_Longjmp@Low1`

HLL function “Os_Longjmp” running in task “Low1”

- **Root of call tree for task “High0”**

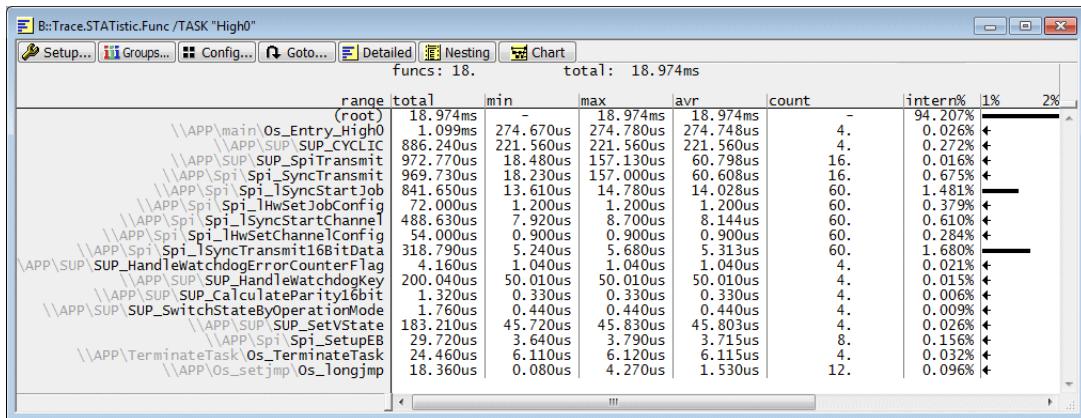
`(root)@High0`

The function nesting is regarded as tree, “root@High0” is the root of the call tree for the task “High0”.

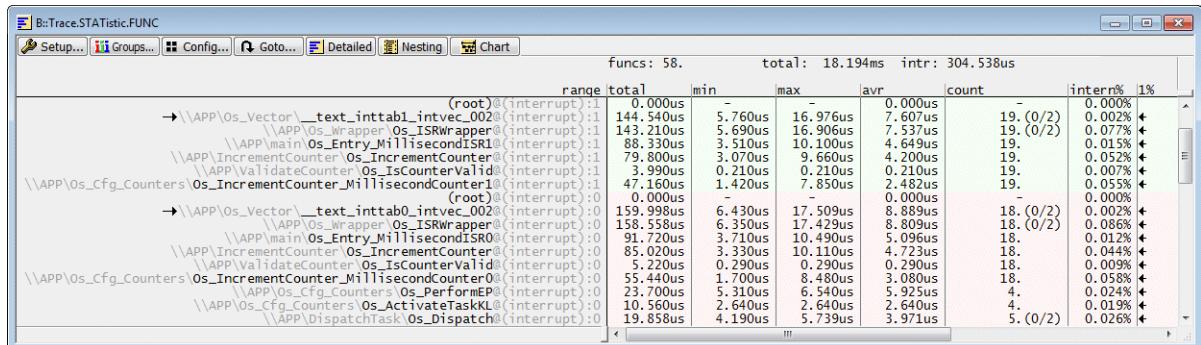
Trace.STATistic.Func /TASK <task_magic> | <task_d> | <task_name>

Please be aware that no core information is provided for tasks and their functions.

Trace.STATistic.Func /TASK "High0"



Interrupt are assigned to the @ (interrupt) task. Core information is provided for the @ (interrupt) task.

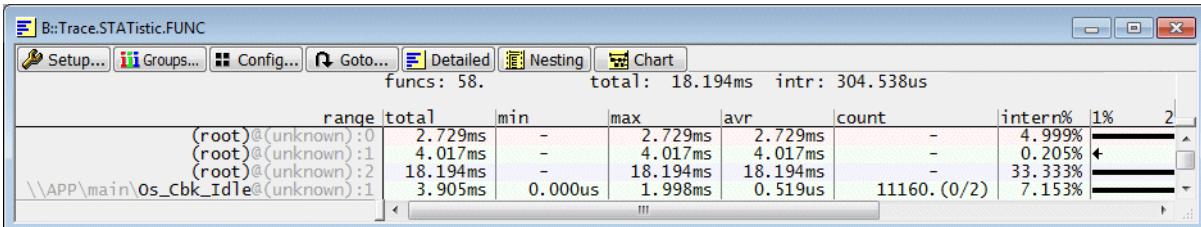


Interrupt (branch to an address of an interrupt vector).

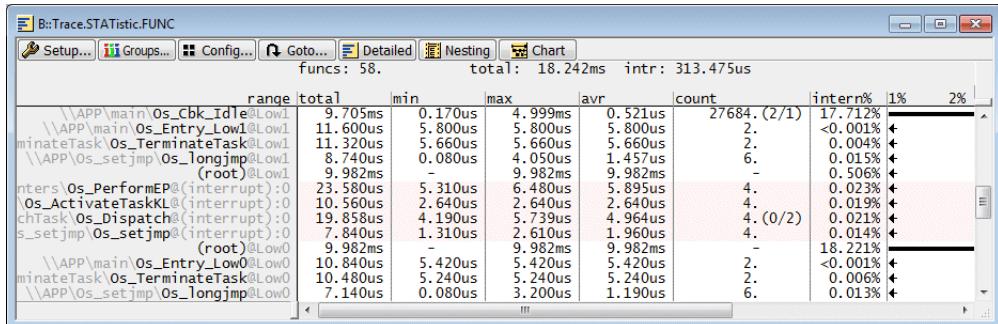
→ \APP\Os_Vector__text_inttab1_intvec_002@(interrupt):1

The unknown Task

All function recorded before the first task switch recorded are assigned to the @ (unknown) task. Core information is provided for the @ (unknown) task.

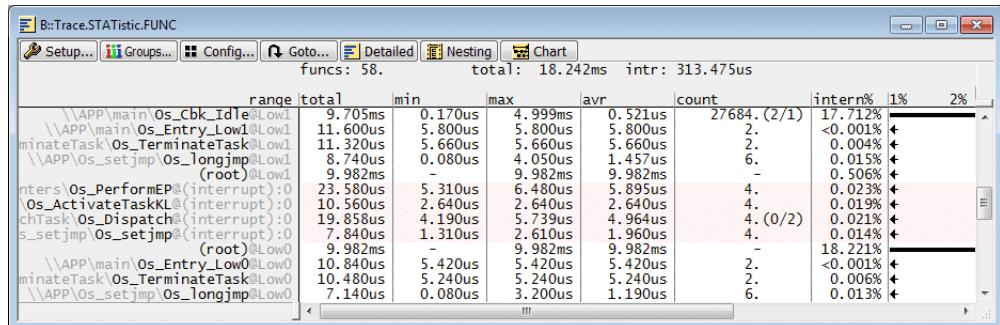


Since no trace information is recorded for TC 1.6.1 CPU2 total of (root)@(unknown):2 is equal to the complete recording time.



columns (cont.)

total	total time within the function
min	shortest time between function entry and exit, time spent in interrupt service routines is excluded No min time is displayed if a function exit was never executed.
max	longest time between function entry and exit, time spent in interrupt service routines is excluded
avr	average time between function entry and exit, time spent in interrupt service routines is excluded



columns (cont.)

count

number of times within the function

If function entries or exits are missing, this is displayed in the following format:

<times within the function>. (<number of missing function entries>I<number of missing function exits>).

3671. (0/1)

Interpretation examples:

1. 2. (2/0): 2 times within the function, 2 function entries missing
2. 4. (0/3): 4 times within the function, 3 function exits missing
3. 11. (1/1): 11 times within the function, 1 function entry and 1 function exit is missing.



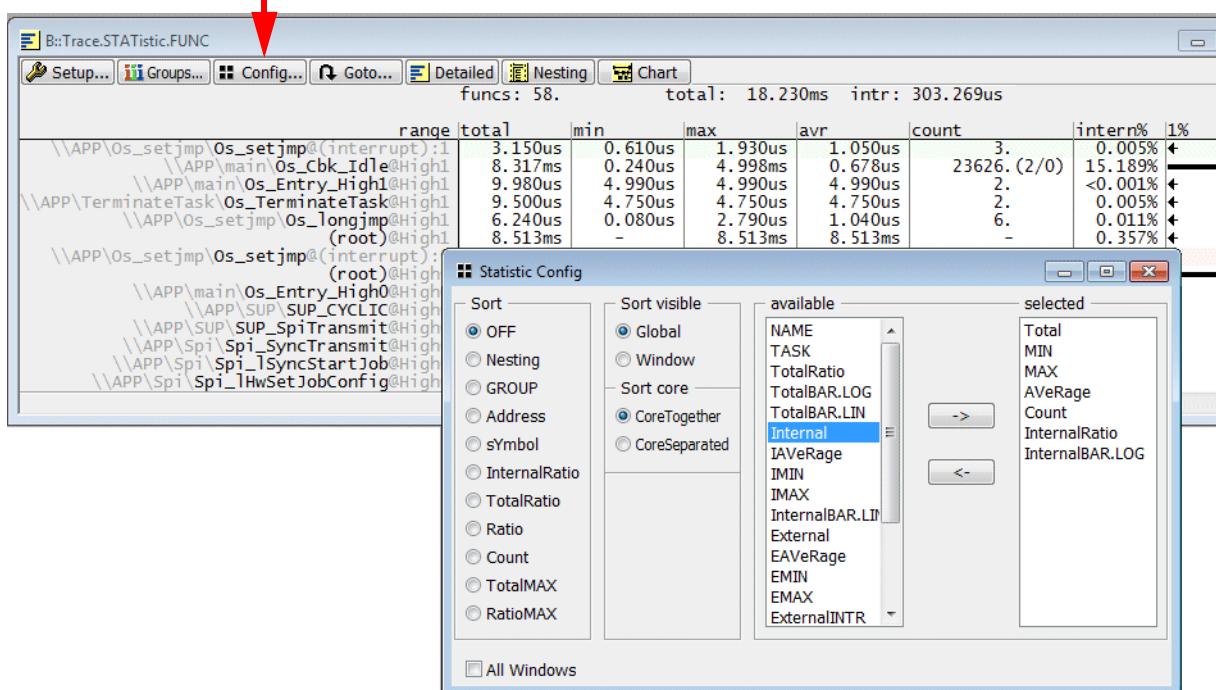
If the number of missing function entries or exits is higher than 1, the analysis performed by the command **Trace.STATistic.Func** might fail due to nesting problems. A detailed view to the trace contents is recommended.

columns (cont.)

intern% (InternalRatio, InternalBAR.LOG)

ratio of time within the function without subfunctions, TRAP handlers, interrupts

Pushing the **Config...** button allows to display additional columns

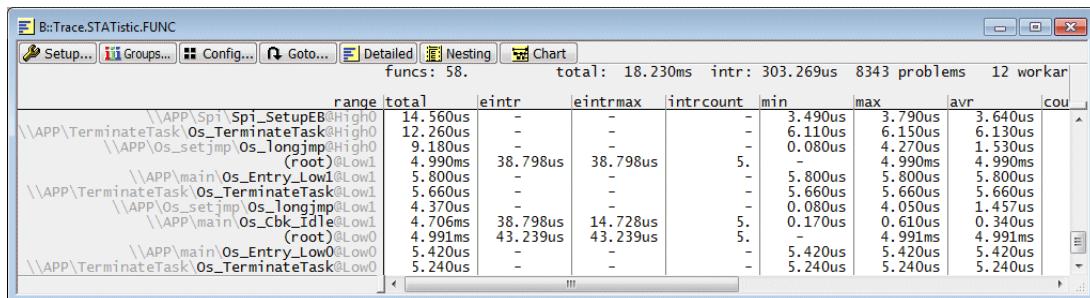


columns (cont.) - times only in function

Internal	total time between function entry and exit without called sub-functions, TRAP handlers, interrupt service routines
IAVeRage	average time between function entry and exit without called sub-functions, TRAP handlers, interrupt service routines
IMIN	shortest time between function entry and exit without called sub-functions, TRAP handlers, interrupt service routines
IMAX	longest time spent in the function between function entry and exit without called sub-functions, TRAP handlers, interrupt service routines
InternalRatio	<i><Internal time of function>/<Total measurement time></i> as a numeric value.
InternalBAR	<i><Internal time of function>/<Total measurement time></i> graphically.

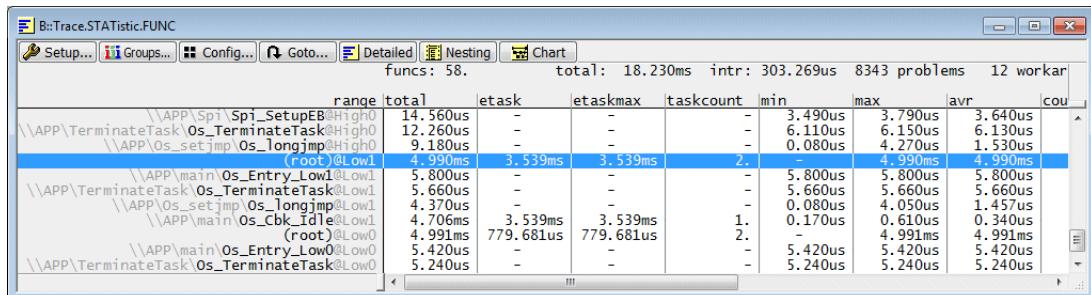
columns (cont.) - times in sub-functions and TRAP handlers

External	total time spent within called sub-functions/TRAP handlers
EAVeRage	average time spent within called sub-functions/TRAP handlers
EMIN	shortest time spent within called sub-functions/TRAP handlers
EMAX	longest time spent within called sub-functions/TRAP handlers



columns (cont.) - interrupt times

ExternalINTR	total time the function was interrupted
ExternalINTRMAX	max. time one function pass was interrupted
INTRCount	number of interrupts that occurred during the function run-time

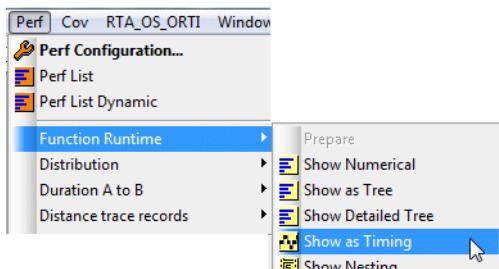


columns - task/thread related information

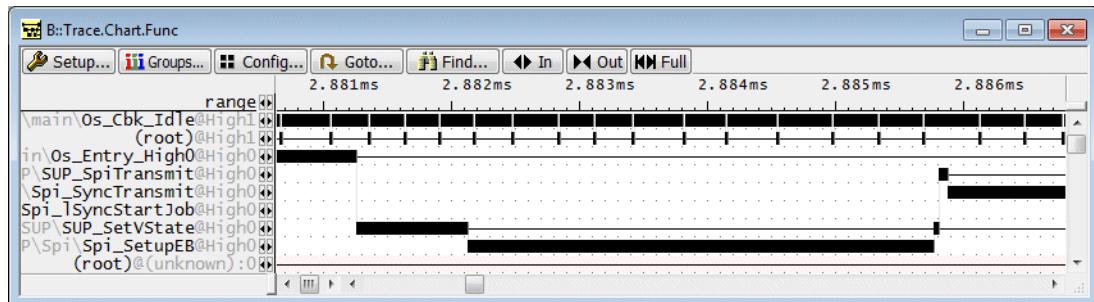
TASKCount	number of tasks that interrupt the function/task
ExternalTASK	total time in other tasks
ExternalTASKMAX	max. time 1 function/task pass was interrupted by a task

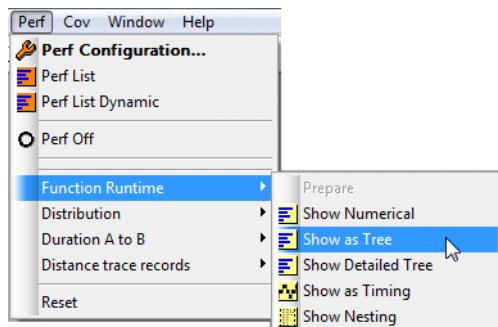
Trace.Chart.Func

Nesting function run-time analysis
- graphical display

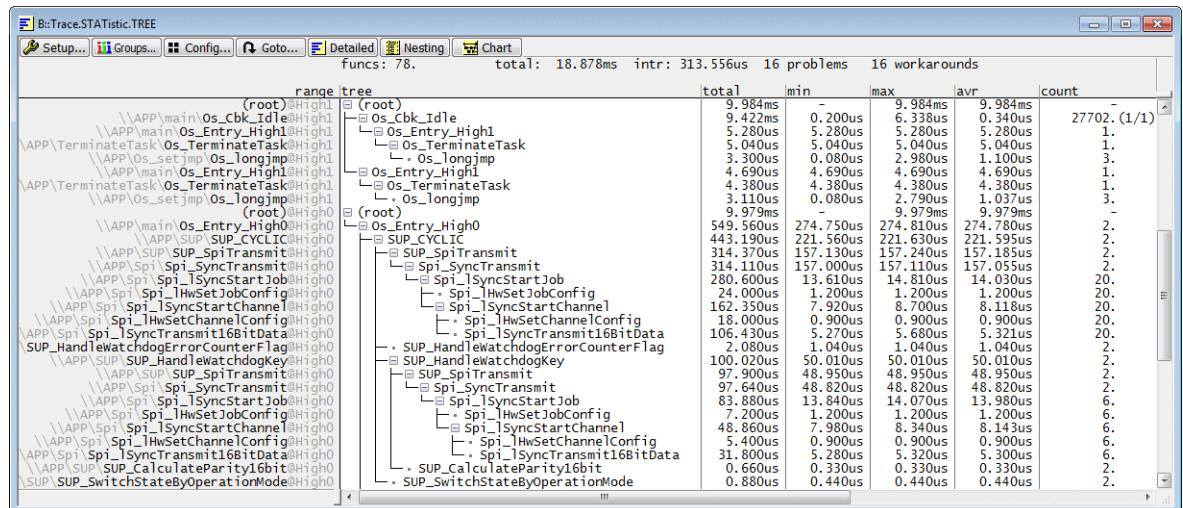


Look and Feel (OS)





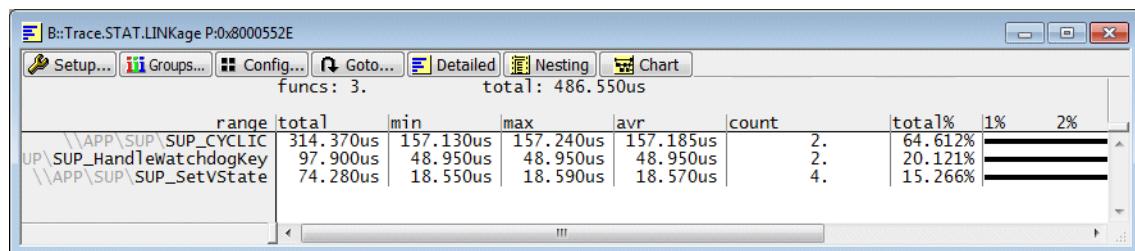
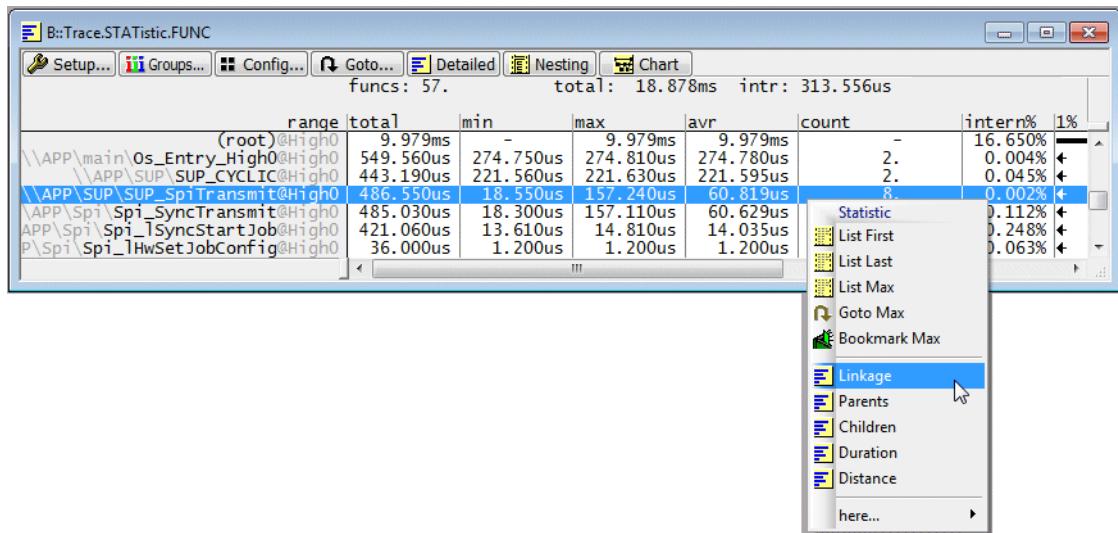
Look and Feel (OS)



It is also possible to get a task/process-specific tree.

Trace.STATistic.TREE /TASK "High0"

Look and Feel (OS)



Trace-based Code Coverage

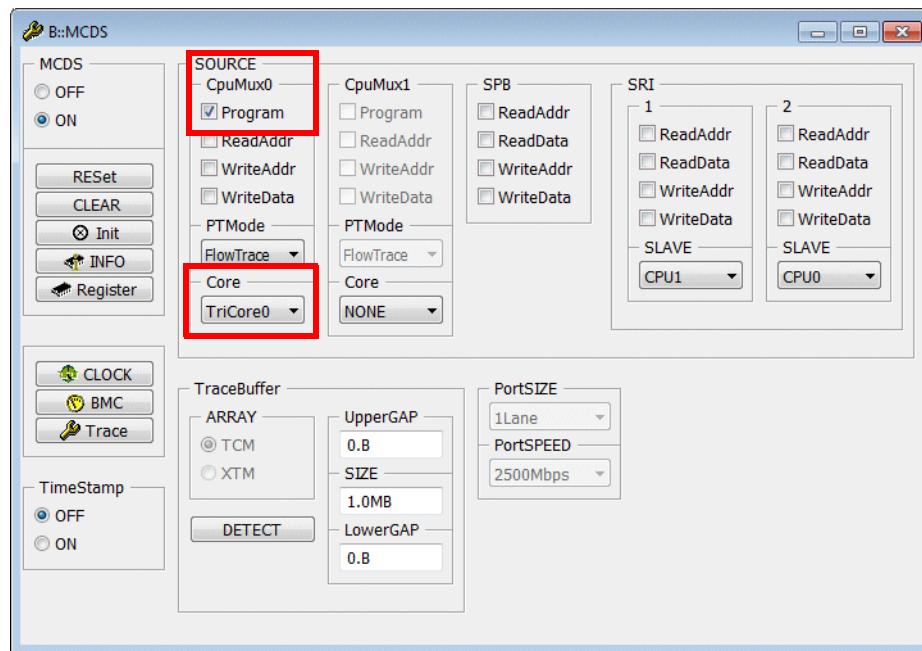
The manual “[Application Note for Trace-Based Code Coverage](#)” (app_code_coverage.pdf) gives a detailed introduction to the trace-based code coverage. However, the manual does not contain details about the architecture-specific setups. Here is an overview of the setups for TriCore™ AURIX™.

General SetUp

Single-Core and AMP Systems

The core under debug has to be configured for the trace multiplexer.

Instruction Pointer Call Messages are sufficient for Trace-based Code Coverage. Time information is not required.



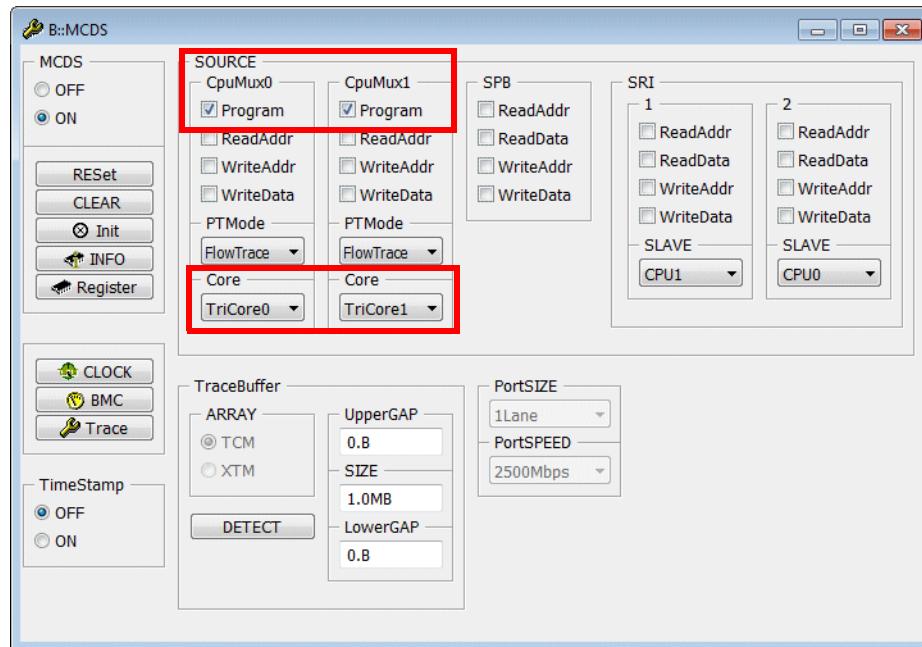
```
MCDS.SOURCE.Set CpuMux0.Core TriCore0      ; enable TC 1.6.1 CPU0 as
                                                ; trace source

MCDS.SOURCE.Set CpuMux0.Program ON          ; enable Instruction Pointer
                                                ; Call Messages for
                                                ; TC 1.6.1 CPU0
```

Since each core in a AMP system executes an independent task, Trace-based Code Coverage has to be performed per core.

Up to two cores can be configured for the trace multiplexer.

Instruction Pointer Call Messages are sufficient for Trace-based Code Coverage. Time information is not required.



```
MCDS.SOURCE.Set CpuMux0.Core TriCore0 ; enable TC 1.6.1 CPU0 as
                                         ; trace source

MCDS.SOURCE.Set CpuMux1.Core TriCore1 ; enable TC 1.6.1 CPU1 as
                                         ; trace source

MCDS.SOURCE.Set CpuMux0.Program ON   ; enable Instruction Pointer
                                         ; Call Messages for
                                         ; TC 1.6.1 CPU0

MCDS.SOURCE.Set CpuMux1.Program ON   ; enable Instruction Pointer
                                         ; Call Messages for
                                         ; TC 1.6.1 CPU1
```

Since the core information is discarded for the Trace-based Code Coverage, the same procedure can be used as for single-core/AMP systems.