



# OS Awareness Manual eCos

TRACE32 Online Help

TRACE32 Directory

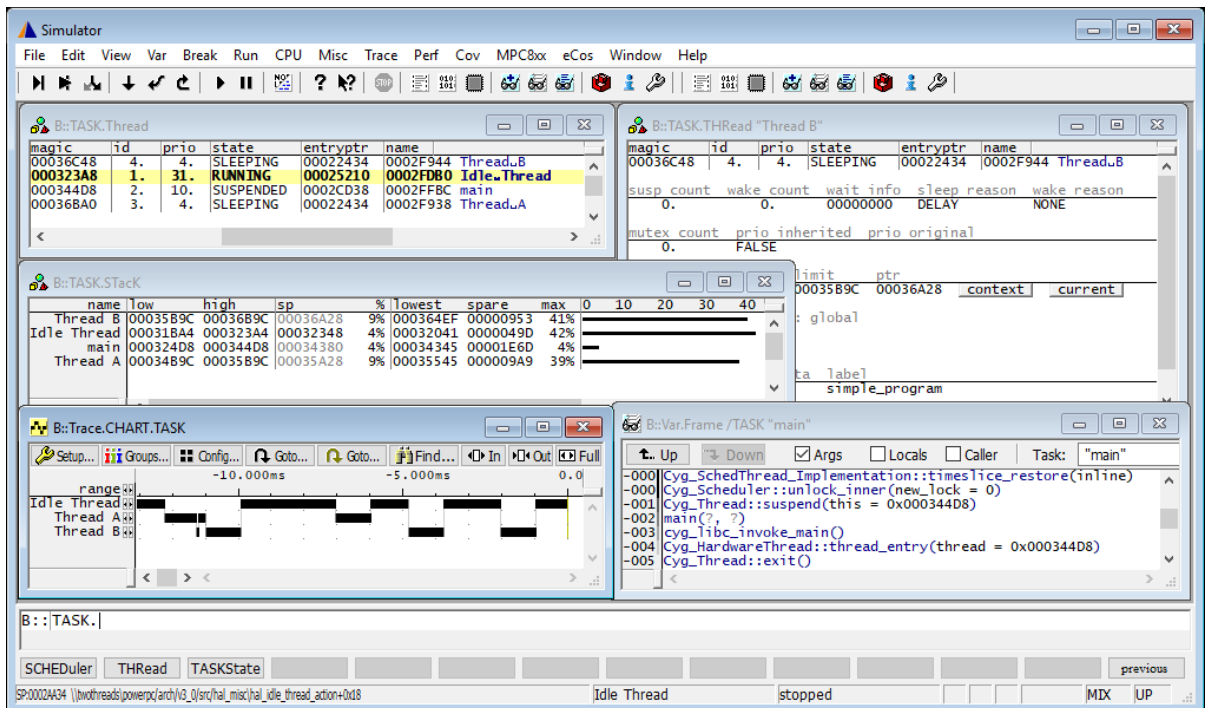
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## History

04-Feb-21 Removing legacy command TASK.TASKState.

## Overview



The OS Awareness for eCos contains special extensions to the TRACE32 Debugger. This manual describes the additional features, such as additional commands and statistic evaluations.

## Terminology

eCos uses the term “threads”. If not otherwise specified, the TRACE32 term “task” corresponds to eCos threads.

# Brief Overview of Documents for New Users

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## Architecture-independent information:

- **“Training Basic Debugging”** (training\_debugger.pdf): Get familiar with the basic features of a TRACE32 debugger.
- **“T32Start”** (app\_t32start.pdf): T32Start assists you in starting TRACE32 PowerView instances for different configurations of the debugger. T32Start is only available for Windows.
- **“General Commands”** (general\_ref\_<x>.pdf): Alphabetic list of debug commands.

## Architecture-specific information:

- **“Processor Architecture Manuals”**: These manuals describe commands that are specific for the processor architecture supported by your Debug Cable. To access the manual for your processor architecture, proceed as follows:
  - Choose **Help** menu > **Processor Architecture Manual**.
- **“OS Awareness Manuals”** (rtos\_<os>.pdf): TRACE32 PowerView can be extended for operating system-aware debugging. The appropriate OS Awareness manual informs you how to enable the OS-aware debugging.

## Supported Versions

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Currently eCos is supported for the following versions:

- eCos V1.3, V2.0 and V3.0 on ARM/Cortex, ColdFire, MIPS, NIOS-II and PowerPC

# Configuration

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The **TASK.CONFIG** command loads an extension definition file called “ecos.t32” (directory “`~/demo/<processor>/kernel/ecos`”). It contains all necessary extensions.

Automatic configuration tries to locate the eCos internals automatically. For this purpose all symbol tables must be loaded and accessible at any time the OS Awareness is used.

If you want to have dual port access for the display functions (display “On The Fly”), you have to map emulation or shadow memory to the address space of all used system tables.

For system resource display and trace functionality, you can do an automatic configuration of the OS Awareness. For this purpose it is necessary that all system internal symbols are loaded and accessible at any time, the OS Awareness is used. Each of the **TASK.CONFIG** arguments can be substituted by '0', which means that this argument will be searched and configured automatically. For a fully automatic configuration omit all arguments:

## **TASK.CONFIG ecos**

See also the example “`~/demo/<processor>/kernel/ecos/ecos.cmm`”.

# Quick Configuration Guide

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To get a quick access to the features of the OS Awareness for eCos with your application, follow this roadmap:

1. Copy the files `ecos.t32` and “`ecos.men`” to your project directory (from TRACE32 directory “`~/demo/<processor>/kernel/ecos`”).
2. Start the TRACE32 Debugger.
3. Load your application as normal.
4. Execute the command:

```
TASK.CONFIG ecos
```

See “[Configuration](#)”.

5. Execute the command:

```
MENU.ReProgram ecos
```

See “[eCos Specific Menu](#)”.

6. Start your application.

Now you can access the eCos extensions through the menu.

In case of any problems, please carefully read the previous Configuration chapters.

## Hooks & Internals in eCos

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No hooks are used in the kernel.

For detecting the current running task, the kernel symbol “`Cyg_Scheduler_Base::current_thread`” is used.

For retrieving the kernel data structures, the OS Awareness uses the global kernel symbols and structure definitions (`Cyg_*`). Ensure that access to those structures is possible every time when features of the OS Awareness are used.

**The OS Awareness for eCos needs a linked thread list to operate correctly. To ensure this, the configuration of eCos must include the `#define macro CYGVAR_KERNEL_THREADS_LIST`. Using the eCos Configuration Tool, enable “Configuration -> eCos kernel -> Thread-related options -> Keep track of all threads using a linked list”.**

# Features

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The OS Awareness for eCos supports the following features.

## Display of Kernel Resources

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The extension defines new commands to display various kernel resources. Information on the following eCos components can be displayed:

<b>TASK.SCHEDuler</b>	Scheduler
<b>TASK.THRead</b>	Threads

For a description of the commands, refer to chapter “[eCos Commands](#)”.

When working with emulation memory or shadow memory, these resources can be displayed “On The Fly”, i.e. while the target application is running, without any intrusion to the application. If using this dual port memory feature, be sure that emulation memory is mapped to all places, where eCos holds its tables.

When working only with target memory, the information will only be displayed if the target application is stopped.

## Task Stack Coverage

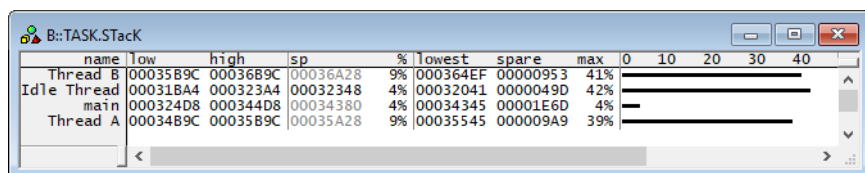
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For stack usage coverage of tasks, you can use the **TASK.STack** command. Without any parameter, this command will open a window displaying with all active tasks. If you specify only a task magic number as parameter, the stack area of this task will be automatically calculated.

To use the calculation of the maximum stack usage, a stack pattern must be defined with the command **TASK.STack.PATtern** (default value is zero).

To add/remove one task to/from the task stack coverage, you can either call the **TASK.STack.ADD** or **TASK.STack.ReMove** commands with the task magic number as the parameter, or omit the parameter and select the task from the **TASK.STack.\*** window.

It is recommended to display only the tasks you are interested in because the evaluation of the used stack space is very time consuming and slows down the debugger display.



name	low	high	sp	%	lowest	spare	max	0	10	20	30	40
Thread B	00035B9C	00036B9C	00036A28	9%	000364EF	00000953	41%					
Idle Thread	00031BA4	000323A4	00032348	4%	00032041	0000049D	42%					
main	000324D8	000344D8	00034380	4%	00034345	00001E6D	4%					
Thread A	00034B9C	00035B9C	00035A28	9%	00035545	000009A9	39%					

# Task-Related Breakpoints

Any breakpoint set in the debugger can be restricted to fire only if a specific task hits that breakpoint. This is especially useful when debugging code which is shared between several tasks. To set a task-related breakpoint, use the command:

**Break.Set** <address>|<range> [/<option>] /TASK <task> Set task-related breakpoint.

- Use a magic number, task ID, or task name for <task>. For information about the parameters, see “[What to know about the Task Parameters](#)” (general\_ref\_t.pdf).
- For a general description of the **Break.Set** command, please see its documentation.

By default, the task-related breakpoint will be implemented by a conditional breakpoint inside the debugger. This means that the target will *always* halt at that breakpoint, but the debugger immediately resumes execution if the current running task is not equal to the specified task.

**NOTE:** Task-related breakpoints impact the real-time behavior of the application.

On some architectures, however, it is possible to set a task-related breakpoint with *on-chip* debug logic that is less intrusive. To do this, include the option **/Onchip** in the **Break.Set** command. The debugger then uses the on-chip resources to reduce the number of breaks to the minimum by pre-filtering the tasks.

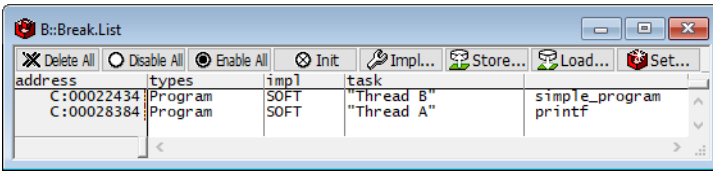
For example, on ARM architectures: *If* the RTOS serves the Context ID register at task switches, and *if* the debug logic provides the Context ID comparison, you may use Context ID register for less intrusive task-related breakpoints:

<b>Break.CONFIG.UseContextID ON</b>	Enables the comparison to the whole Context ID register.
<b>Break.CONFIG.MatchASID ON</b>	Enables the comparison to the ASID part only.
<b>TASK.List.tasks</b>	If <b>TASK.List.tasks</b> provides a trace ID ( <b>traceid</b> column), the debugger will use this ID for comparison. Without the trace ID, it uses the magic number ( <b>magic</b> column) for comparison.

When single stepping, the debugger halts at the next instruction, regardless of which task hits this breakpoint. When debugging shared code, stepping over an OS function may cause a task switch and coming back to the same place - but with a different task. If you want to restrict debugging to the current task, you can set up the debugger with **SETUP.StepWithinTask ON** to use task-related breakpoints for single stepping. In this case, single stepping will always stay within the current task. Other tasks using the same code will not be halted on these breakpoints.

If you want to halt program execution as soon as a specific task is scheduled to run by the OS, you can use the **Break.SetTask** command.





## Task Context Display

You can switch the whole viewing context to a task that is currently not being executed. This means that all register and stack-related information displayed, e.g. in [Register](#), [Data.List](#), [Frame](#) etc. windows, will refer to this task. Be aware that this is only for displaying information. When you continue debugging the application ([Step](#) or [Go](#)), the debugger will switch back to the current context.

To display a specific task context, use the command:

**Frame.TASK** [*<task>*]      Display task context.

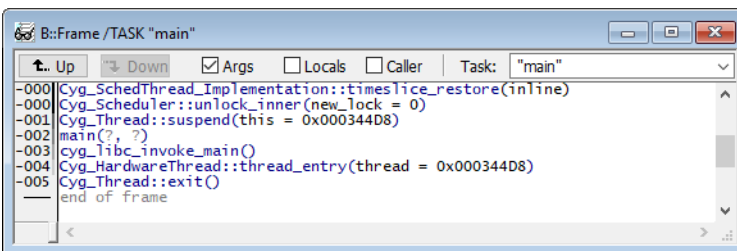
- Use a magic number, task ID, or task name for *<task>*. For information about the parameters, see [“What to know about the Task Parameters”](#) (general\_ref\_t.pdf).
- To switch back to the current context, omit all parameters.

To display the call stack of a specific task, use the following command:

**Frame /Task** *<task>*      Display call stack of a task.

If you'd like to see the application code where the task was preempted, then take these steps:

1. Open the **Frame /Caller /Task** *<task>* window.
2. Double-click the line showing the OS service call.



The OS Awareness supports symmetric multiprocessing (SMP).

An SMP system consists of multiple similar CPU cores. The operating system schedules the threads that are ready to execute on any of the available cores, so that several threads may execute in parallel. Consequently an application may run on any available core. Moreover, the core at which the application runs may change over time.

To support such SMP systems, the debugger allows a “system view”, where one TRACE32 PowerView GUI is used for the whole system, i.e. for all cores that are used by the SMP OS. For information about how to set up the debugger with SMP support, please refer to the [Processor Architecture Manuals](#).

All core relevant windows (e.g. [Register.view](#)) show the information of the current core. The [state line](#) of the debugger indicates the current core. You can switch the core view with the [CORE.select](#) command.

Target breaks, be they manual breaks or halting at a breakpoint, halt all cores synchronously. Similarly, a [Go](#) command starts all cores synchronously. When halting at a breakpoint, the debugger automatically switches the view to the core that hit the breakpoint.

Because it is undetermined, at which core an application runs, breakpoints are set on all cores simultaneously. This means, the breakpoint will always hit independently on which core the application actually runs.

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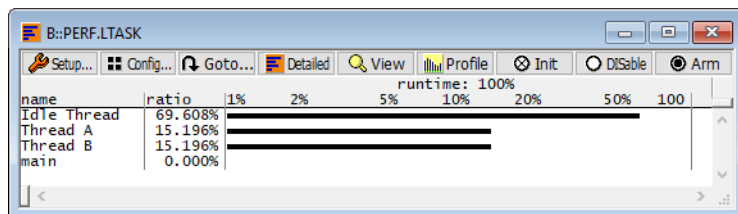
## Dynamic Task Performance Measurement

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The debugger can execute a dynamic performance measurement by evaluating the current running task in changing time intervals. Start the measurement with the commands [PERF.Mode TASK](#) and [PERF.Arm](#), and view the contents with [PERF.ListTASK](#). The evaluation is done by reading the ‘magic’ location (= current running task) in memory. This memory read may be non-intrusive or intrusive, depending on the [PERF.METHOD](#) used.

If [PERF](#) collects the PC for function profiling of processes in MMU-based operating systems ([SYSTEM.Option.MMUSPACES ON](#)), then you need to set [PERF.MMUSPACES](#), too.

For a general description of the [PERF](#) command group, refer to “[General Commands Reference Guide P](#)” (general\_ref\_p.pdf).



**NOTE:** This feature is *only* available, if your debug environment is able to trace task switches (program flow trace is not sufficient). It requires either an on-chip trace logic that is able to generate task information (eg. data trace), or a software instrumentation feeding one of TRACE32 software based traces (e.g. [FDX](#) or [Logger](#)). For details, refer to “[OS-aware Tracing](#)” (glossary.pdf).

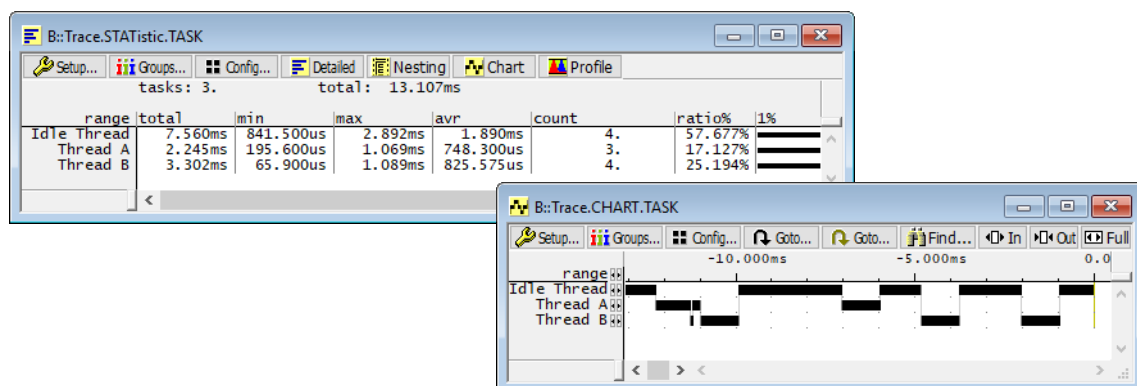
Based on the recordings made by the [Trace](#) (if available), the debugger is able to evaluate the time spent in a task and display it statistically and graphically.

To evaluate the contents of the trace buffer, use these commands:

<a href="#">Trace.List</a> <b>List.TASK</b> <b>Default</b>	Display trace buffer and task switches
<a href="#">Trace.STATistic</a> .TASK	Display task runtime statistic evaluation
<a href="#">Trace.Chart</a> .TASK	Display task runtime timechart
<a href="#">Trace.PROfile</a> <b>STATistic</b> .TASK	Display task runtime within fixed time intervals statistically
<a href="#">Trace.PROfile</a> <b>Chart</b> .TASK	Display task runtime within fixed time intervals as colored graph
<a href="#">Trace.FindAll</a> <b>Address</b> <b>TASK.CONFIG(magic)</b>	Display all data access records to the “magic” location
<a href="#">Trace.FindAll</a> <b>CYcle owner OR CYcle context</b>	Display all context ID records

The start of the recording time, when the calculation doesn't know which task is running, is calculated as “(unknown)”.

All kernel activities up to the task switch are added to the calling task. The start of the recording time, when the calculation doesn't know, which task is running, is calculated as “(root)”.



**NOTE:** This feature is *only* available, if your debug environment is able to trace task switches and data accesses (program flow trace is not sufficient). It requires either an on-chip trace logic that is able to generate a data trace, or a software instrumentation feeding one of TRACE32 software based traces (e.g. **FDX** or **Logger**). For details, refer to “**OS-aware Tracing**” (glossary.pdf).

The time different tasks are in a certain state (running, ready, suspended or waiting) can be evaluated statistically or displayed graphically.

This feature requires that the following data accesses are recorded:

- All accesses to the status words of all tasks
- Accesses to the current task variable (= magic address)

Adjust your trace logic to record all data write accesses, or limit the recorded data to the area where all TCBs are located (plus the current task pointer).

**Example:** This script assumes that the TCBs are located in an array named TCB\_array and consequently limits the tracing to data write accesses on the TCBs and the task switch.

```
Break.Set Var.RANGE(TCB_array) /Write /TraceData  
Break.Set TASK.CONFIG(magic) /Write /TraceData
```

To evaluate the contents of the trace buffer, use these commands:

<b>Trace.STATistic.TASKState</b>	Display task state statistic
<b>Trace.Chart.TASKState</b>	Display task state timechart

The start of the recording time, when the calculation doesn't know which task is running, is calculated as “(unknown)”.

All kernel activities up to the task switch are added to the calling task. The start of the recording time, when the calculation doesn't know, which task is running, is calculated as “(root)”.

**NOTE:** This feature is *only* available, if your debug environment is able to trace task switches (program flow trace is not sufficient). It requires either an on-chip trace logic that is able to generate task information (eg. data trace), or a software instrumentation feeding one of TRACE32 software based traces (e.g. [FDX](#) or [Logger](#)). For details, refer to “[OS-aware Tracing](#)” (glossary.pdf).

All function-related statistic and time chart evaluations can be used with task-specific information. The function timings will be calculated dependent on the task that called this function. To do this, in addition to the function entries and exits, the task switches must be recorded.

To do a selective recording on task-related function runtimes based on the data accesses, use the following command:

```
; Enable flow trace and accesses to the magic location  
Break.Set TASK.CONFIG(magic) /TraceData
```

To do a selective recording on task-related function runtimes, based on the Arm Context ID, use the following command:

```
; Enable flow trace with Arm Context ID (e.g. 32bit)  
ETM.ContextID 32
```

To evaluate the contents of the trace buffer, use these commands:

<a href="#">Trace.ListNesting</a>	Display function nesting
<a href="#">Trace.STATistic.Func</a>	Display function runtime statistic
<a href="#">Trace.STATistic.TREE</a>	Display functions as call tree
<a href="#">Trace.STATistic.sYmbol /SplitTASK</a>	Display flat runtime analysis
<a href="#">Trace.Chart.Func</a>	Display function timechart
<a href="#">Trace.Chart.sYmbol /SplitTASK</a>	Display flat runtime timechart

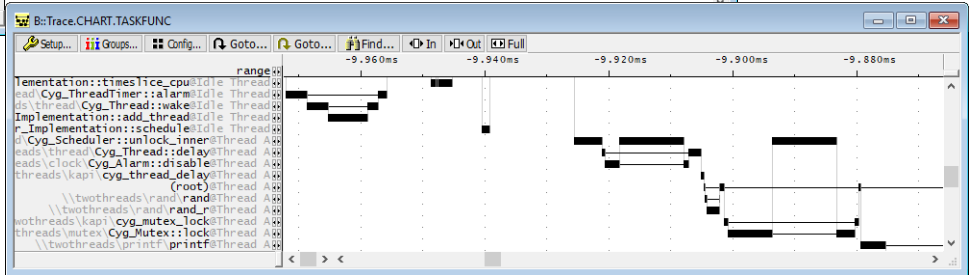
The start of the recording time, when the calculation doesn't know which task is running, is calculated as “(unknown)”.

All kernel activities up to the task switch are added to the calling task.

B:Trace.STATIC.TASKTREE

funcs: 747. total: 13.107ms 118 workarounds

range	tree	total	min	max	avr	count	intern%	l%
(root)@Thread	(root)	3.047ms	-	3.047ms	-	-	-	0.030%
reads\kapi\cyg_mutex_lock@Thread	cyg_mutex_lock	63.000us	21.700us	21.700us	21.700us	3. (1/0)	3. (0/1)	0.020%
\twothreads\printf\printf@Thread	printf	2.798ms	957.200us	975.000us	966.100us	3.	0.	0.099%
ads\kapi\cyg_mutex_unlock@Thread	cyg_mutex_unlock	40.400us	20.200us	20.200us	20.200us	2.	0.	0.016%
ads\kapi\cyg_thread_delay@Thread	cyg_thread_delay	136.600us	68.300us	68.300us	68.300us	2.	0.	0.025%
\thread\Cyg_Thread::delay@Thread	Cyg_Thread::delay	133.200us	66.600us	66.600us	66.600us	2.	0.	0.125%
\twothreads\rand\rand@Thread	rand	4.600us	2.300us	2.300us	2.300us	2.	0.	0.004%
(root)@Thread	(root)	3.252ms	-	3.252ms	-	-	-	0.041%
ads\kapi\cyg_thread_delay@Thread	cyg_thread_delay	204.900us	68.300us	68.300us	68.300us	4. (1/1)	4. (1/1)	0.038%
\twothreads\rand\rand@Thread	rand	6.900us	2.300us	2.300us	2.300us	3.	0.	0.006%
\twothreads\rand\rand@Thread	rand_r	6.000us	2.000us	2.000us	2.000us	3.	0.	0.045%
reads\kapi\cyg_mutex_lock@Thread	cyg_mutex_lock	65.100us	21.700us	21.700us	21.700us	3.	0.	0.025%
\twothreads\printf\printf@Thread	printf	2.909ms	958.900us	975.200us	969.767us	3.	0.	0.102%
ads\kapi\cyg_mutex_unlock@Thread	cyg_mutex_unlock	60.600us	20.200us	20.200us	20.200us	3.	0.	0.025%
(root)@Idle Thread	(root)	6.808ms	-	6.808ms	-	-	-	0.399%
bal\restore_state@0x90@Idle Thread	restore_state@0x90	6.580ms	-	6.580ms	-	1. (1/0)	1. (1/0)	0.177%
hal_idle_thread_action@Idle Thread	hal_idle_thread_action	175.700us	1.300us	91.200us	1.300us	66. (0/1)	66. (0/1)	0.651%



## eCos specific Menu

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The menu file “ecos.men” contains a menu with eCos specific menu items. Load this menu with the **MENU.ReProgram** command.

You will find a new menu called **eCos**.

- The **Display** menu items launch the kernel resource display windows.
- The **Stack Coverage** submenu starts and resets the eCos specific stack coverage and provides an easy way to add or remove tasks from the stack coverage window.

In addition, the menu file (\*.men) modifies these menus on the TRACE32 [main menu bar](#):

- The **Trace** menu is extended. In the **List** submenu, you can choose if you want a trace list window to show only task switches (if any) or task switches together with default display.
- The **Perf** menu contains additional submenus for task runtime statistics, task-related function runtime statistics or statistics on task states.

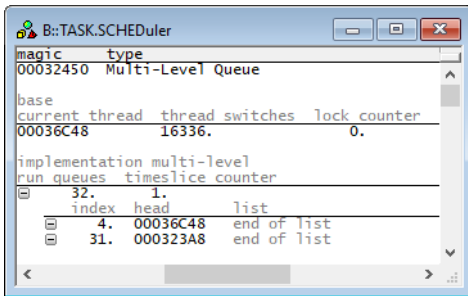
## TASK.SCHEDuler

Display scheduler information

---

Format: **TASK.SCHEDuler**

Displays detailed information about the used scheduler.



```
B::TASK.SCHEDuler
magic      type
00032450  Multi-Level Queue

base
current thread  thread switches  lock counter
00036C48      16336.              0.

implementation multi-level
run queues      timeslice counter
  32.           1.
  index head    list
  4.  00036C48  end of list
  31. 000323A8  end of list
```

“magic” is a unique ID, used by the OS Awareness to identify the scheduler (address of the scheduler object).

The fields “magic” and “head” are mouse sensitive, double clicking on them opens appropriate windows.

## TASK.THRead

Display threads

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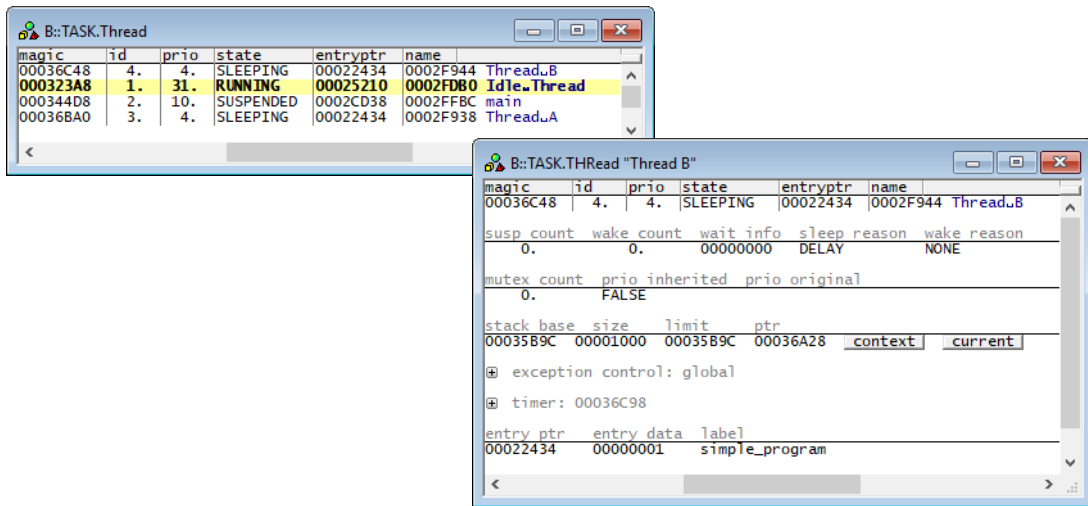
Format: **TASK.THRead** [*<thread>*]

Displays the thread table of eCos or detailed information about one specific thread.

Without any arguments, a table with all created threads will be shown.



Specify a thread name or magic number to display detailed information on that thread.



“magic” is a unique ID, used by the OS Awareness to identify a specific thread (address of the thread object).

The “magic” field and various other fields are mouse sensitive. Double-clicking on them opens appropriate windows. Right clicking on the “magic” will show a local menu.

There are special definitions for eCos specific PRACTICE functions.

## TASK.CONFIG()

## OS Awareness configuration information

---

Syntax: **TASK.CONFIG(magic | magicsize)**

### Parameter and Description:

<b>magic</b>	<b>Parameter Type:</b> <a href="#">String</a> ( <i>without</i> quotation marks). Returns the magic address, which is the location that contains the currently running task (i.e. its <a href="#">task magic number</a> ).
<b>magicsize</b>	<b>Parameter Type:</b> <a href="#">String</a> ( <i>without</i> quotation marks). Returns the size of the task magic number (1, 2 or 4).

**Return Value Type:** [Hex value](#).